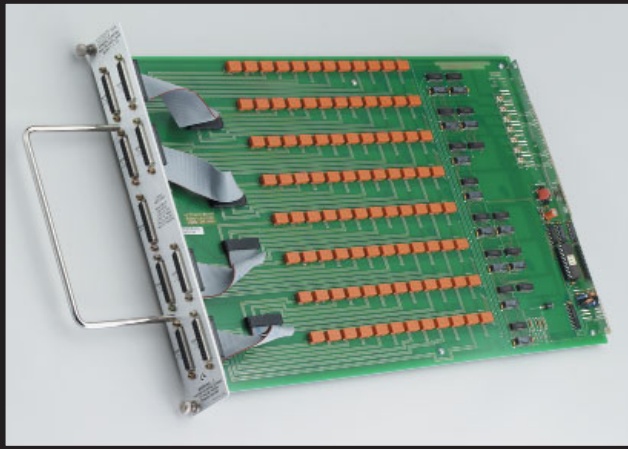


# 7075

## Two-pole Multiplexer Card Eight 1x12



The Model 7075 is a general purpose multiplex switching card that consists of eight banks of independent 112 multiplexer switching. Each bank has two switched circuits (HI and GUARD). The row is connected through jumpers on the card to the general purpose analog backplane in the Model 707A or 708A switching mainframe. This provides the interconnect between cards for multiplexer expansion (124, 136, etc.). Jumpers may be removed to isolate any bank. A single card can be expanded to 196 by reconfiguring the supplied bank-to-bank jumpers. Eight 25-pin D connectors are provided for bank connections and one for row connection.

- Low cost
- <math>5\mu\text{V}</math> voltage offset
- <math>100\text{pA}</math> offset current
- 110V, 1A signal levels
- Uses standard 25-pin D connectors
- Compatible with Models 707A and 708A

### Ordering Information

**7075** Eight 1x12 Two-pole Multiplexer Card

### Accessories Supplied

Jumpers for multiplexer expansion

**MULTIPLEX CONFIGURATION:** Eight 1x12 banks. Adjacent banks can be connected together. Jumpers can be removed to isolate any bank from the backplane.

**CONTACT CONFIGURATION:** 2-pole Form A (HI, GUARD).

**CONNECTOR TYPE:** 25-pin subminiature D connector, eight for bank connection, one for row connection.

**MAXIMUM SIGNAL LEVEL:**

**DC Signals:** 110V DC pin-to-pin, 1A switched, 30VA (resistive load).

**AC Signals:** 175V AC peak pin-to-pin, 1A switched, 60VA (resistive load).

**COMMON MODE VOLTAGE:** 110V DC, 175V AC peak pin-to-pin or pin-to-chassis.

**CONTACT LIFE:**

**Cold Switching:**  $10^8$  closures.

**At Maximum Signal Level:**  $10^5$  closures.

**CHANNEL RESISTANCE (per conductor):** <math>0.50\Omega</math> initial, <math>1.5\Omega</math> at end of contact life.

**CONTACT POTENTIAL:** <math>5\mu\text{V}</math> per contact pair (HI to GUARD).  
**OFFSET CURRENT:** <math>100\text{pA}</math>.

**CROSSTALK (1MHz, 50 $\Omega$  load):** Bank: <math>-60\text{dB}</math>. Channel: <math>-60\text{dB}</math>.

**INSERTION LOSS (1MHz, 50 $\Omega$  source, 50 $\Omega$  load):** 0.1dB typical.

**ISOLATION:**

**Bank:** >10<sup>10</sup> $\Omega$ , <math>3\text{pF}</math>.

**Channel:** >10<sup>10</sup> $\Omega$ , <math>5\text{pF}</math>.

**Differential:** Configured as 1x12: >10<sup>9</sup> $\Omega$ , <math>100\text{pF}</math> nominal. Configured as 1x96: >10<sup>9</sup> $\Omega$ , <math>600\text{pF}</math> nominal.

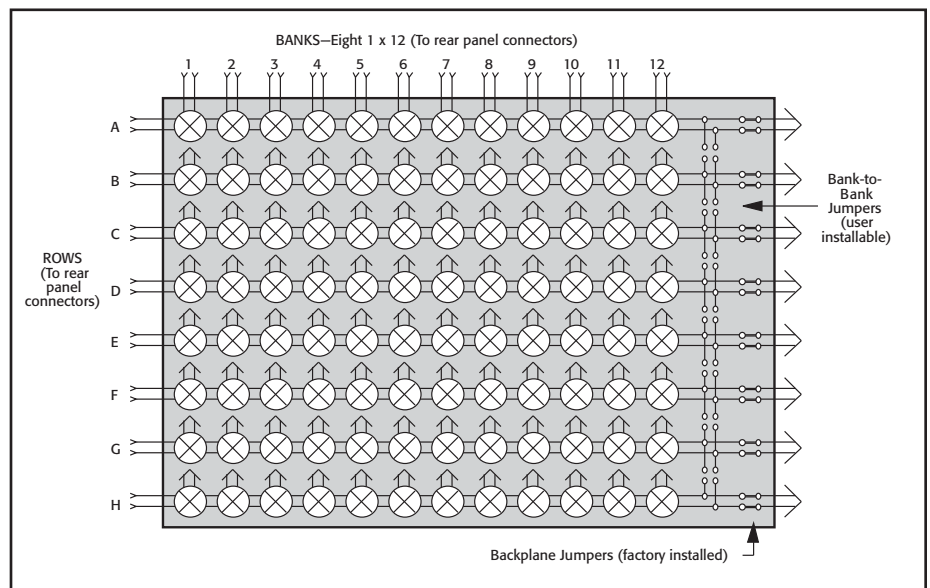
**Common Mode:** Configured as 1x12: >10<sup>9</sup> $\Omega$ , <math>165\text{pF}</math> nominal. Configured as 1x96: >10<sup>9</sup> $\Omega$ , <math>700\text{pF}</math> nominal.

**3dB BANDWIDTH (50 $\Omega$  load):**

Configured as 1x12: 30MHz typical.

Configured as 1x96: 2.5MHz typical.

**RELAY SETTLING TIME:** <math>3\text{ms}</math>.



Use with Models 707A and 708A switching matrix mainframes

SWITCHING AND CONTROL

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