

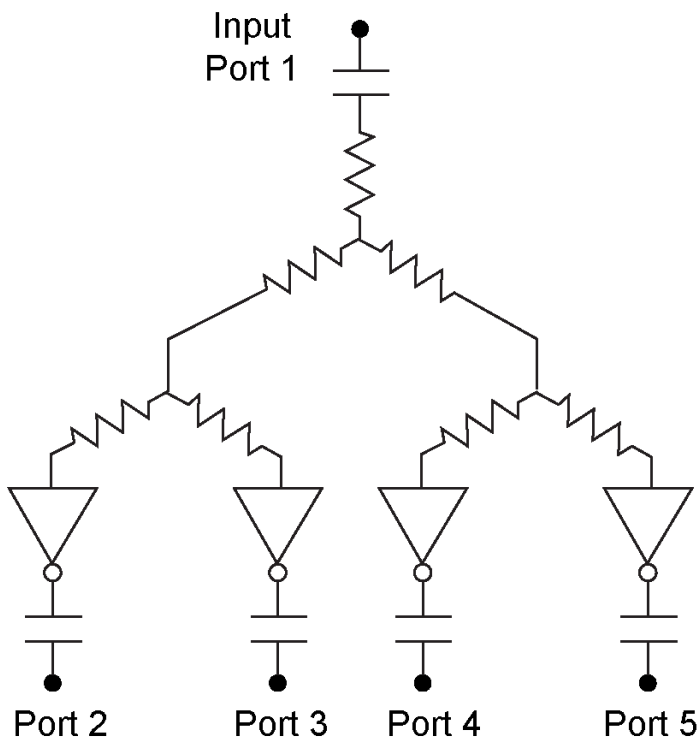
1:4 Amplified 4-Way Splitter

PSPL5335 Datasheet



Key performance specifications

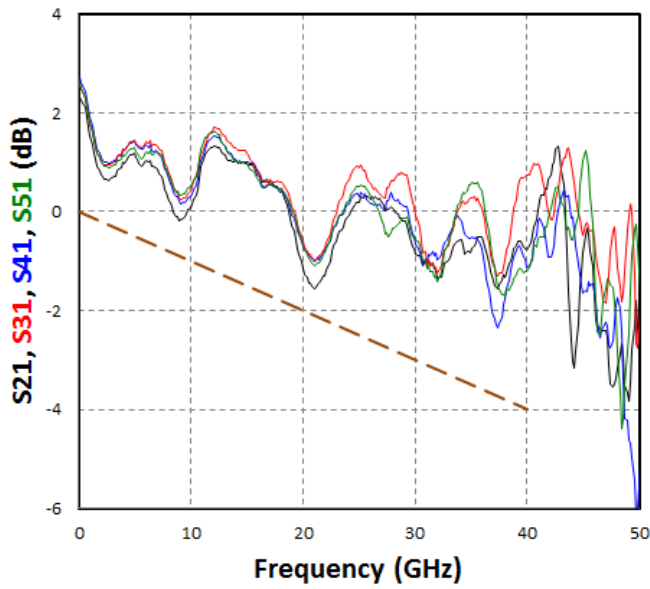
- 0 dB nominal insertion loss
- Equal distribution to four ports
- Single power supply



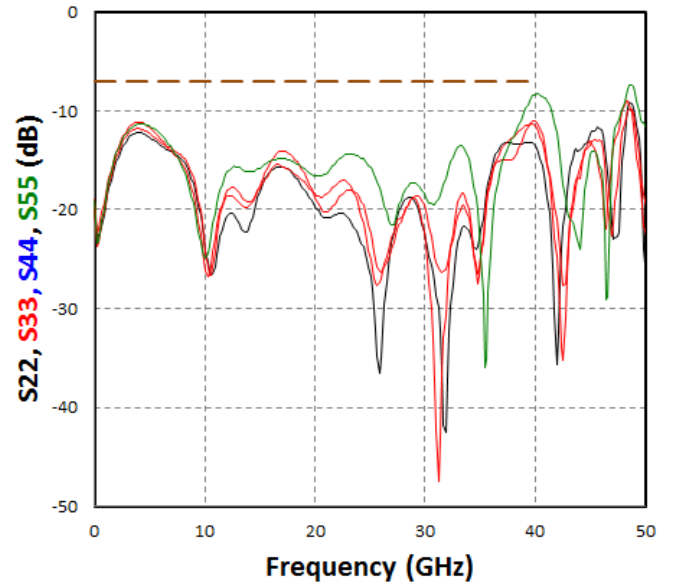
The PSPL5335 Amplified Splitter provides an effective way to distribute a single signal to four ports with no loss in amplitude. The PSPL5335 is not optimized for data transmission, it can be used as a clock splitter or general purpose amplifier. Unused ports can be left unterminated.

Typical performance

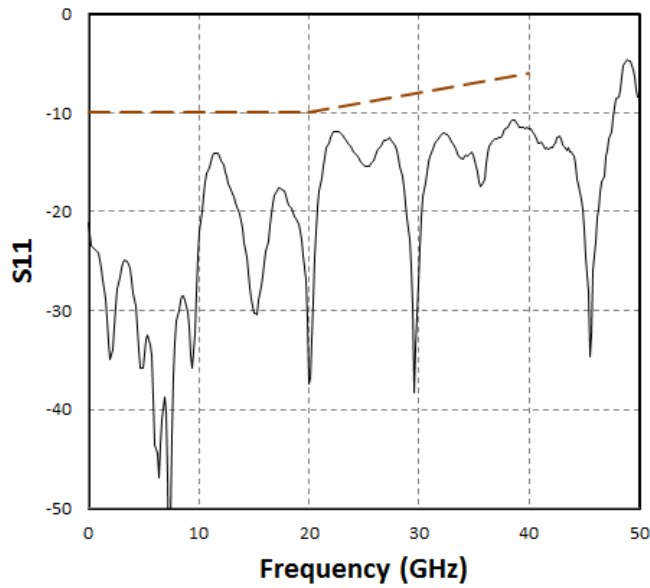
Insertion Loss



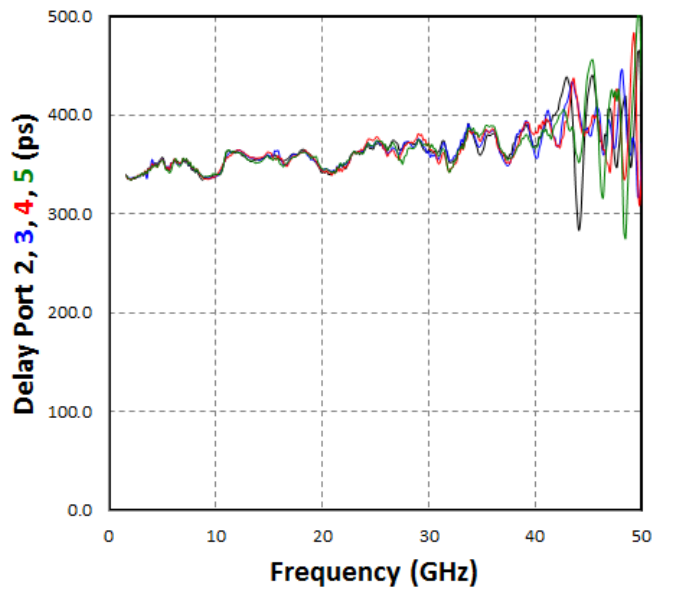
Output Return Loss



Input Return Loss



Group Delay



Specifications

Electrical specifications

Parameter	Performance
Polarity	Inverting
Input / output coupling	AC
Input / output impedance	50 Ω
Bandwidth (-3 dB)	100 MHz to 35 GHz ¹
Insertion loss	0 dB
Input return loss 40 MHz < f < 20 GHz 20 GHz < f < 40 GHz	-10 dB ¹ -6 dB ¹
Output return loss 40 MHz < f < 40 GHz	-7 dB ¹
Group delay	350 ps
Output power at 1 dB gain compression	+4 dBm
Warranty	One year

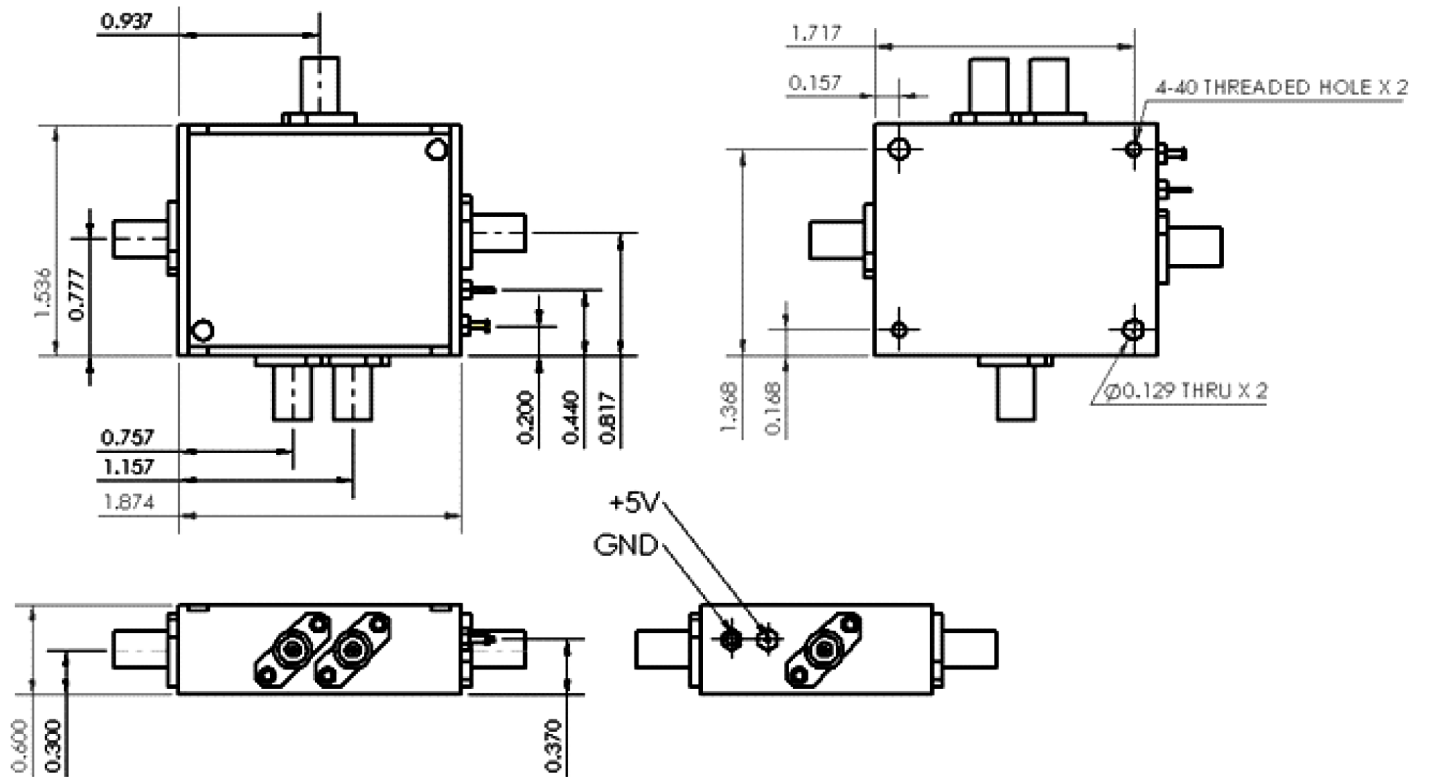
Operating specifications

Parameter	Units	Minimum	Typical	Maximum	Comments
DC supply voltage	V _{DC}	4.75		5.5	
DC supply current	I _{DC}		400	600	Damage threshold ²
Maximum allowed input	V _{amp}			2	Damage threshold
Power dissipation	W		2	3.3	
Operating temperature	°C		25		

¹ Tested in production at 25 °C.

² Tektronix recommends setting a current limit on the DC supply. Certain operating conditions (such as driving with low duty cycle negative-going pulses) can increase the supply current and cause the device to overheat.

Mechanical dimensions



Ordering information

Models

PSPL5335

Port 1 (input), 2.92 mm jack (f), Ports 2 - 5 (outputs), 2.92 mm jack (f)

ASEAN / Australasia (65) 6356 3900
Belgium 00800 2255 4835*
Central East Europe and the Baltics +41 52 675 3777
Finland +41 52 675 3777
Hong Kong 400 820 5835
Japan 81 (3) 6714 3010
Middle East, Asia, and North Africa +41 52 675 3777
People's Republic of China 400 820 5835
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Taiwan 886 (2) 2656 6688

Austria 00800 2255 4835*
Brazil +55 (11) 3759 7627
Central Europe & Greece +41 52 675 3777
France 00800 2255 4835*
India 000 800 650 1835
Luxembourg +41 52 675 3777
The Netherlands 00800 2255 4835*
Poland +41 52 675 3777
Russia & CIS +7 (495) 6647564
Sweden 00800 2255 4835*
United Kingdom & Ireland 00800 2255 4835*

Balkans, Israel, South Africa and other ISE Countries +41 52 675 3777
Canada 1 800 833 9200
Denmark +45 80 88 1401
Germany 00800 2255 4835*
Italy 00800 2255 4835*
Mexico, Central/South America & Caribbean 52 (55) 56 04 50 90
Norway 800 16098
Portugal 80 08 12370
South Africa +41 52 675 3777
Switzerland 00800 2255 4835*
USA 1 800 833 9200

* European toll-free number. If not accessible, call: +41 52 675 3777

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