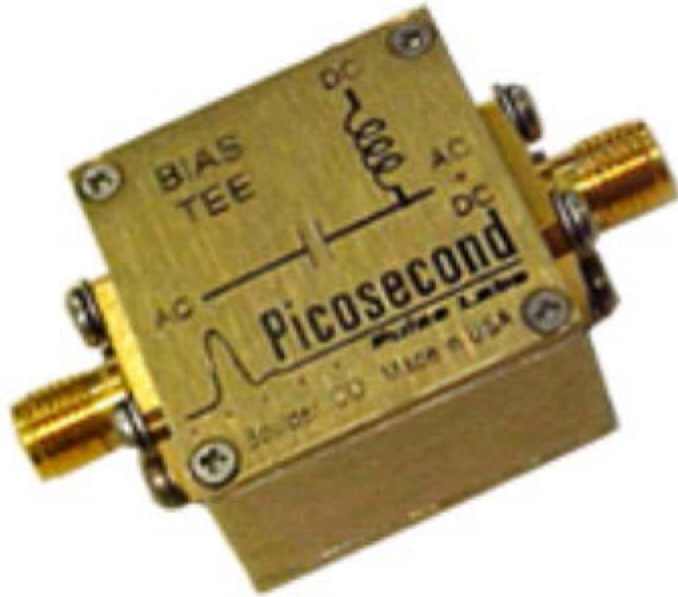


# 18 GHz High Current Bias Tee

## PSPL5585 Datasheet

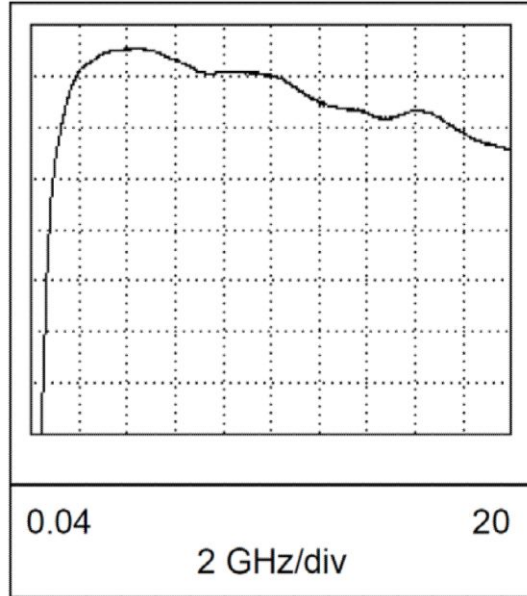


The PSPL5585 High Current Bias Tee is intended for frequency domain, high current applications. Unlike most high current bias tees that typically are specified for only an octave band, this bias tee can typically cover a decade. The DC input is a solder terminal feed-thru capacitor.

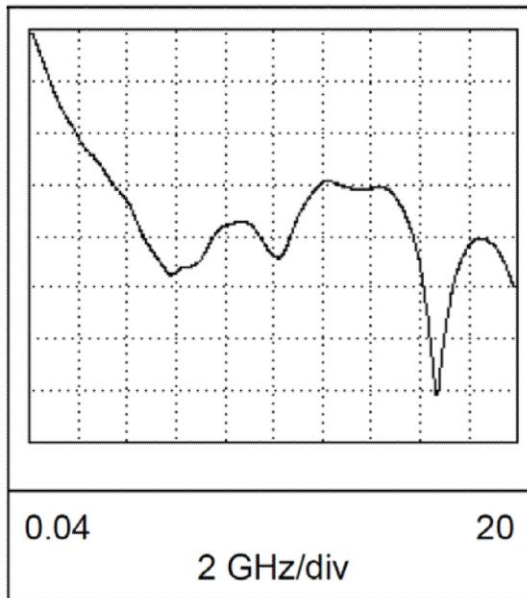
### Key performance specifications

- 2 GHz to 18 GHz
- 100 V, 6 A

### Typical performance



Insertion Loss, S21. 0.5 dB/div. top line is the 0 dB reference.

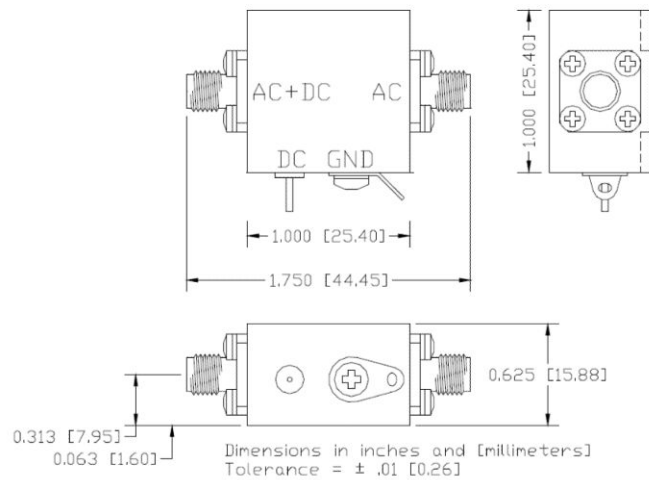


Return Loss, S11. 0.5 dB/div. top line is the 0 dB reference.

# Specifications

Parameter	Symbol	Units	Minimum	Typical	Maximum	Comments
Impedance	Z	ohms		50		
Frequency range	f	GHz		2 to 18 4 to 10		IL < 1 dB, RL > 10 dB IL < 0.5 dB, RL > 15 dB
Insertion loss	S <sub>21</sub>	dB		0.4		At 5 GHz
Input (AC) Return loss	S <sub>11</sub>	dB		20		Mid band, AC port
DV voltage	V	V			100	
DC current	I	A			6	
Capacitance (bypass)	C	nF		2		
Capacitance	C	nF		1		
Inductance	L	nH		6		
Resistance	R	ohms		0.01		
RF power	P	W			20	Average power
Isolation	S <sub>13</sub>	dB		>40		
DC path bandwidth	f <sub>c,DC</sub>	MHz		2		
Temperature	T	°C	-55		+125	Storage and operating
RF connectors	SMA jacks (f)					
DC connector	Solder pin					
Warranty	One year					

## Mechanical dimensions



## Ordering information

### Models

PSPL5585

BIAS TEE, 2-18 GHz, 100 V, 6 A

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\* European toll-free number. If not accessible, call: +41 52 675 3777

Updated 10 April 2013

**For Further Information.** Tektronix maintains a comprehensive, constantly expanding collection of application notes, technical briefs and other resources to help engineers working on the cutting edge of technology. Please visit [www.tektronix.com](http://www.tektronix.com).

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