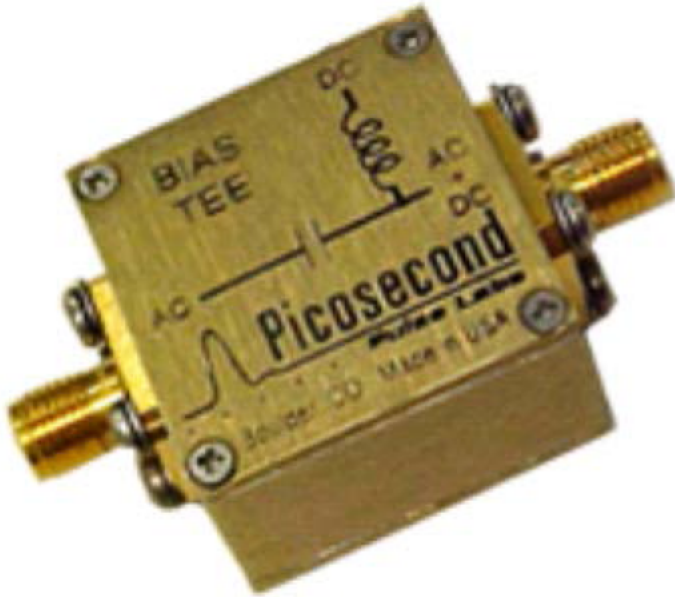


2 GHz High Current Bias Tee

PSPL5587 Datasheet

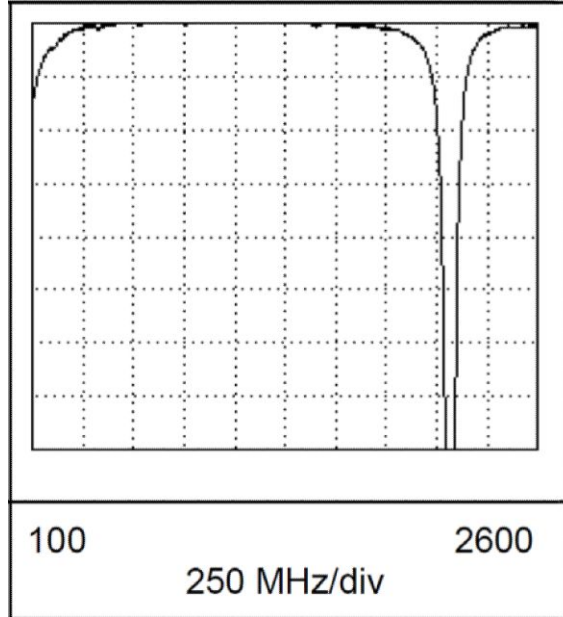


The PSPL5587 High Current Bias Tee is intended for frequency domain, high current applications. Unlike most high current bias tees that typically are specified for only an octave band, this bias tee can typically cover a decade. The DC input is a solder terminal feed-thru capacitor.

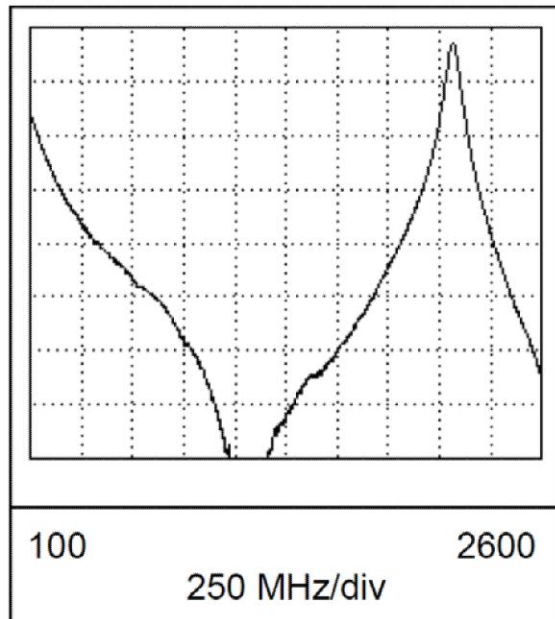
Key performance specifications

- 0.2 GHz to 2 GHz
- 100 V, 6 A

Typical performance



Insertion Loss, S21. 0.5 dB/div, top line is the 0 dB reference.

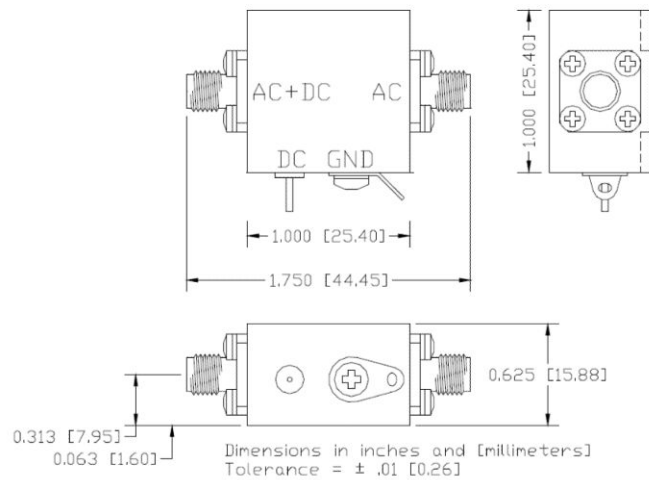


Return Loss, S11. 5 dB/div, top line is the 0 dB reference.

Specifications

Parameter	Symbol	Units	Minimum	Typical	Maximum	Comments
Impedance	Z	ohms		50		
Frequency range	f	GHz		0.2 to 2 0.3 to 1.8		IL < 1 dB, RL > 10 dB IL < 0.5 dB, RL > 15 dB
Insertion loss	S ₂₁	dB		0.05		At 1 GHz
Input (AC), Return loss	S ₁₁	dB		>30		Mid band, AC port
DC voltage	V	V			100	
DC current	I	A			6	
Capacitance (bypass)	C	nF		22		
Capacitance	C	nF		1		
Inductance	L	nH		88		
Resistance	R	ohms		0.2		
RF power	P	W			100	Average power
Isolation	S ₁₃	dB		>50		
DC path bandwidth	f _{c,DC}	kHz		275		
Temperature	T	Deg C	-55		+125	Storage and operating
RF connectors	SMA jacks (f)					
DC connector	Solder pin					
Warranty	One year					

Mechanical dimensions



Ordering information

Models

PSPL5587

BIAS TEE, 0.2-2 GHz, 100 V, 6 A

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