

KEITHLEY

Low Level Measurements Handbook

Precision DC Current, Voltage,
and Resistance Measurements

EIGHTH EDITION

Symbol	Prefix	Exponent
y	yocto-	10^{-24}
z		10^{-21}
a		10^{-18}
f		10^{-15}
(no)		10^{-12}
k		10^3
M		10^6
G		10^9
T		10^{12}
P		10^{15}
E		10^{18}
Z		10^{21}
	tera-	
	peta-	
	exa-	
	zetta-	

femto-

pico-

nano-

micro-

milli-

tera-

peta-

exa-

zetta-

10^{-24}

10^{-21}

10^{-18}

10^{-15}

10^{-12}

10^3

10^6

10^9

10^{12}

10^{15}

10^{18}

10^{21}

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Foreword

The first edition of this handbook, published in 1972 as *Electrometer Measurements*, set out to explain both the theory and practice behind some of the most challenging low-level DC measurements. At the time, a silicon processor contained about 2,500 transistors. Pong had just appeared in arcades. Email and word processing were still emerging technologies available only to a handful of organizations.

Much has changed in the decades since. Today, advanced semiconductor devices contain hundreds of billions of transistors. We carry extraordinary computing power in our pockets and rely on technologies that would have seemed unimaginable in 1972. The world's most advanced innovations depend on measuring electrical signals that are smaller, faster, and more complex than ever before.

Keithley has evolved alongside those technologies. What began as *Electrometer Measurements*—the “pink book” familiar to generations of engineers—became the *Low Level Measurements Handbook*, recognized by its signature red color. Our instruments have become faster, more accurate, and more versatile. Along the way, we introduced innovations of our own, from the first standalone source measure unit (SMU) to Test Script Processor, helping engineers automate and accelerate discovery. As part of Tektronix, we continue to provide the precision DC and AC measurements needed to advance science and technology.

One thing has remained constant: our commitment to good measurements.

This handbook captures decades of Keithley expertise in instrument design and sensitive measurement techniques, built from close collaboration with engineers and scientists around the world. Every breakthrough begins with a measurement someone trusts. Pushing the boundaries of what we can measure today is how we discover what is possible tomorrow.

Here's to the next evolution.

Contributors to This Edition

Mary Anne Tupta is a Distinguished Applications Engineer with over 40 years of DC measurement expertise. She holds a Master of Science in Physics and a Bachelor of Science in Engineering Physics from John Carroll University (Cleveland, Ohio). Mary Anne has contributed to nearly every Keithley product family, from DMMs to SMUs to Parameter Analyzers. Her passion for customer measurement success has led her to prolific publication in the areas of C-V and sensitive DC measurements and applications such as nanotechnology, semiconductor devices, Hall effect, solar cells, material resistivity and much more. This is her fourth time contributing to the Low Level Measurements Handbook, previously contributing to the 5th, 6th and 7th editions.

Elizabeth Makley is an Applications Engineering Manager with nearly 10 years of DC measurement expertise. She holds a Bachelor of Science in Electrical Engineering and a Bachelor of Science in Computer Science and Engineering from the University of Toledo (Toledo, Ohio). Liz grew her career from internship to Application Engineer and has supported most of Keithley's measurement instrumentation. She enjoys teaching and training on proper measurement techniques, and has researched applications involving battery cells, optical devices, temperature measurement and instrument automation. This is her first time contributing to the Low Level Measurements Handbook.

Additional Special Thanks to: Andrew Shetler, Applications Engineer; Christopher Miller, Principal Engineer

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SECTION 1

**Low Level DC
Measuring
Instruments**

FIGURE 1-1: Standard Symbols Used in this Text

Prefixes		
Symbol	Prefix	Exponent
y	yocto-	10^{-24}
z	zepto-	10^{-21}
a	atto-	10^{-18}
f	femto-	10^{-15}
p	pico-	10^{-12}
n	nano-	10^{-9}
μ	micro-	10^{-6}
m	milli-	10^{-3}
(none)	(none)	10^0
k	kilo-	10^3
M	mega-	10^6
G	giga-	10^9
T	tera-	10^{12}
P	peta-	10^{15}
E	exa-	10^{18}
Z	zetta-	10^{21}
Y	yotta-	10^{24}

Quantities		
Symbol	Unit	Quantity
V	volts	EMF
A	amperes	current
Ω	ohms	resistance
C	coulombs	charge
s	seconds	time
W	watts	power
F	farads	capacitance
Hz	cycles/s	frequency
K	Kelvin	temperature

1.1 Introduction

DC voltage, DC current, and resistance are measured most often with digital multimeters (DMMs). Generally, these instruments are adequate for measurements at signal levels greater than $1\ \mu\text{V}$ or $1\ \mu\text{A}$, or less than $1\ \text{G}\Omega$. (See **Figure 1-1** for standard symbols used in this text.) However, they don't approach the theoretical limits of sensitivity. For low level signals, more sensitive instruments such as electrometers, picoammeters, and nano-voltmeters must be used.

Section 1 offers an overview of the theoretical limits of DC measurements and the instruments used to make them. It includes instrument descriptions and basic instrument circuit designs. For easier reference, this information is organized into a number of subsections:

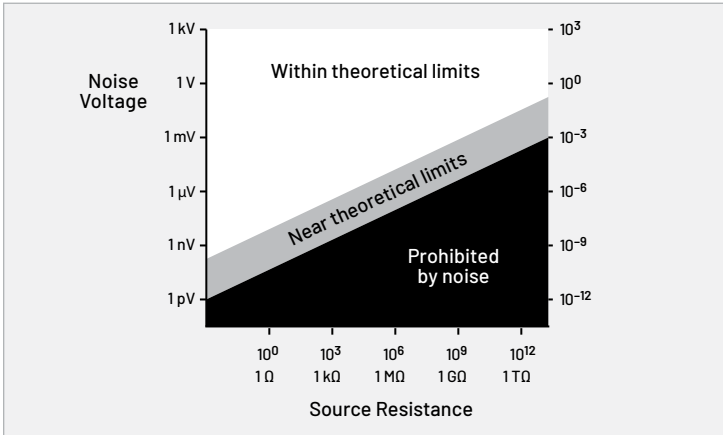
- 1.2 Theoretical Measurement Limits: A discussion of both the theoretical measurement limitations and instrument limitations for low level measurements.
- 1.3 Instrument Definitions: Descriptions of electrometers, DMMs, nano-voltmeters, picoammeters, source measure units (SMUs), low current preamps, and micro-ohmmeters.
- 1.4 Understanding Instrument Specifications: A review of the terminology used in instrument specifications, such as accuracy (resolution, sensitivity, transfer stability), deratings (temperature coefficient, time drift), noise (NMRR and CMRR), and speed.
- 1.5 Circuit Design Basics: Describes basic circuit design for voltmeter circuits, ammeter circuits, coulombmeter circuit, high resistance ohmmeter circuits, low resistance ohmmeter circuits, and complete instrument block diagrams.

1.2 Theoretical Measurement Limits

The theoretical limit of sensitivity in any measurement is determined by the noise generated by the resistances present in the circuit. As discussed in Sections 2.6.5 and 3.2.6, voltage noise is proportional to the square root of the resistance, bandwidth, and absolute temperature. **Figure 1-2** shows theoretical voltage measurement limits at room temperature (300 K) with a response time of 0.1 second to ten seconds. Note that high source resistance limits the theoretical sensitivity of the voltage measurement. Although it's certainly possible to measure a $1\ \mu\text{V}$ signal that has a $1\ \Omega$ source resistance, it's not possible to measure that same $1\ \mu\text{V}$ signal level from a $1\ \text{T}\Omega$ source. Even with a much lower $1\ \text{M}\Omega$ source resistance, a $1\ \mu\text{V}$ measurement is near theoretical limits, so it would be very difficult to make using an ordinary DMM.

In addition to having insufficient voltage or current sensitivity (most DMMs are no more sensitive than $1\ \mu\text{V}$ or $1\ \text{nA}$ per digit), DMMs have high input

FIGURE 1-2: Theoretical Limits of Voltage Measurements



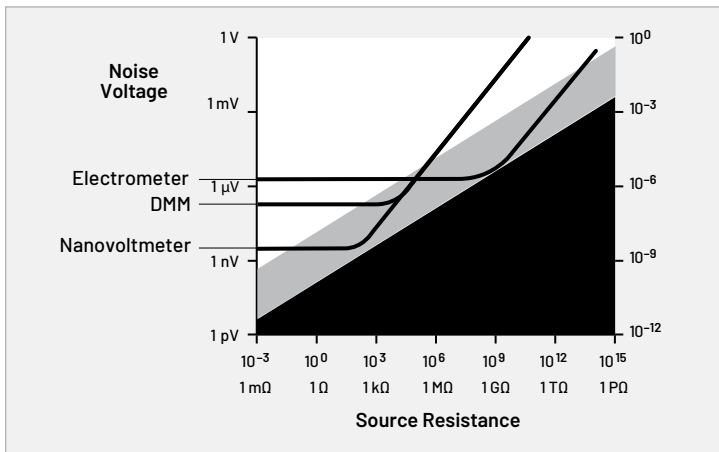
bias current¹ when measuring voltage and lower input resistance compared to more sensitive instruments intended for low level DC measurements. These characteristics cause errors in the measurement; refer to Sections 2 and 3 for further discussion of them.

Given these DMM characteristics, it's not possible to use a DMM to measure signals at levels close to theoretical measurement limits, as shown in **Figure 1-3**. However, if the source resistance is 1 MΩ or less, or if the desired resolution is no better than 0.1 μV (with low source resistance), the signal level isn't "near theoretical limits," a DMM is adequate. If better voltage sensitivity is desired, and the source resistance is low (as it must be because of theoretical limitations), a nanovoltmeter provides a means of measuring at levels much closer to the theoretical limits of measurement. With very high source resistance values (for example, 1 TΩ), a DMM isn't a suitable voltmeter. DMM input resistance ranges from 10 MΩ to 10 GΩ—several orders of magnitude less than a 1 TΩ source resistance, resulting in severe input loading errors. Also, input currents are typically many picoamps, creating large voltage offsets. However, because of its much higher input resistance, an electrometer or high impedance SMU can make voltage measurements at levels that approach theoretical limits. A similar situation exists for low level current measurements; DMMs generally have

¹ Input current flows in the input lead of an active device or instrument. With voltage measurements, the *input current* is ideally zero; thus, any input current represents an error. With current measurements, the *signal current* becomes the *input current* of the measuring instrument. However, some background current is always present when no signal current is applied to the instrument input. This unwanted current is the *input bias current* (often called just the *offset current*) of the instrument.

The source and test connections can also generate unwanted *offset currents* and *offset voltages*. A *leakage current* is another unwanted error current resulting from voltage across an undesired resistance path (called *leakage resistance*). This current, combined with the *offset current*, is the total error current.

FIGURE 1-3: Typical Digital Multimeter (DMM), Nanovoltmeter, and Electrometer Limits of Measurement at Various Source Resistances



a high input voltage drop (input burden), which affects low level current measurements, and DMM resolution is generally no better than 1 nA. Thus, an electrometer or picoammeter with its much lower input burden and better sensitivity will operate at levels much closer to the theoretical (and practical) limits of low current measurements.

1.3 Instrument Definitions

A number of different types of instruments are available to make DC measurements, including electrometers, DMMs, nanovoltmeters, picoammeters, source measure units (SMUs), low current preamps, micro-ohmmeters, and low current sources. The following paragraphs discuss and compare the important characteristics of these instruments.

1.3.1 The Electrometer

An electrometer is a highly refined DC multimeter. As such, it can be used for many measurements performed by a conventional DC multimeter. Additionally, an electrometer's special input characteristics and high sensitivity allow it to make voltage, current, resistance, and charge measurements far beyond the capabilities of a conventional DMM.

An electrometer must be used when any of the following conditions exist:

1. The task requires an extended measurement range unavailable with conventional instruments, such as for detecting or measuring:
 - Currents less than 10 nA (10^{-8} A).
 - Resistances greater than 1 G Ω (10^9 Ω).

2. Circuit loading must be minimized, such as when:
 - Measuring voltage from a source resistance of 100 M Ω or higher.
 - Measuring current when input voltage drop (burden) of less than a few hundred millivolts is required (when measuring currents from sources of a few volts or less).
3. Charge measurement is required.
4. Measuring signals at or near Johnson noise limitations (as indicated in **Figure 1-2**).

Electrometers have four functions: voltmeter, ammeter, ohmmeter, and coulombmeter.

Voltmeter Function

The input resistance of an electrometer voltmeter is extremely high, typically greater than 100 T Ω (10^{14} Ω). Furthermore, the input bias current is less than 3 fA (3×10^{-15} A). These characteristics describe a device that can measure voltage with a very small amount of circuit loading.

Because of the high input resistance and low input bias current, the electrometer voltmeter has minimal effect on the circuit being measured. As a result, the electrometer can be used to measure voltage in situations where an ordinary multimeter would be unusable. For example, the electrometer can measure the voltage on a 500 pF capacitor without significantly discharging the device; it can also measure the potential of piezoelectric crystals and high impedance pH electrodes. Electrometer voltmeters are also used to measure the resistivity of high resistance semiconductor materials.

Ammeter Function

As an ammeter, the electrometer is capable of measuring extremely low currents, limited only by theoretical limits or by the instrument's input bias current. It also has a much lower voltage burden than conventional DMMs.

With its extremely low input bias current and minimal input voltage burden, it can detect currents as low as 1 fA (10^{-15} A). Because of this high sensitivity, it's suitable for measuring ion beam current, dark current of photodiodes, gate leakage current, and current output of mass spectrometers and other devices.

Ohmmeter Function

An electrometer may measure resistance by using either a constant-current or a constant-voltage method. If using the constant-current method, the electrometer's high input resistance and low offset current enables measurements up to 200 G Ω . When using the constant-voltage method, the electrometer applies a constant voltage to the unknown resistance, measures the current, and then calculates the resistance. This is the preferred method because it allows the unknown resistor to be tested at a known voltage. An electrometer can measure resistances up to 10 P Ω (10^{16} Ω) using

this method. The constant-voltage method is used to measure the resistivity of insulators and the insulation resistance of devices such as cables, connectors, and printed circuit boards.

Coulombmeter Function

Current integration and measurement of charge are electrometer coulombmeter capabilities not found in multimeters. The electrometer coulombmeter can detect charge as low as 10 fC (10^{-14} C). It's equivalent to an active integrator and, therefore, has low voltage burden, typically less than 100 μ V.

The coulombmeter function can measure lower currents than the ammeter function can because no noise is contributed by internal resistors. Currents as low as 1 fA (10^{-15} A) may be detected using this function. See Section 2.3.8 for further details. The coulombmeter function can be used with a Faraday cage to measure charge on objects or can be used to measure capacitance using a DC voltage.

1.3.2 The DMM

Digital multimeters vary widely in performance, from low cost handheld $3\frac{1}{2}$ -digit units to $8\frac{1}{2}$ -digit high precision system DMMs. Although there are many models available from a wide variety of manufacturers, none approaches the theoretical limits of measurement discussed previously. These limitations don't imply that DMMs are inadequate instruments; they simply point out the fact that the vast majority of measurements are made at levels far from theoretical limits, and DMMs are designed to meet these more conventional measurement needs.

Although low level measurements are by definition those that are close to theoretical limits, and are thus outside the range of DMMs, advances in technology are narrowing the gap between DMMs and dedicated low level instruments. For example, the most sensitive DMMs can detect DC voltages as low as 10 nV, resolve DC currents down to 10 pA, and measure resistances as low as 100 $\mu\Omega$ and as high as 1 G Ω . Although these characteristics still fall far short of the corresponding capabilities of more sensitive instruments like the electrometer described previously, all the measurement theory and accuracy considerations in this book apply to DMM measurements, as well as to nanovoltmeter, picoammeter, electrometer, or SMU measurements. The difference is only a matter of degree; when making measurements close to theoretical limits, all measurement considerations are vitally important. When measuring at levels far from theoretical limits, only a few basic considerations (accuracy, loading, etc.) are generally of concern.

1.3.3 The Nanovoltmeter

A nanovoltmeter is a very sensitive voltage meter. As shown in **Figure 1-3**, this type of instrument is optimized to provide voltage measurements near the theoretical limits from low source resistances, in contrast to the electrometer, which is optimized for use with high source resistances.

Compared to an electrometer, the voltage noise and drift are much lower, and the current noise and drift are much higher. Input resistance is usually similar to that of a DMM and is much lower than that of an electrometer.

As is the case with electrometers, nanovoltmeters are just as easy to operate as DMMs. Their distinguishing characteristic is their voltage sensitivity, which can be as good as 1nV down to a few picovolts. Most nanovoltmeters aren't multifunction instruments and are correspondingly less complex than electrometers.

1.3.4 The Picoammeter

A picoammeter is an ammeter built along the lines of the ammeter function of an electrometer. When compared with an electrometer, a picoammeter has a similar low voltage burden, similar or faster speed, less sensitivity, and a lower price. It may also have special characteristics, such as high speed logarithmic response or a built-in voltage source.

1.3.5 The Source Measure Unit

The source measure unit (SMU) combines measuring and sourcing capabilities and can provide four basic functions: measure voltage, measure current, source voltage, and source current. These functions can be used separately or together in the following combinations: simultaneously source voltage and measure current or simultaneously source current and measure voltage. The addition of four-quadrant sourcing functions makes these instruments more convenient and versatile than using separate instruments for applications such as generating I-V curves of semiconductors and other types of devices.

Some SMUs may have a number of electrometer-like characteristics that make them suitable for low level measurements. For example, they may provide very high input resistance (typically 100 T Ω or more), minimizing circuit loading when making voltage measurements from high impedance sources. Their current measurement sensitivity is also similar to that of the electrometer picoammeter—typically as low as 10 fA. Instruments with triax connectors also allow making extremely low noise measurements; banana jacks are usually available for more general-purpose or high throughput production test measurements.

Sweep capabilities are another important advantage that many of these instruments offer. Either voltage or current can be swept across the desired range at specified increments, and the resulting current or voltage can be measured at each step. Built-in source-delay-measure cycles allow optimizing measurement speed while ensuring sufficient circuit settling time to maintain measurement integrity.

SMUs can display resistance measurements directly, as well as voltage and current. Unlike a digital multimeter (DMM), which can make a measurement at only one point, an SMU can generate a family of I-V curves, because it has a built-in source. This is especially useful when studying semicon-

ductor devices and making materials measurements. When used as a current source, an SMU can be used in conjunction with a nanovoltmeter to measure very low resistances by automatically reversing the polarity of the source to correct for offsets.

1.3.6 The Low Current Preamp

Some SMUs may have a remote low current preamp. With this design, the sensitive amplifier circuitry is separate from the SMU. This makes it possible to place the most sensitive part of the instrument very close to the device being tested, thereby eliminating a major source of error, the noise and leakage from the cables themselves.

1.3.7 The Micro-ohmmeter

A micro-ohmmeter is a special type of ohmmeter designed especially for making low level resistance measurements. The typical micro-ohmmeter can resolve resistances as low as 10 $\mu\Omega$. Micro-ohmmeters are optimized for measuring low resistance, but many of the features used in micro-ohmmeters, such as using the four-wire technique, offset compensation, and dry circuit testing, can be found in some DMMs.

Measurements made using the micro-ohmmeter are always performed using the four-wire technique in order to minimize errors caused by test leads and connections. The typical micro-ohmmeter also has additional features such as offset compensation and dry circuit testing to optimize low resistance measurements. Offset compensation is performed by pulsing the test current to cancel offsets from thermoelectric EMFs. The dry circuit test mode limits the voltage across the unknown resistance to a very small value (typically <20 mV) to avoid puncturing oxides when testing such devices as relay contacts, connectors, and switches.

1.3.8 The Low Current Source

Low current sourcing is important in a wide range of applications from R&D to production, including semiconductor, electrochemistry, nanotechnology, and superconductor tests. Low current sources are designed to output a specified current through the DUT and apply whatever voltage is required to force that current.

Key considerations for using a low current source include the maximum current, minimum current and step resolution, output settling time required, and the level of voltage compliance needed. It must also have a level of noise acceptable for the application and sufficiently high output impedance. If the source's output impedance is only 100 $\times$ higher than the resistance of the DUT, then the actual current through the DUT will be off by 1%.

Although it's possible to create a current source from a voltage source and series resistor, such "homemade" solutions have several drawbacks, including no voltage compliance and unpredictable output.

1.4 Understanding Instrument Specifications

Knowing how to interpret instrument specifications properly is an important aspect of making good low level measurements. Although instrument accuracy is probably the most important of these specifications, there are several other factors to consider when reviewing specifications, including noise, deratings, and speed.

1.4.1 Definition of Accuracy Terms

This section defines a number of terms related to instrument accuracy. Some of these terms are further discussed in subsequent paragraphs. **Table 1-1** summarizes conversion factors for various unit specifications associated with instruments.

TABLE 1-1: Specification Conversion Factors

Percent	PPM	Digits	Bits	dB	Portion of 10 V	Number of time constants to settle to rated accuracy
10%	100000	1	3.3	-20	1 V	2.3
1%	10000	2	6.6	-40	100 mV	4.6
0.1%	1000	3	10	-60	10 mV	6.9
0.01%	100	4	13.3	-80	1 mV	9.2
0.001%	10	5	16.6	-100	100 μ V	11.5
0.0001%	1	6	19.9	-120	10 μ V	13.8
0.00001%	0.1	7	23.3	-140	1 μ V	16.1
0.000001%	0.01	8	26.6	-160	100 nV	18.4
0.0000001%	0.001	9	29.9	-180	10 nV	20.7

SENSITIVITY - the smallest *change* in the signal that can be detected.

RESOLUTION - the smallest *portion* of the signal that can be observed.

REPEATABILITY - the closeness of agreement between *successive* measurements carried out under the same conditions.

REPRODUCIBILITY - the closeness of agreement between measurements of the same quantity carried out with a stated *change in conditions*.

ABSOLUTE ACCURACY - the closeness of agreement between the result of a measurement and its true value or accepted *standard value*, taking into account the uncertainty of the standard value.

RELATIVE ACCURACY - the closeness of agreement between the result of a measurement and its true value or accepted *standard value* but without taking the uncertainty of the standard value into account.

ERROR - the *deviation* (difference or ratio) of a measurement *from its true value*. Note that true values are by their nature indeterminate.

RANDOM ERROR – the *mean* of a large number of measurements influenced by random error *matches the true value*.

SYSTEMATIC ERROR – the *mean* of a large number of measurements influenced by systematic error *deviates from the true value*.

UNCERTAINTY – an estimate of the bounded interval around the measured value, which is likely to contain the true value.

“Precision” is a more qualitative term than many of those defined here. It is often applied in the context of repeatability or reproducibility, but it shouldn’t be used in place of “accuracy.”

1.4.2 Accuracy

One of the most important considerations in any measurement situation is reading accuracy. For any given test setup, a number of factors can affect accuracy. The most important factor is the accuracy of the instrument itself, which may be specified in several ways, including a percentage of full scale, a percentage of reading, or a combination of both. Instrument accuracy aspects are covered in the following paragraphs.

Other factors such as input loading, leakage resistance, electrostatic interference, and thermoelectric EMFs may also have a serious impact on overall accuracy. These and many other important measurement considerations are discussed in detail in Sections 2 and 3.

Measurement Instrument Specifications

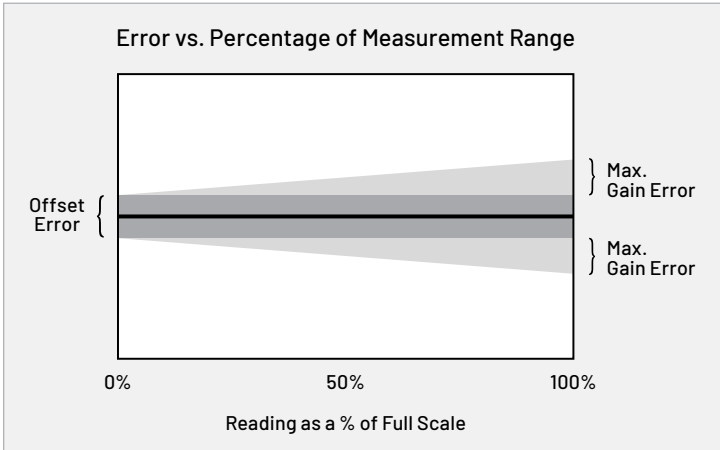
A typical expression of accuracy is $\pm(\text{gain error} + \text{offset error})$. However, manufacturers can use different formats to express accuracy, so it can be difficult to compare instruments. Gain and offset errors are sometimes combined into a specification that expresses accuracy in terms of a percentage and counts or parts per million.

The graph in **Figure 1-4** shows the relationship of the offset and gain errors over a measurement range. Note that the offset error is constant over a particular range. When the reading is at a lower percentage of full scale of range, the offset error is the dominant term. When a reading is taken near the full-scale value of a selected range, the gain error becomes significant.

Instrument accuracy is often specified as a percentage of reading, plus a percentage of range (or a number of counts of the least significant digit). For example, a typical DMM accuracy specification may be stated as: $\pm(0.005\% \text{ of reading} + 0.002\% \text{ of range})$. The percentage of reading term represents the gain error and the percentage of range term is the offset term.

Accuracy may also be specified in ppm (parts per million). Typically, this accuracy specification is given as $\pm(\text{ppm of reading} + \text{ppm of range})$. For example, the DC voltage accuracy of a higher resolution DMM might be specified as $\pm(25 \text{ ppm of reading} + 5 \text{ ppm of range})$.

FIGURE 1-4: Relationship Between Offset and Gain Errors



Resolution

The resolution of a digital instrument is determined by the number of counts that can be displayed, which depends on the number of digits. A typical digital electrometer might have $5\frac{1}{2}$ digits, meaning five whole digits (each with possible values between 0 and 9) plus a leading half digit that can take on the values 0 or ± 1 . Thus, a $5\frac{1}{2}$ -digit display can show 0 to 199,999, a total of 200,000 counts. The resolution of the display is the ratio of the smallest count to the maximum count ($1/200,000$ or 0.0005% for a $5\frac{1}{2}$ -digit display).

For example, the specification of $\pm(0.05\% + 1 \text{ count})$ on a $4\frac{1}{2}$ -digit meter reading 10.000 volts corresponds to a total error of $\pm(5 \text{ mV} + 1 \text{ mV})$ or $\pm 6 \text{ mV}$ out of 10 V, or $\pm(0.05\% \text{ of reading} + 0.01\% \text{ of range})$, totaling $\pm 0.06\%$. Generally, the higher the resolution, the better the accuracy.

Sensitivity

The sensitivity of a measurement is the smallest change of the measured signal that can be detected. For example, voltage sensitivity may be $1 \mu\text{V}$, which simply means that any change in input signal less than $1 \mu\text{V}$ won't show up in the reading. Similarly, a current sensitivity of 10 fA implies that only changes in current greater than that value will be detected.

The ultimate sensitivity of a measuring instrument depends on both its resolution and the lowest measurement range. For example, the sensitivity of a $5\frac{1}{2}$ -digit DMM with a 200 mV measurement range is $1 \mu\text{V}$.

Absolute and Relative Accuracy

The absolute accuracy is the measure of instrument accuracy that is directly traceable to the International System of Units (SI) through the primary standards at the National Institute of Standards and Technology

(NIST) or other National Metrology Institute (NMI). Absolute accuracy may be specified as $\pm(\%$ of reading + counts), or it can be stated as $\pm(\text{ppm of reading} + \text{ppm of range})$, where ppm signifies parts per million of error.

Relative accuracy specifies instrument accuracy without taking the uncertainty of the standard into account. As with absolute accuracy, relative accuracy can be specified as $\pm(\%$ of reading + counts) or it may be stated as $\pm(\text{ppm of reading} + \text{ppm of range})$.

Transfer Stability

A special case of relative accuracy is the transfer stability, which defines relative instrument accuracy over a very short time span and narrow ambient temperature range (typically within five minutes and $\pm 1^\circ\text{C}$). The transfer stability specification is useful in situations where an instrument is used in conjunction with a more accurate standard to transfer a quantity value to a third device.

Calculating Uncertainties from Instrument Accuracy Specifications

Instrument accuracy specifications can be expressed in a variety of ways. To illustrate how to calculate measurement uncertainty from instrument specifications, here are three examples of calculations – one measuring voltage and two measuring resistance.

Example #1: DC Voltmeter Accuracy Calculation

Input Signal = 1.5 V

Voltage Range = 2 V

Accuracy Specification = $\pm(25 \text{ ppm of reading} + 5 \text{ ppm of range})$

The potential error contribution based on the specifications is calculated as:

$$\begin{aligned}\text{Error} &= \pm[1.5 \text{ V}(25 \times 10^{-6}) + 2 \text{ V}(5 \times 10^{-6})] \\ &= \pm[(37.5 \text{ V} \times 10^{-6}) + (10 \text{ V} \times 10^{-6})] \\ &= \pm 47.5 \text{ V} \times 10^{-6} \\ &= \pm 47.5 \mu\text{V}\end{aligned}$$

Thus, the reading in this example could fall anywhere within the range of $1.5 \text{ V} \pm 47.5 \mu\text{V}$, an uncertainty of $\pm 0.003\%$.

Example #2: Calculate the Accuracy of a Resistance Measurement Made Using a DC Voltmeter and a Current Source

This example shows how to calculate the accuracy of a resistance measurement that is made using two separate instruments, a voltmeter and a current source. First, the accuracy of each instrument is found separately. Then, the expected resistance accuracy is calculated using the root-sum-square (RSS) method.

Device to be measured = 20 Ω resistor using 100 mA test current

Current Source Accuracy:

Current Output = 100 mA on 100 mA range

Accuracy Specification = $\pm(0.066\%$ of output + 20 μA)

Error (ΔI) = $\pm[(100 \text{ mA} \times 0.00066) + 20 \mu\text{A}]$

= $\pm(66 \mu\text{A} + 20 \mu\text{A})$

= $\pm 86 \mu\text{A}$

Voltage Measure Accuracy:

Input Signal = $(20 \Omega \times 100 \text{ mA}) = 2 \text{ V}$

Accuracy Specification of 2 V Range = $\pm(0.012\%$ of reading + 300 μV)

Error (ΔV) = $\pm[(2 \text{ V} \times 0.00012) + 300 \mu\text{V}]$

= $\pm(240 \mu\text{V} + 300 \mu\text{V})$

= $\pm 540 \mu\text{V}$

Because $R = V/I$, the error of the resistance can be calculated using the following root-sum-square equation:

$$\left| \frac{\Delta R}{R} \right| = \sqrt{\left| \frac{\Delta I}{I} \right|^2 + \left| \frac{\Delta V}{V} \right|^2}$$

Substituting the calculated ΔI and ΔV values into the equation:

Total resistance uncertainty = $\sqrt{\left| \frac{86 \mu\text{A}}{100 \text{ mA}} \right|^2 + \left| \frac{540 \mu\text{V}}{2 \text{ V}} \right|^2} = 9.01 \times 10^{-4}$
or $\pm 0.0901\%$

Example #3: Calculate the Accuracy of a Resistance Measurement Made Using an SMU

For calculating the resistance accuracy for an SMU, use the summation method. With this method, the accuracy of the source and measurements are found separately and then added together. For higher accuracy measurements, use the Source Readback function of the SMU to measure the source output instead of using the programmed source value. In this case, both the current and voltage measurement accuracy specifications are used in the uncertainty calculation of a 20 Ω resistor with 100 mA test current:

Current Measure Accuracy (Source Readback enabled):

Input Signal = 100 mA on 100 mA range (this is the current output)

Accuracy Specification = $\pm(0.055\%$ of reading + 6 μA)

Error% = $\pm 0.061\%$

Voltage Measure Accuracy:

Input Signal = $(20 \Omega \times 100 \text{ mA}) = 2 \text{ V}$

Accuracy Specification of 2 V Range = $\pm(0.012\%$ of reading + 300 μV)

Error% = $\pm 0.027\%$

Total resistance uncertainty using Source Readback = $\pm(0.061\% + 0.027\%) = \pm 0.088\%$

The preceding examples are simplified to demonstrate calculating uncertainties from product specifications and combining them when using multiple instruments. In formal practice, the calculation involves reducing the system uncertainties to a single standard deviation, combining these uncertainties, then expanding the final system uncertainty to describe the relationship of the reported value to the true value based on a probability distribution. For details, please refer to the ISO Guide to the Expression of Uncertainty in Measurement.

1.4.3 Deratings

Accuracy specifications are subject to deratings for temperature and time drift, as discussed in the following paragraphs.

Temperature Coefficient

The temperature of the operating environment can affect accuracy. For this reason, instrument specifications are usually given over a defined temperature range. Keithley accuracy specifications on newer electrometers, nanovoltmeters, DMMs, and SMUs are usually given over the range of 18°C to 28°C. For temperatures outside of this range, a temperature coefficient such as $\pm(0.005\% + 0.1 \text{ count})/^{\circ}\text{C}$ or $\pm(5 \text{ ppm of reading} + 1 \text{ ppm of range})/^{\circ}\text{C}$ is specified. As with the accuracy specification, this value is given as a percentage of reading plus a number of counts of the least significant digit (or as a ppm of reading plus ppm of range) for digital instruments. If the instrument is operated outside the 18°C to 28°C temperature range, this figure must be taken into account, and errors can be calculated in the manner described previously for every degree less than 18°C or greater than 28°C.

Time Drift

Most electronic instruments, including electrometers, picoammeters, nanovoltmeters, DMMs, and SMUs, are subject to changes in accuracy and other parameters over a long period of time, whether or not the equipment is operating. Because of these changes, instrument specifications usually include a time period beyond which the instrument's accuracy cannot be guaranteed. The time period is stated in the specifications, and is typically over specific increments such as 90 days or one year. As noted previously, transfer stability specifications are defined for a much shorter period of time—typically five or 10 minutes.

1.4.4 Noise and Noise Rejection

Noise is often a consideration when making virtually any type of electronic measurement, but noise problems can be particularly severe when making low level measurements. Thus, it's important that noise specifications and terms are well understood when evaluating the performance of an instrument.

Normal Mode Rejection Ratio

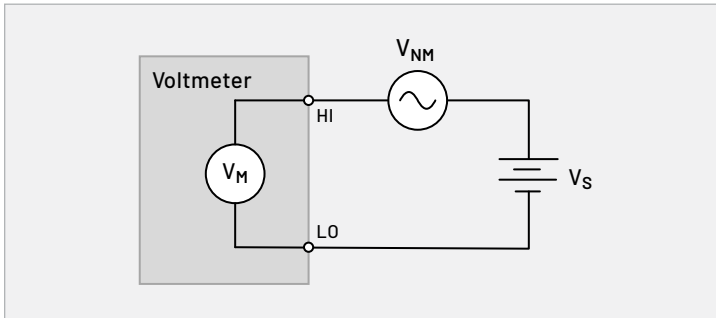
Normal mode rejection ratio (NMRR) defines how well the instrument rejects or attenuates noise that appears between the HI and LO input terminals. Noise rejection is accomplished by using the integrating A/D converter in the measuring instrument to attenuate noise at specific frequencies (usually 50 and 60 Hz) while passing low frequency or DC normal mode signals.

As shown in **Figure 1-5**, normal mode noise (V_{NM}) is an error signal that adds to the desired input signal (V_S). V_{NM} is an AC signal expressed in V_{p-p} . The DC voltmeter (V_M) measures both the V_{NM} and V_S voltages. Normal mode noise is detected by the voltmeter as a peak noise or deviation in a DC signal (V_S). NMRR is calculated as:

$$NMRR = 20 \log \left[\frac{V_{NMp-p}}{V_{M(max)} - V_{M(min)}} \right]$$

The denominator of the NMRR equation is the difference between the maximum and minimum voltage measurements (V_M) taken by the DC voltmeter. Some instruments provide statistical data, including the peak-to-peak value, in their reading buffer.

FIGURE 1.5: Normal Mode Noise



Normal mode noise can seriously affect measurements unless steps are taken to minimize the amount added to the desired signal. Careful shielding will usually attenuate normal mode noise, and many instruments have internal filtering to reduce the effects of such noise even further.

If V_{NM} noise is 50 or 60 Hz, then using 1 PLC (power line cycle) measurement integration will provide another 60 dB of rejection. In this case, V_M will be very close to V_S . Further information about line cycle integration can be found in Section 3.2.8.

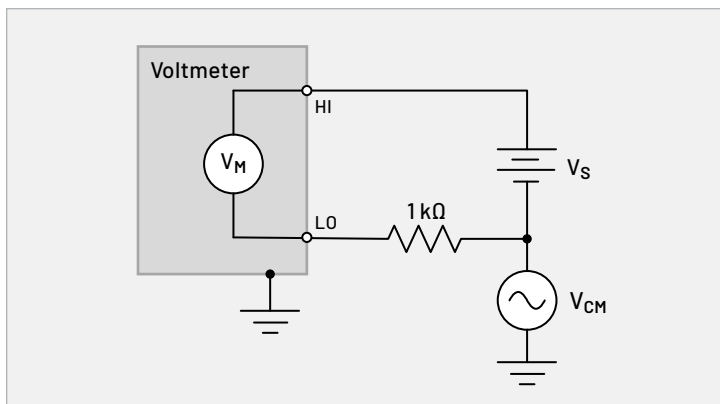
Common Mode Rejection Ratio

Common mode rejection ratio (CMRR) specifies how well an instrument rejects noise signals (V_{CM}) that appear between both the HI and LO input terminals and chassis ground, as shown in **Figure 1-6**. V_{CM} is an AC signal

expressed in V_{p-p} . Both the V_{CM} and V_S voltages are measured by the DC voltmeter (V_M). CMRR is usually measured with a 1 k Ω resistor imbalance in one of the instrument test leads. CMRR is calculated as:

$$CMRR = 20 \log \left[\frac{V_{CMp-p}}{V_{M(max)} - V_{M(min)}} \right]$$

FIGURE 1-6: Common Mode Noise



Like the denominator of the NMRR equation, $V_{M(max)} - V_{M(min)}$ is the difference between the maximum and minimum voltage measurements (V_M) taken by the DC voltmeter. V_{CMp-p} can be an AC sinusoidal, DC step voltage, or peak-to-peak of DC levels.

Although the effects of common mode noise are usually less severe than normal mode noise, this type of noise can still be a factor in sensitive measurement situations. To minimize common mode noise, connect shields only to a single point in the test system.

Noise Specifications

Both NMRR and CMRR are generally specified in dB at 50 and 60 Hz, which are the interference frequencies of greatest interest. (CMRR is often specified at DC as well.) Typical values for NMRR and CMRR are >80 dB and >120 dB respectively.

Each 20 dB increase in noise rejection ratio reduces noise voltage or current by a factor of 10. For example, a rejection ratio of 80 dB indicates noise reduction by a factor of 10^4 , but a ratio of 120 dB shows that the common mode noise would be reduced by a factor of 10^6 . Thus, a 1 V noise signal would be reduced to 100 μ V with an 80 dB rejection ratio and down to 1 μ V with a 120 dB rejection ratio.

1.4.5 Speed

Instrument measurement speed is often important in many test situations. When specified, measurement speed is usually stated as a specific number of readings per second for given instrument operating conditions. Certain factors such as integration period and the amount of filtering may affect overall instrument measurement speed. However, changing these operating modes may also alter resolution and accuracy, so there is often a tradeoff between measurement speed and accuracy.

Instrument speed is most often a consideration when making low impedance measurements. At higher impedance levels, circuit settling times become more important and are usually the overriding factor in determining overall measurement speed. Section 2.6.4 discusses circuit settling time considerations in more detail.

1.5 Circuit Design Basics

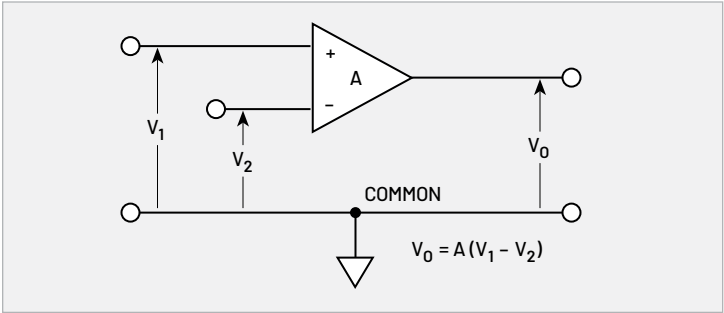
Circuits used in the design of many low level measuring instruments, whether a voltmeter, ammeter, ohmmeter, or coulombmeter, generally use circuits that can be understood as operational amplifiers. **Figure 1-7** shows a basic operational amplifier. The output voltage is given by:

$$V_0 = A(V_1 - V_2)$$

The gain (A) of the amplifier is very large, a minimum of 10^4 to 10^5 , and often 10^6 . The amplifier has a power supply (not shown) referenced to the common lead.

Current into the op amp inputs is ideally zero. The effect of feedback properly applied is to reduce the input voltage difference ($V_1 - V_2$) to zero.

FIGURE 1-7: Basic Operational Amplifier



1.5.1 Voltmeter Circuits

Electrometer Voltmeter

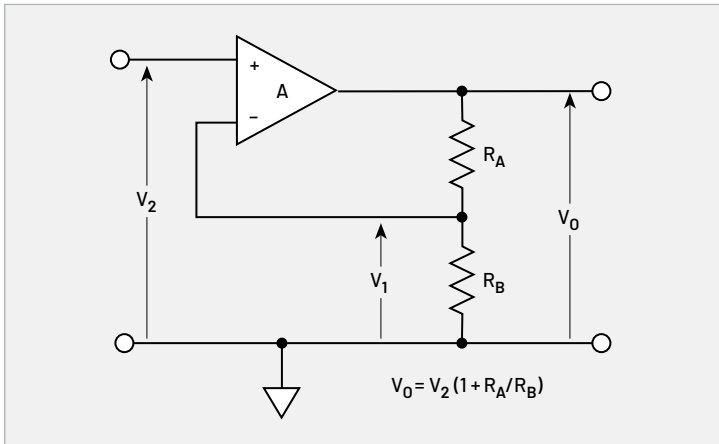
The operational amplifier becomes a voltage amplifier when connected as shown in **Figure 1-8**. The input bias current is low, so the current flowing

through R_A and R_B is the same. Assuming the gain (A) is very high, the voltage gain of the circuit is defined as:

$$V_0 = V_2 (1 + R_A/R_B)$$

Thus, the output voltage (V_0) is determined both by the input voltage (V_2), and amplifier gain set by resistors R_A and R_B . Given that V_2 is applied to the amplifier input lead, the high input resistance of the operational amplifier is the only load on V_2 , and the only current drawn from the source is the very low input bias current of the operational amplifier. In many electrometer voltmeters, R_A is shorted and R_B is open, resulting in unity gain.

FIGURE 1-8: Voltage Amplifier



Nanovoltmeter Preamplifier

The same basic circuit configuration shown in **Figure 1-8** can be used as an input preamplifier for a nanovoltmeter. Much higher voltage gain is required, so the values of R_A and R_B are set accordingly; a typical voltage gain for a nanovoltmeter preamplifier is 10^3 .

Electrometer and nanovoltmeter characteristics differ, so the operational amplifier requirements for these two types of instruments are also somewhat different. Although the most important characteristics of the electrometer voltmeter operational amplifier are low input bias current and high input impedance, the most important requirement for the nanovoltmeter input preamplifier is low input noise voltage.

1.5.2 Ammeter Circuits

There are two basic circuits used for making current measurements: these are the shunt ammeter and the feedback ammeter circuits. DMMs and older electrometers use the shunt method, but picoammeters, SMUs, and the

ammeter function of electrometers use the feedback ammeter configuration only.

Shunt Ammeter

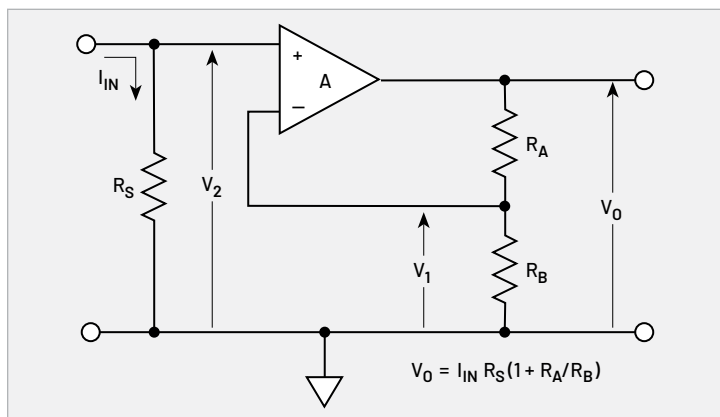
Shunting the input of a voltmeter with a resistor forms a shunt ammeter, as shown in **Figure 1-9**. The input current (I_{IN}) flows through the shunt resistor (R_S). The output voltage is defined as:

$$V_O = I_{IN} R_S (1 + R_A/R_B)$$

For several reasons, it's generally advantageous to use the smallest possible value for R_S .

First, low value resistors have better accuracy, time and temperature stability, and voltage coefficient than high value resistors. Second, lower resistor values reduce the input time constant and result in faster instrument response time. To minimize circuit loading, the input resistance (R_S) of an ammeter should be small, thus reducing the voltage burden (V_2). However, note that reducing the shunt resistance will degrade the signal-to-noise ratio.

FIGURE 1-9: Shunt Ammeter



Feedback Ammeter

In this configuration, shown in **Figure 1-10**, the input current (I_{IN}) flows through the feedback resistor (R_F). The low input bias current of the amplifier (A) changes the current (I_{IN}) by a negligible amount. The amplifier output voltage is calculated as:

$$V_O = -I_{IN} R_F$$

Thus, the output voltage is proportional to the input current, and overall sensitivity is determined by the feedback resistor (R_F). The low voltage burden (V_1) and corresponding fast rise time are achieved by the high gain op amp, which forces V_1 to be nearly zero.

Picoammeter amplifier gain can be changed as in the voltmeter circuit by using the combination shown in **Figure 1-11**. Here, the addition of R_A and R_B forms a “multiplier,” and the output voltage is defined as:

$$V_0 = -I_{IN}R_F(1 + R_A/R_B)$$

FIGURE 1-10: Feedback Ammeter

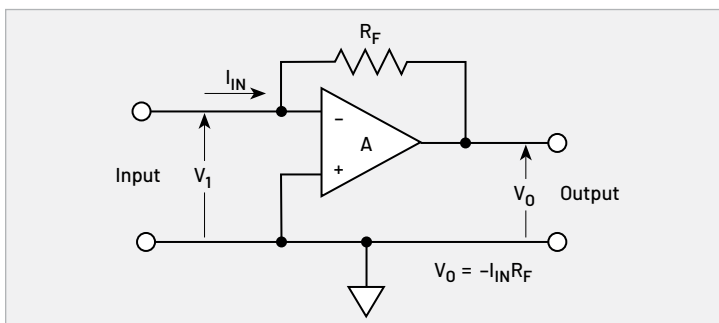
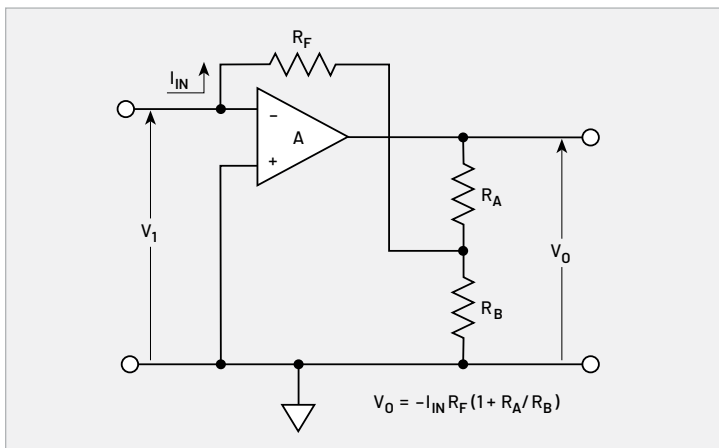


FIGURE 1-11: Feedback Ammeter with Selectable Voltage Gain



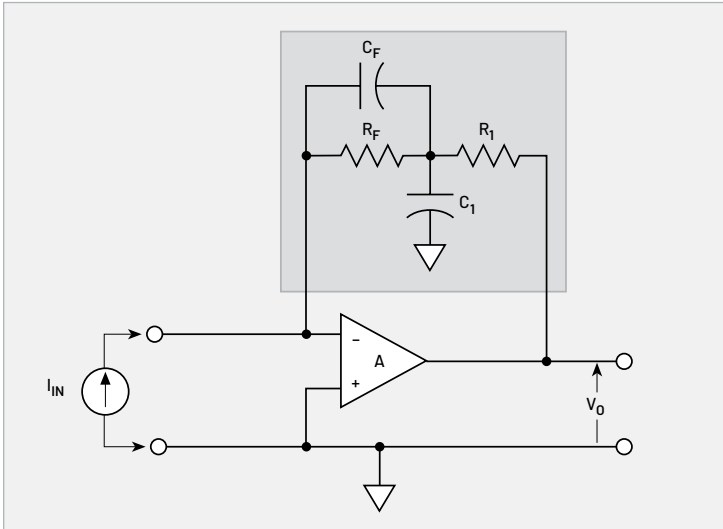
High Speed Picoammeter

The rise time of a feedback picoammeter is normally limited by the time constant of the feedback resistor (R_F) and any shunting capacitance (C_F). A basic approach to high speed measurements is to minimize stray shunting capacitance through careful mechanical design of the picoammeter.

Remaining shunt capacitance can be effectively neutralized by a slight modification of the feedback loop, as shown in **Figure 1-12**. If the time constant $R_1 C_1$ is made equal to the time constant $R_F C_F$, the shaded area of the

circuit behaves exactly as a resistance R_F with zero C_F . The matching of time constants in this case is fairly straightforward, because the capacitances involved are all constant and aren't affected by input capacitances.

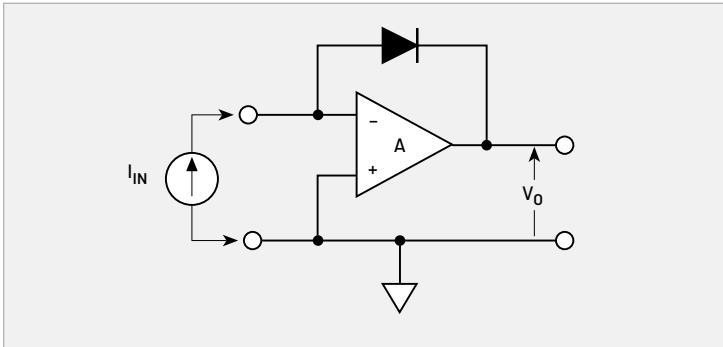
FIGURE 1-12: Neutralizing Shunt Capacitance



Logarithmic Picoammeter

A logarithmic picoammeter can be formed by replacing the feedback resistor in a picoammeter with a diode or transistor exhibiting a logarithmic voltage-current relationship, as shown in **Figure 1-13**. The output voltage (and the meter display) is then equal to the logarithm of the input current. As a result, several decades of current can be read on the meter without changing the feedback element.

FIGURE 1-13: Logarithmic Picoammeter



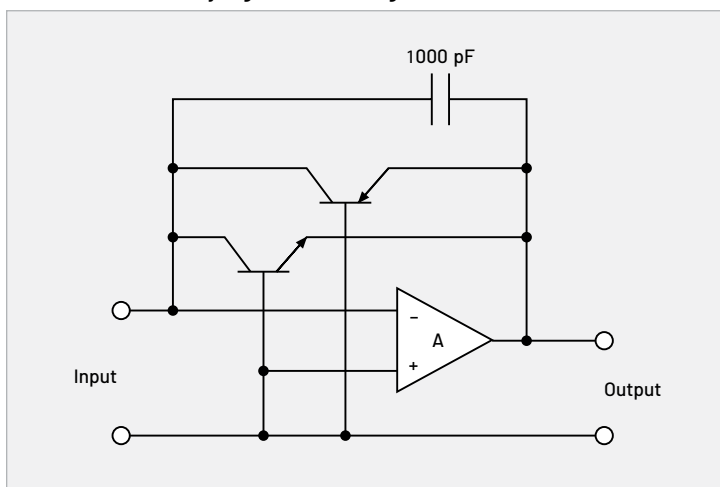
The main advantage of a logarithmic picoammeter is its ability to follow current changes over several decades without range changing.

The big disadvantage is the loss of accuracy and resolution, but some digital picoammeters combine accuracy and dynamic range by combining autoranging and digital log conversion.

If two diodes are connected in parallel, back-to-back, this circuit will function with input signals of either polarity.

Using a small-signal transistor in place of a diode produces somewhat better performance. **Figure 1-14** shows an NPN transistor and a PNP transistor in the feedback path to provide dual polarity operation.

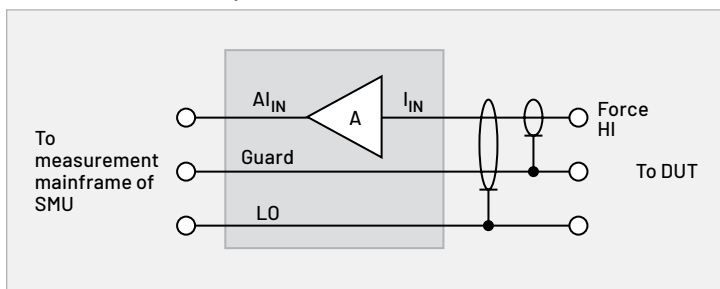
FIGURE 1-14: Dual Polarity Log Current to Voltage Converter



Remote Preamp Circuit (Source V, Measure I Mode)

Figure 1-15 illustrates a typical preamp circuit. In the Source V, Measure I mode, the SMU applies a programmed voltage and measures the current

FIGURE 1-15: Remote Preamp in Source V, Measure I Mode



flowing from the voltage source. The sensitive input is surrounded by a guard, which can be carried right up to the DUT for fully guarded measurements. The remote preamp amplifies the low current signal passing through the DUT; therefore, the cable connecting the remote preamp to the measurement mainframe carries only high level signals, minimizing the impact of cable noise.

1.5.3 Coulombmeter Circuit

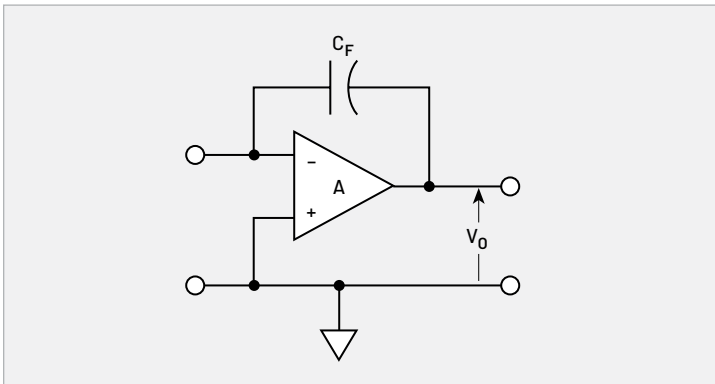
The coulombmeter measures electrical charge that has been stored in a capacitor or that might be produced by some charge generating process.

For a charged capacitor, $Q = CV$, where Q is the charge in coulombs on the capacitor, C is the capacitance in farads, and V is the potential across the capacitor in volts. Using this relationship, the basic charge measuring scheme is to transfer the charge to be measured to a capacitor of known value and then measure the voltage across the known capacitor; thus, $Q = CV$.

The electrometer is ideal for charge measurements because the low input bias current won't alter the transferred charge during short time intervals and the high input resistance won't allow the charge to bleed away.

Electrometers use a feedback circuit to measure charge, as shown in **Figure 1-16**. The input capacitance of this configuration is AC_F . Thus, large effective values of input capacitance are obtained using reasonably sized capacitors for C_F .

FIGURE 1-16: Feedback Coulombmeter



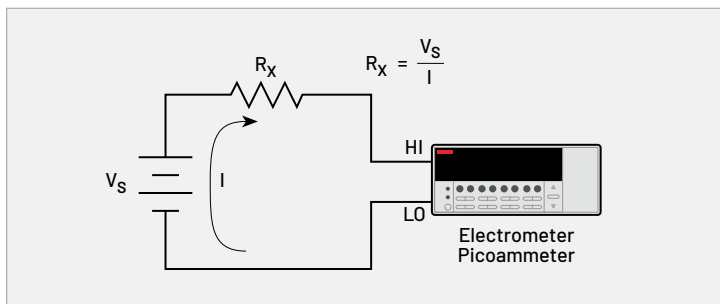
1.5.4 High Resistance Ohmmeter Circuits

Electrometer Picoammeter and Voltage Source

In this configuration (**Figure 1-17**), a voltage source (V_S) is placed in series with an unknown resistor (R_X) and an electrometer picoammeter. The voltage drop across the picoammeter is small, so essentially all the voltage

appears across R_X , and the unknown resistance can be computed from the sourced voltage and the measured current (I).

FIGURE 1-17: High Resistance Measurement Using External Voltage Source



The advantages of this method are that it's fast and, depending on the power supply voltage and insulating materials, it allows measuring extremely high resistance. Also, with an adjustable voltage source, the voltage dependence of the resistance under test can be obtained directly.

Usually, this method requires two instruments: a voltage source and a picoammeter or electrometer. Some electrometers and picoammeters, however, have a built-in voltage source and are capable of measuring the resistance directly. SMUs that measure very small currents (less than nano-amps) can also be used.

Electrometer Ohmmeter Using Built-In Current Source

Figure 1-18 shows the basic configuration of an alternative form of electrometer ohmmeter. A built-in constant-current source, formed by V_S and R_i , forces a known current (I) through the unknown resistance (R_X). The resulting voltage drop is proportional to the unknown resistance and is indicated by the meter as resistance, rather than voltage.

The disadvantage of this method is that the voltage across the unknown is a function of its resistance, so it cannot be easily controlled. Very high resistances tend to have large voltage coefficients; therefore, measurements made with a constant voltage are more meaningful. In addition, the response speed for resistances greater than 10 G Ω will be rather slow. This limitation can be partially overcome by guarding.

Electrometer Ohmmeter with Guarded Ohms Mode

Figure 1-19 shows a modification of the circuit in **Figure 1-18** in which the HI input node is surrounded with a guard voltage from the operational amplifier output. The amplifier has unity gain, so this guard voltage is virtually the same potential as V_i and the capacitance (C_g) of the input cable is largely neutralized, resulting in much faster measurements of resistances greater than 10 G Ω . The guarded mode also significantly reduces the effect of input cable leakage resistance, as discussed in Section 2.4.2.

FIGURE 1-18: Electrometer Ohmmeter with Built-In Current Source

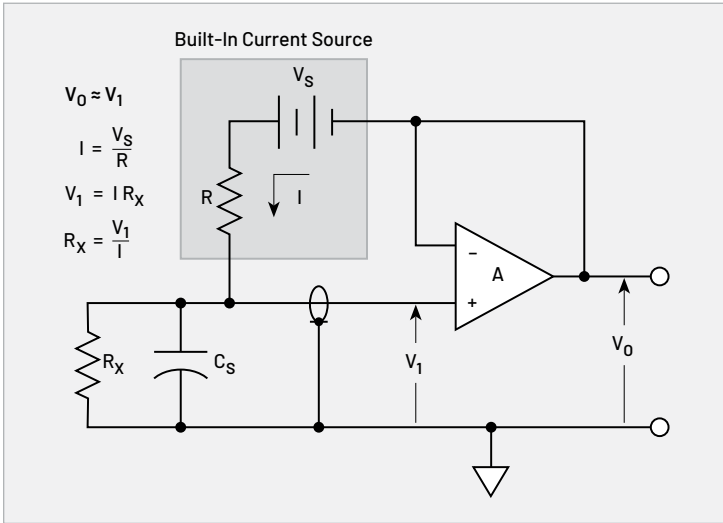
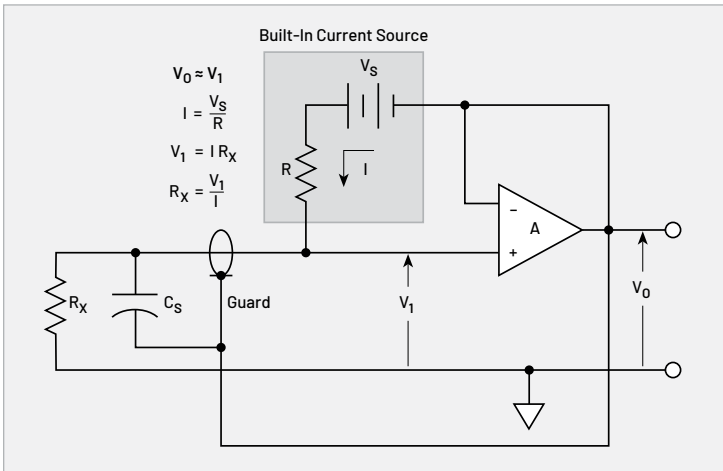


FIGURE 1-19: Electrometer Ohmmeter with Guarded Ohms



Electrometer Voltmeter and External Current Source

In this method, shown in **Figure 1-20**, a current source generates current (I), which flows through the unknown resistor (R_X). The resulting voltage drop (V_1) is measured with an electrometer voltmeter, and the value of R_X is calculated from the voltage and current.

FIGURE 1-20: High Resistance Measurement Using External Current Source with Electrometer Voltmeter

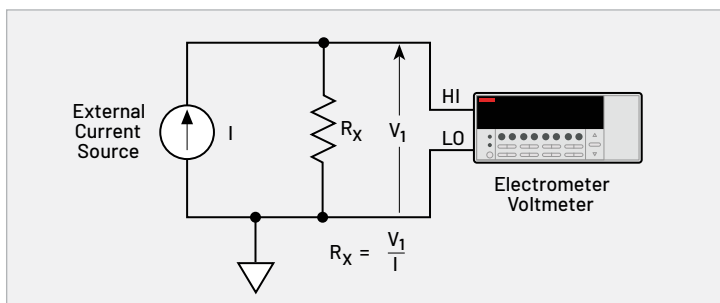
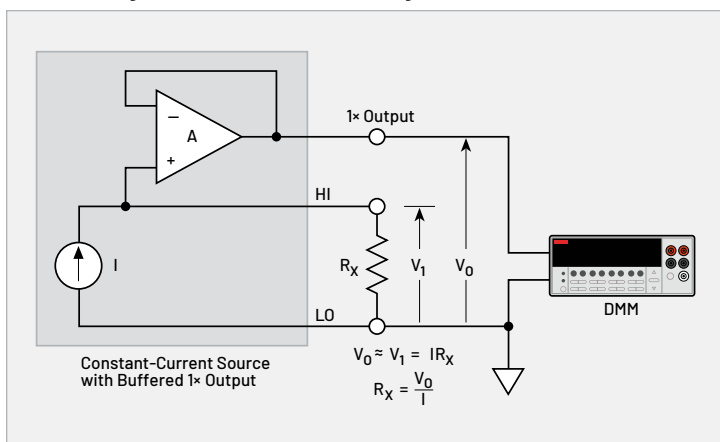


FIGURE 1-21: High Resistance Measurement Using a True Current Source with a DMM



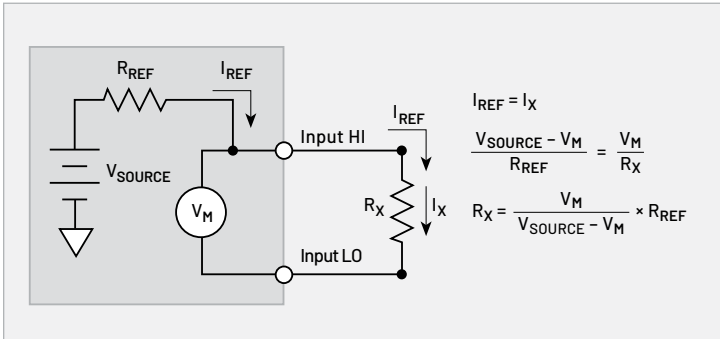
If the current source has a buffered $\times 1$ output, a low impedance voltmeter, such as a DMM, may be used to read the voltage across R_X . This arrangement is shown in **Figure 1-21**.

DMM Ohmmeter

For high resistance measurements, a DMM may use the ratiometric method shown in **Figure 1-22**. This technique is only used on the higher resistance ranges (10 M Ω and 100 M Ω) to allow the lowest noise performance. Test current for this method is generated by a built-in voltage source (V_{SOURCE}) through a reference resistance (R_{REF}) as shown.

Because the DMM voltmeter has very high input impedance (>10 G Ω), current through the voltmeter branch is insignificant and can be discounted. Therefore, I_{REF} equals the current of the unknown resistance (I_X). The voltage drop across the unknown resistance (R_X) is measured by the DMM voltmeter (V_M) and the resistance (R_X) is calculated.

FIGURE 1-22: Two-Wire Ratiometric Method of DMM to Measure High Resistance



1.5.5 Low Resistance Ohmmeter Circuits

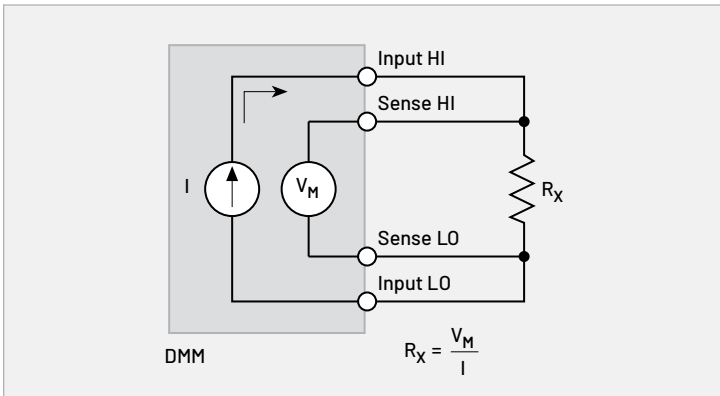
Nanovoltmeter and External Current Source

If the electrometer in **Figure 1-20** is replaced with a nanovoltmeter, the circuit can be used to measure very low resistances (less than micro-ohms). Using a four-wire method eliminates any lead resistance from the measurement. A current source that can automatically change polarity can be used to correct for offsets. First, a voltage measurement is taken with positive test current, then another voltage measurement is taken with negative test current. Averaging the difference between the two readings cancels the offsets.

DMM Ohmmeter

For making low resistance measurements, the typical DMM will use a four-wire constant-current source method as shown in **Figure 1-23**. Using this method, the DMM sources a constant current (I) to the device under test

FIGURE 1-23: Four-Wire Constant-Current Source Method



(R_x) and measures the voltage (V_M). Resistance is then calculated using the known current and measured voltage. In the four-wire mode, the voltage is measured at the DUT so that the voltage drops due to the lead resistances will not affect the measurement accuracy. A DMM capable of measuring low resistance usually has a way of compensating for voltage offsets.

The test current sourced to the DUT depends on the selected measurement range. For example, a DMM with a 100 Ω range may have a test current of 1 mA.

Some DMM ohmmeters may have a dry circuit mode. Dry circuit ohms uses a constant current source with voltage monitoring that is used to clamp the current source voltage. The current source will remain constant as long as the monitoring voltage is <20 mV. When voltage exceeds 20 mV, the current source shunts current internal to the DMM until 20 mV is maintained at the DUT.

1.5.6 Complete Instruments

Digital Electrometers

Figure 1-24 is a block diagram of a typical digital electrometer. The analog section is similar to the circuitry discussed previously. An electrometer preamplifier is used at the input to increase sensitivity and raise input resistance. The output of the main amplifier is applied to both the analog output and the A/D converter. Range switching and function switching, instead of being performed directly, are controlled by the microprocessor.

The microprocessor also controls the A/D converter and supervises all other operating aspects of the instrument. The input signal to the A/D converter is generally 0–2 V DC. After conversion, the digital data is sent to the display and to the communications interface (IEEE-488, RS-232, USB, or Ethernet).

Digital Multimeters (DMMs)

Most DMMs include five measurement functions: DC volts, AC volts, ohms, DC amps, and AC amps. As shown in **Figure 1-25**, various signal processing circuits are used to convert the input signal into a DC voltage that can be converted to digital information by the A/D converter.

The DC and AC attenuator circuits provide ranging for the AC and DC functions. The AC converter changes AC signals to DC; the ohms converter provides a DC analog signal for resistance measurements. Precision shunts are used to convert currents to voltages for the amps functions.

Once the input signal is appropriately processed, it's converted to digital information by the A/D converter. Digital data is then sent to the display and to the communications interface (IEEE-488, RS-232, USB, or Ethernet).

Nanovoltmeters

A nanovoltmeter is a sensitive voltmeter optimized to measure very low voltages. As shown in **Figure 1-26**, the nanovoltmeter incorporates a low

FIGURE 1-24: Typical Digital Electrometer

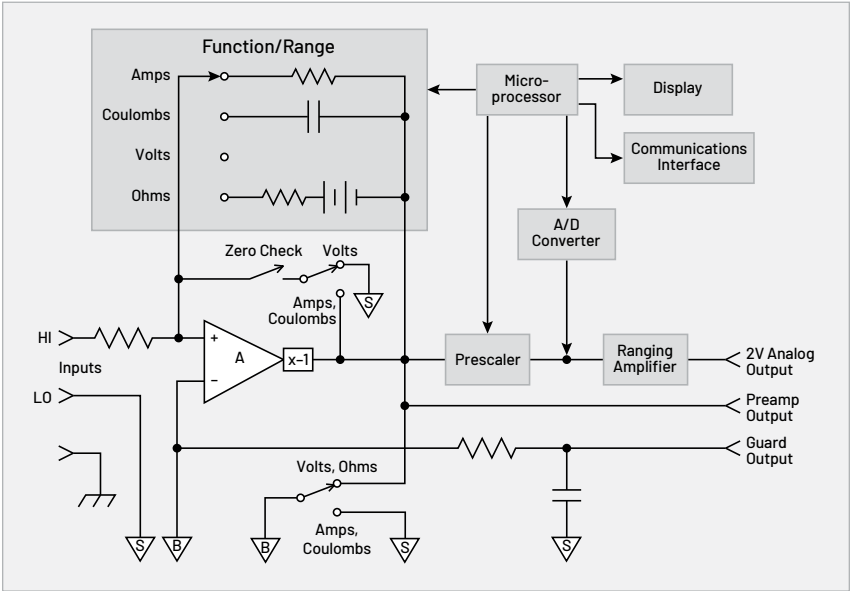
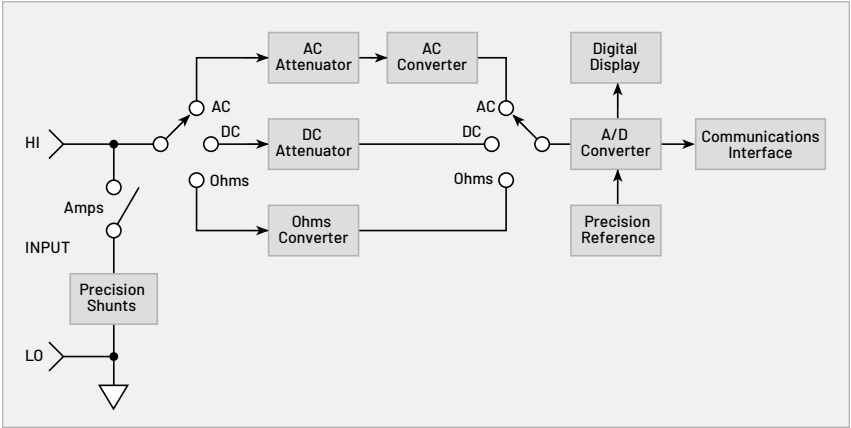


FIGURE 1-25: DMM Block Diagram



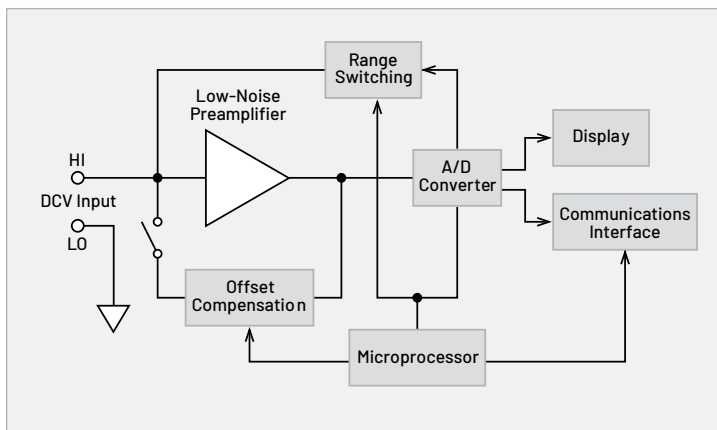
noise preamplifier, which amplifies the signal to a level suitable for A/D conversion (typically 2–3 V full scale). Specially designed preamplifier circuits ensure that unwanted noise, thermoelectric EMFs, and offsets are kept to an absolute minimum.

In order to cancel internal offsets, an offset or drift compensation circuit allows the preamplifier offset voltage to be measured during spe-

cific phases of the measurement cycle. The resulting offset voltage is subsequently subtracted from the measured signal to maximize measurement accuracy.

Once the preamplifier amplifies the signal, it's converted to digital information by the A/D converter. Digital data is then sent to the display and the communications interface.

FIGURE 1-26: Typical Nanovoltmeter



Source Measure Unit (SMU)

Source measure units (SMUs) provide four functions in one instrument: source current, source voltage, measure current, and measure voltage. These instruments have four-quadrant source functionality, which provides for a sink capability, so they can be used as electronic loads. Some also have resistance measurement and pulsing capabilities. **Figure 1-27** illustrates the Source I and Measure V mode of an SMU with a driven guard. In this configuration, the instrument functions as a high impedance current source with voltage and current measurements and voltage limit capability. The driven guard ($\times 1$ Buffer) ensures that the Guard and Force HI terminals are always at the same potential. Proper use of guarding virtually eliminates leakage paths in the cable, test fixture, and connectors.

Selecting either local or remote sense determines where the voltage measurements will be made. In local sense, the voltage is measured between the Force HI and Force LO terminals. In remote sense (Kelvin or 4-wire mode), the voltage is measured at the device under test, between the Sense HI and Sense LO terminals, eliminating any voltage drops due to lead resistance.

When configured to Source V and Measure I (as shown in **Figure 1-28**), the instrument functions as a low impedance voltage source with current and voltage measurements and current limit capability. Sense circuitry

monitors the output voltage continuously and adjusts the voltage source as needed.

FIGURE 1-27: Source I and Measure V Mode of Source Measure Unit (SMU)

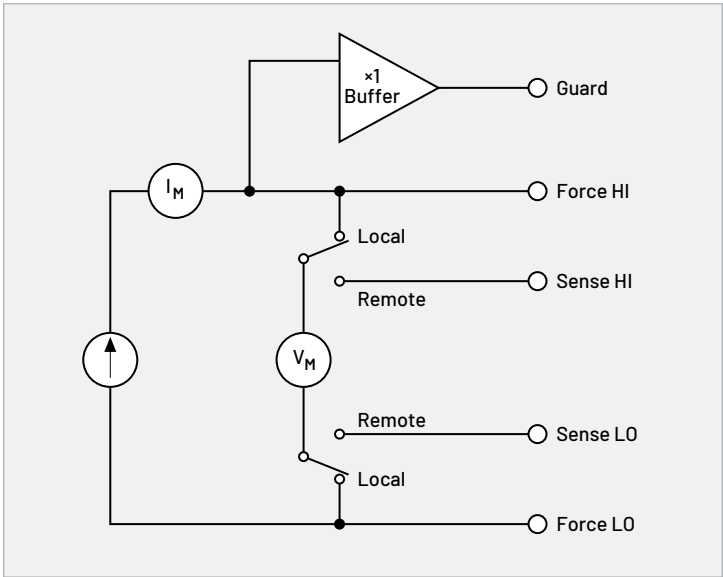
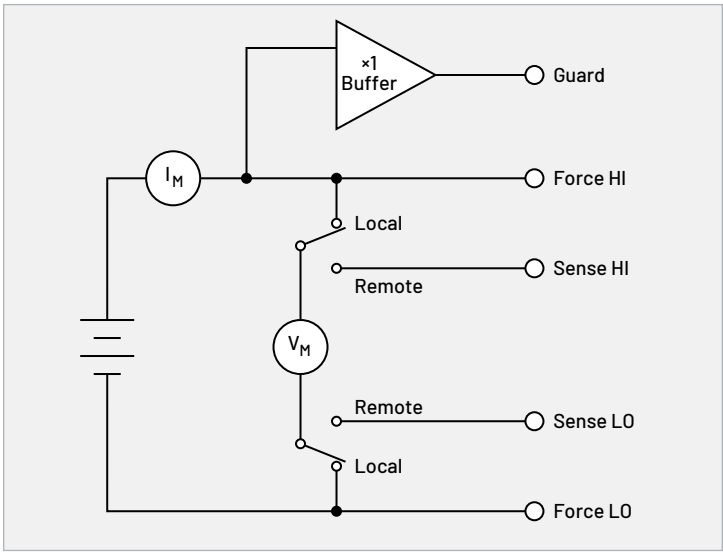


FIGURE 1-28: Source V and Measure I Mode of Source Measure Unit (SMU)



SECTION 2

Measurements from High Resistance Sources

2.1 Introduction

As described in Section 1 of this handbook, measurements made from high resistance sources include DC voltage, low DC current, high resistance, and charge measurements. The instruments used to make these high impedance measurements include electrometers, picoammeters, and source measure units (SMUs). Section 1 described the basic circuits of these instruments and their measurement functions. Section 2 offers more detailed information about these functions, various interferences and error sources, and ways to maximize the accuracy of measurements made from high resistance sources. For easier reference, the information in Section 2 is organized into these subsections:

- 2.2 High Impedance Voltage Measurements: A discussion of loading errors and the use of guarding to minimize these errors, as well as information on insulating materials used for making high impedance measurements.
- 2.3 Low Current Measurements: Information about making successful low current measurements is described with such topics as leakage current and guarding, noise and source impedance, zero drift, generated currents, voltage burden, overload protection, and using a coulomb-meter to measure low current.
- 2.4 High Resistance Measurements: Describes the constant-voltage and constant-current methods for measuring high resistance.
- 2.5 Charge Measurements: A discussion of the error sources and ways to minimize them, zero check, and extending the range of the charge function of an electrometer.
- 2.6 General High Impedance Measurement Considerations: A discussion of techniques and error sources that affect high impedance measurements in general. Some of the topics include measurement connections, electrostatic interference and shielding, environmental factors, speed considerations, etc.
- 2.7 High Impedance Measurement Optimization Summary

2.2 Voltage Measurements from High Resistance Sources

Measurements from voltage sources with high internal resistance are subject to a number of errors, such as loading errors from the voltmeter's input resistance and input bias current, as well as from external shunt resistance and capacitance. The following paragraphs discuss these error sources and ways to minimize their effects. For a discussion of errors due to improper connections and electrostatic interference, see Section 2.6.

2.2.1 Loading Errors and Guarding

Input Resistance Loading

Voltage measurements from high resistance sources are subject to loading errors from the meter input resistance, as well as the leakage resistance of

the connecting cable. A practical voltmeter may be represented by an ideal infinite input-resistance voltmeter (V_M) in parallel with a resistor equal to the specified input resistance (R_{IN}), as shown in **Figure 2-1**. When a source whose Thevenin equivalent is V_S in series with R_S is connected to the input, the voltage (V_M) appearing across the meter input terminals is reduced by the voltage divider action of R_S and R_{IN} as follows:

$$V_M = V_S \left(\frac{R_{IN}}{R_S + R_{IN}} \right)$$

For example, assume $R_S = 100 \text{ k}\Omega$ and $R_{IN} = 10 \text{ M}\Omega$. If $V_S = 5 \text{ V}$, the actual voltage measured by the meter is:

$$V_M = 5 \left(\frac{10^7}{10^5 + 10^7} \right)$$

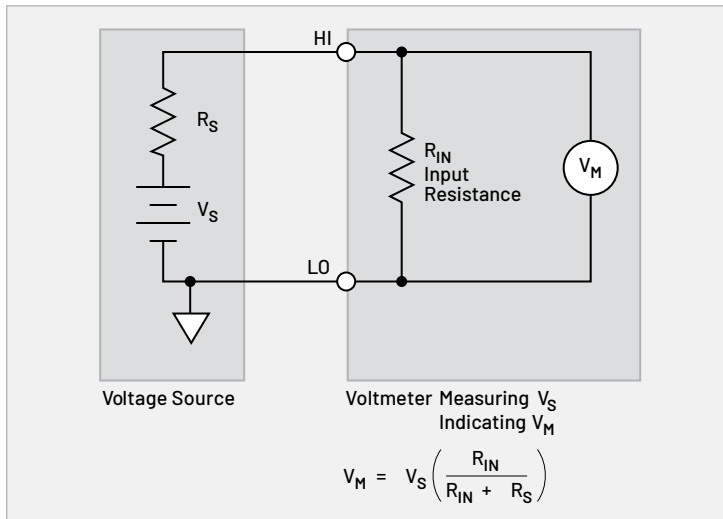
$$V_M = 4.95 \text{ V}$$

Thus, input resistance loading would result in an error of 1% in this example.

The meter input resistance should be much higher than the source resistance. For example, if the desired accuracy is 1%, then the meter resistance must be more than 100 times the source resistance. For higher accuracy, this ratio must be correspondingly higher.

The connecting cable ordinarily isn't a factor, but with very high source resistances ($>10 \text{ G}\Omega$) or under extreme environmental conditions, it can cause significant loading errors. It may be possible to guard the cable and

FIGURE 2-1: Effects of Input Resistance Loading on Voltage Measurement Accuracy



thus reduce its loading on the measurement. This is discussed in the paragraphs on Shunt Resistance Loading and Guarding.

Input Bias Current Loading

Another consideration when measuring voltages from high resistance sources is the input bias current of the voltmeter. The input bias current flows at the instrument input due to internal instrument circuitry and the internal bias voltage. As shown in **Figure 2-2**, the input bias current (I_{BIAS}) develops an error voltage across the source resistance (R_S). Thus, the actual measured voltage (V_M) differs from the source voltage (V_S) as follows:

$$V_M = V_S \pm I_{BIAS}R_S$$

For example, assume the following parameters:

$$I_{BIAS} = 1 \text{ pA} \quad R_S = 10 \text{ G}\Omega \quad V_S = 10 \text{ V}$$

The actual voltage measured by the meter is:

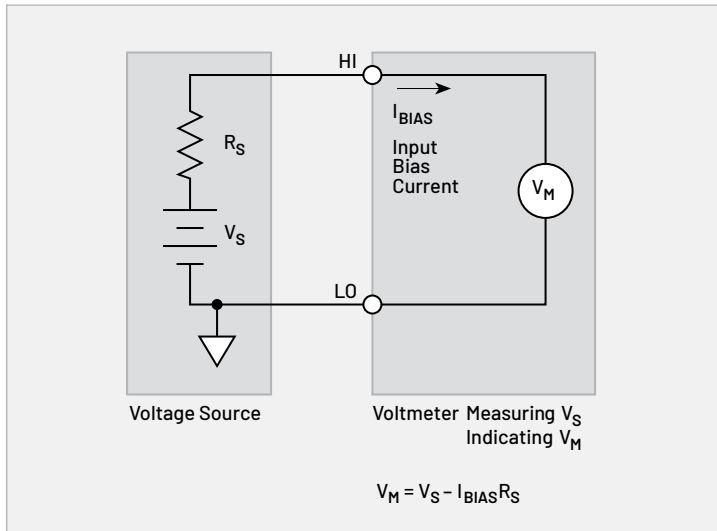
$$V_M = 10 \pm (10^{-12} \cdot 10^{10})$$

$$V_M = 10 \pm 0.01$$

$$V_M = 9.99 \text{ V or } 10.01 \text{ V (depending on the input bias current polarity)}$$

Thus, the error caused by input bias current would be about 0.1% in this example.

FIGURE 2-2: Effects of Input Bias Current on Voltage Measurement Accuracy



DMMs and nanovoltmeters have bias currents from 1 pA to 1 nA, although DMM bias currents are not always specified. Electrometers are known for their low input bias current, which is usually a few femtoamps. The input bias current specification of an electrometer is often listed in the ammeter section of the specifications even though it affects all of the instrument's functions. SMUs also have very low input bias currents, although usually not as low as an electrometer's. For determining the input bias current of an SMU voltmeter, use the accuracy of the current source at 0 A on the specific range.

Although input bias current is a common source of this type of error, currents generated by external circuits can also result in errors due to voltage drops across the source resistance. Typical sources of such offset currents are insulators and cables.

Shunt Resistance Loading and Guarding

External shunt resistances, such as leaky cables and dirty insulators, may also cause loading errors.

Any external shunt resistance across the voltage source will attenuate the measured voltage, as shown in **Figure 2-3**. As in the case of input resistance voltage loading, the shunt resistance (R_{SHUNT}) and the source resistance (R_S) form a voltage divider that reduces the measured voltage (V_M) as follows:

$$V_M = V_S \left(\frac{R_{SHUNT}}{R_{SHUNT} + R_S} \right)$$

For example, assume $R_S = 10 \text{ G}\Omega$ and $R_{SHUNT} = 100 \text{ G}\Omega$. If V_S has a value of 10 V, the measured voltage (V_M) is:

$$V_M = 10 \left(\frac{10^{11}}{10^{11} + 10^{10}} \right)$$

$$V_M = 9.09 \text{ V}$$

In this instance, the error due to shunt loading is approximately 9%.

Cable leakage resistance is a common source of shunt resistance loading, as shown in **Figure 2-4**. In this case, the measured voltage (V_M) is attenuated by the voltage divider formed by R_S and the cable resistance (R_L):

$$V_M = V_S \left(\frac{R_L}{R_S + R_L} \right)$$

To reduce errors due to shunt resistance, use cables, connectors, and test fixturing with the highest possible insulation resistance. In addition, the use of guarding will eliminate any residual errors.

The error due to cable leakage can be greatly reduced by the use of guarding, as shown in **Figure 2-5**. In the guarded configuration, the cable shield is now connected to the output of the guard buffer instead of the voltmeter LO terminal. R_G represents the resistance from the cable shield

FIGURE 2-3: Effects of Shunt Resistance on Voltage Measurement Accuracy

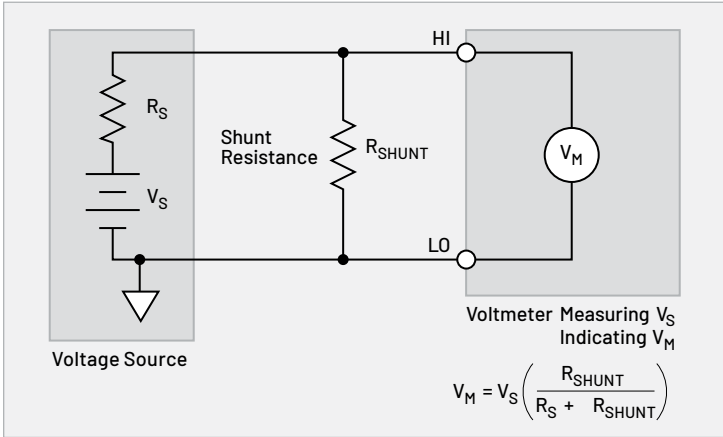
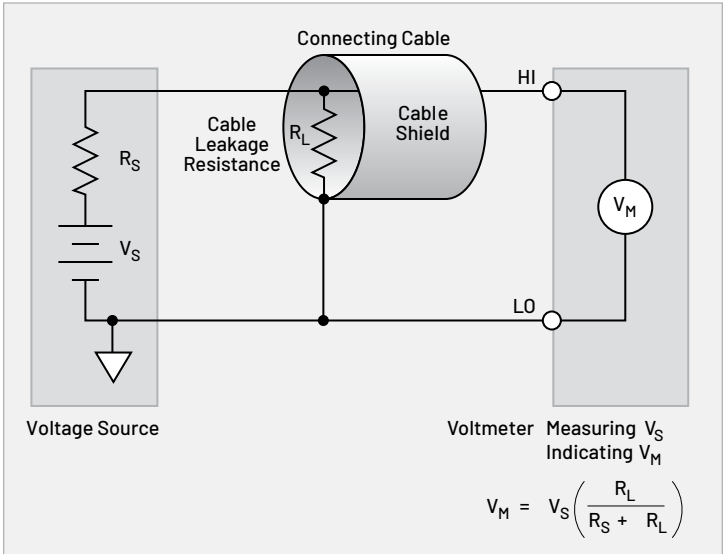


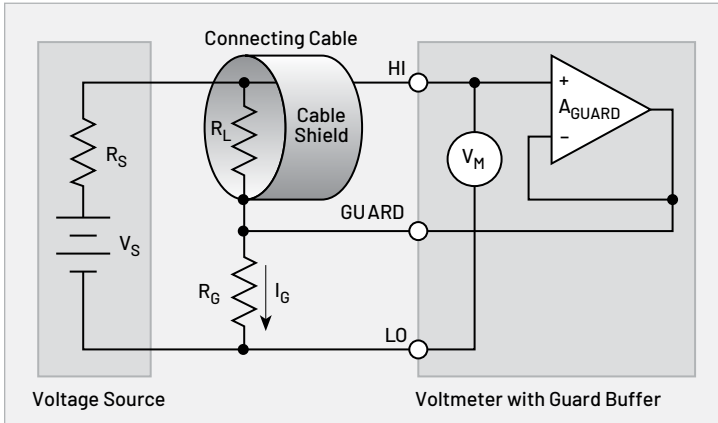
FIGURE 2-4: Effect of Cable Leakage Resistance on Voltage Measurement Accuracy



to voltmeter LO, and I_G is the current through R_G as a result of driving the shield to the same potential as the input HI terminal. This current is supplied by the guard buffer, not the voltage source. The voltage across R_L is now many decades lower, so the leakage current will be negligible in most cases.

By definition, a guard is a low impedance point in the circuit that's at nearly the same potential as the high impedance input terminal.

FIGURE 2-5: Guarded Configuration



In electrometers, the preamplifier output terminal is such a point, and can be used to reduce the effect of cable leakage, as shown in **Figure 2-5**. An additional benefit is that the effective cable capacitance is also reduced, making the response speed of the circuit much faster. This is discussed in detail in the paragraphs on Shunt Capacitance Loading and Guarding.

The source measure unit (SMU) can also be used to measure voltages from a high resistance source and the Guard terminal will make a similar improvement. Check your SMU's documentation for how to connect to guard.

The circuit of the electrometer when used as a voltmeter is actually as shown in **Figure 2-6**. The guard amplifier is a unity-gain amplifier with very high input impedance. The open-loop gain, A_{GUARD} , ranges from 10^4 to 10^6 . The leakage resistance (R_L) is multiplied by this gain and the measured voltage becomes:

$$V_M = V_S \left(\frac{A_{GUARD} R_L}{R_S + A_{GUARD} R_L} \right)$$

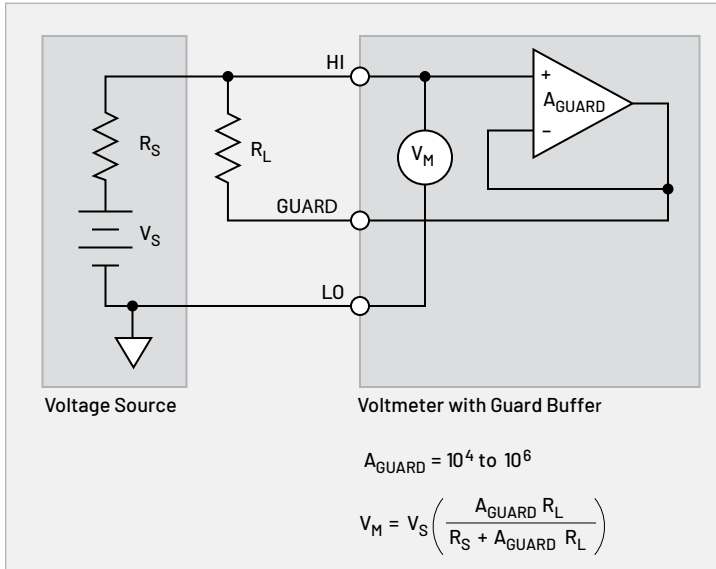
Example: Assume R_S has a value of 10 G Ω and R_L is 100 G Ω . If we assume a mid-range value of 10^5 for A_{GUARD} and a value of 10 V for V_S , the voltage measured by the meter is:

$$V_M = 10 \left(\frac{10^{16}}{1.000001 \times 10^{16}} \right)$$

$$V_M = 9.99999 \text{ V}$$

Thus, we see the loading error with guarding is less than 0.001%. In contrast, the unguarded error voltage with this combination of source and shunt resistances would be about 9%.

FIGURE 2-6: Guarding Leakage Resistance



Shunt Capacitance Loading and Guarding

The settling time of a voltage measurement depends both on the equivalent source resistance and the effective capacitance at the input of the voltmeter; this input capacitance consists of the meter input capacitance in parallel with the input cable capacitance. Even a small amount of shunt capacitance can result in long settling times if the source resistance is high. For example, a shunt capacitance of 100 pF (including the input cable) and a source resistance of 20 GΩ will result in an RC time constant of two seconds. Ten seconds must be allowed for the measurement to settle to within 1% of the final value.

Figure 2-7 demonstrates the effects of shunt capacitance loading on the input of a typical high impedance voltmeter. The signal source is represented by V_S and R_S , the shunt capacitance is C_{SHUNT} , and the measured voltage is V_M . Initially, the switch is open, and C_{SHUNT} holds zero charge.

When the switch is closed, the source voltage (V_S) is applied to the input, but the measured voltage across C_{SHUNT} doesn't rise instantaneously to its final value. Instead, the voltage rises exponentially as follows:

$$V_M = V_S (1 - e^{-t/R_S C_{\text{SHUNT}}})$$

Also, the charge (Q_{IN}) transferred to the capacitor is:

$$Q_{\text{IN}} = V_S C_{\text{SHUNT}}$$

FIGURE 2-7: Shunt Capacitance Loading

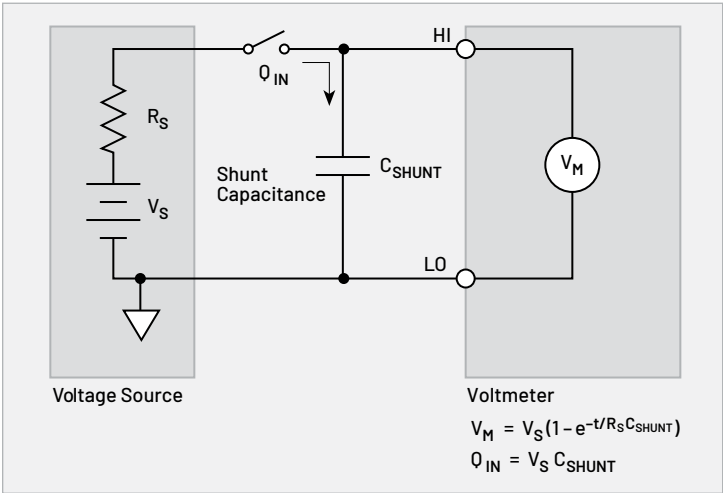
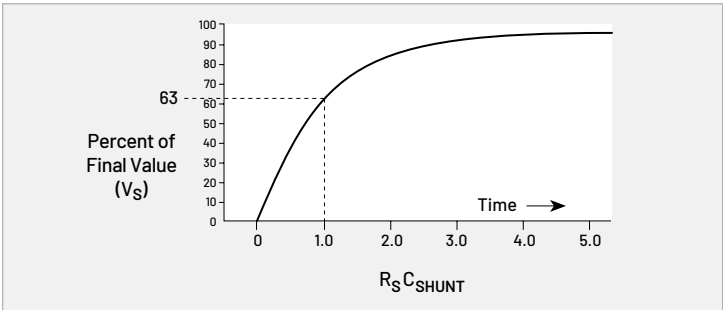


FIGURE 2-8: Exponential Response of Voltage Across Shunt Capacitance



The charging of C_{SHUNT} yields the familiar exponential curve shown in **Figure 2-8**. After one time constant ($\tau = RC$), the measured voltage rises to within 63% of its final value; final values for various time constants are summarized in **Table 2-1**.

TABLE 2-1: Settling Times to Percent of Final Value

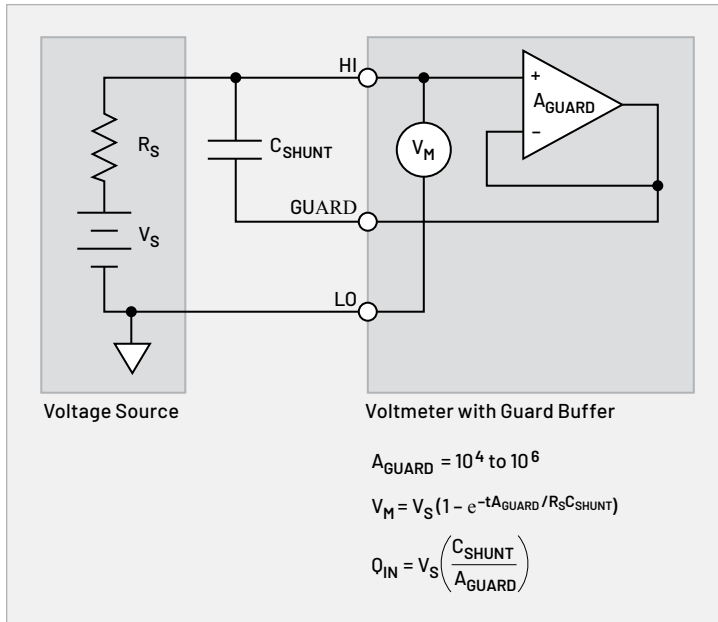
Time Constant (τ^*)	Percent of Final Value
1	63 %
2	86 %
3	95 %
4	98 %
5	99.3%

* $\tau = RC$, where R = resistance (ohms), C = capacitance (farads)

Example: Assume $R_S = 10 \text{ G}\Omega$ and $C_{SHUNT} = 100 \text{ pF}$. This combination results in an RC time constant of one second. Thus, it would take five seconds for the circuit to settle to within less than 1% of final value. With a 10 V change in V_S , a total of 1nC of charge would be transferred to C_{SHUNT} using the equation $Q_{IN} = V_S C_{SHUNT}$.

Although the primary advantage of guarding is a reduction in the effects of shunt resistance, another important aspect is the reduction in the effects of shunt capacitance. As shown in **Figure 2-9**, the guard buffer significantly reduces the charging time of C_{SHUNT} because of the open-loop gain (A_{GUARD}), which is typically 10^4 to 10^6 .

FIGURE 2-9: Guarding Shunt Capacitance



With guarding, the rise time of the measured voltage (V_M) now becomes:

$$V_M = V_S (1 - e^{-t A_{GUARD} / R_S C_{SHUNT}})$$

and the charge transferred to C_{SHUNT} is:

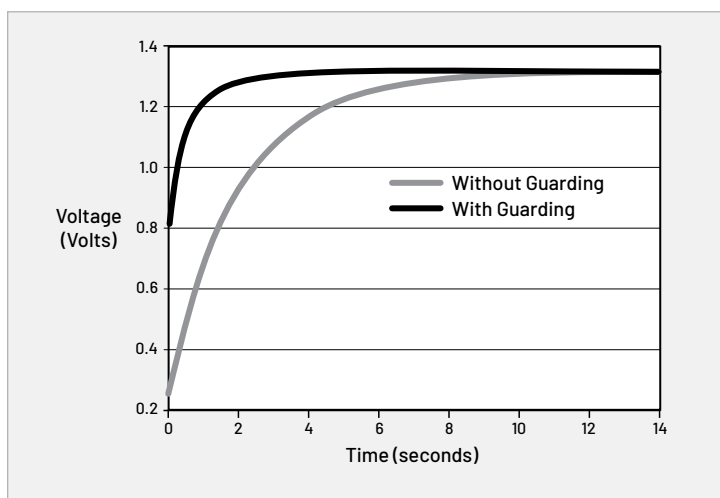
$$Q_{IN} = \left(\frac{V_S C_{SHUNT}}{A_{GUARD}} \right)$$

Example: Assume $R_S = 10 \text{ G}\Omega$ and $C_{SHUNT} = 100 \text{ pF}$, as in the unguarded example given previously. With a nominal value of 10^5 for A_{GUARD} , we can see the guarded RC settling time to 99.3% of final value is reduced to approximately $5 \text{ s} / 10^5 = 50 \text{ }\mu\text{s}$, an insignificant period of time compared to

the time it typically takes an instrument to process a single reading. Note that with a 10 V change in V_S , the charge transferred (Q_{IN}) is only 10 fC, a reduction of $10^5:1$.

Example: To illustrate the effects of guarding, a 1.3 V voltage source (V_S) with an internal resistance of 100 G Ω (R_S) was connected to an electrometer voltmeter using a 2 m triax cable (C_{SHUNT}). Voltage measurements of this high impedance voltage source were taken by an electrometer with and without the use of guarding. The graph in **Figure 2-10** indicates the measured voltage as a function of time showing both cases. Without guarding, the voltage measurements took about nine seconds to settle. With guarding, the voltage measurements settled within about two seconds. Thus, guarding drastically reduced the effects of the shunt capacitance on timing.

FIGURE 2-10: High Impedance Electrometer Voltage Measurements Taken With and Without Guarding



2.2.2 Insulation Resistance of the Test Circuit

Electrometers and some SMUs as voltmeters are characterized by high input resistance. High resistance insulation in the test circuits is one of the first requirements of making successful electrometer measurements. Thus, a knowledge of the various types of insulating materials and how to apply them properly is important. To measure voltages from high resistance sources accurately, the insulation leakage resistance of the test fixtures, test leads, and measuring voltmeter must be several orders of magnitude higher than the Thevenin equivalent resistance of the circuit under test, depending on the number of decades of precision, resolution, or accuracy required. If the insulation resistances aren't decades higher,

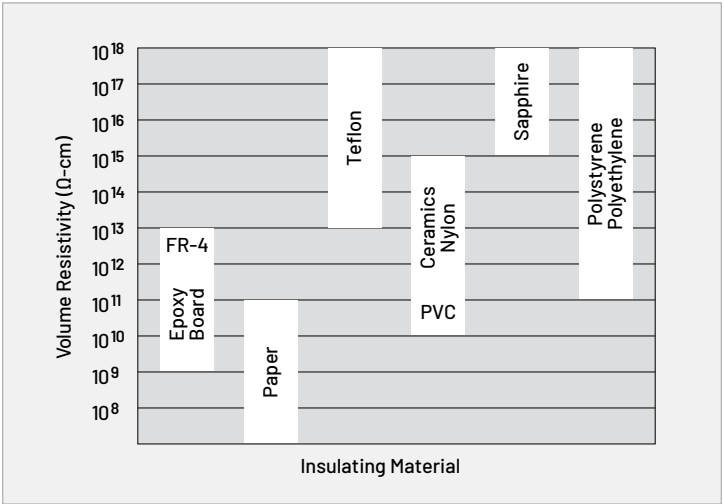
TABLE 2-2: Properties of Various Insulating Materials

Material	Volume Resistivity (Ohm-cm)	Resistance to Water Absorption	Minimal Piezoelectric Effects ¹	Minimal Triboelectric Effects	Minimal Dielectric Absorption
Teflon® PTFE	>10 ¹⁸	+	-	-	+
Sapphire	>10 ¹⁸	+	+	0	+
Polyethylene	10 ¹⁶	0	+	0	+
Polystyrene	>10 ¹⁶	0	0	-	+
Kel-F®	>10 ¹⁸	+	0	-	0
Ceramic	10 ¹⁴ -10 ¹⁵	-	0	+	+
Nylon	10 ¹³ -10 ¹⁴	-	0	-	-
Glass Epoxy	10 ¹³	-	0	-	-
PVC	5×10 ¹³	+	0	0	-

KEY: + Material very good in regard to the property.
0 Material moderately good in regard to the property.
- Material weak in regard to the property.

¹ Stored charge effects in non-piezoelectric insulators.

FIGURE 2-11: Approximate Resistivity of Various Insulating Materials



the shunting effects of the insulation will reduce the source voltage being measured, as discussed previously.

Detecting inferior insulation in test setups is difficult because the erroneous reading can appear well-behaved and steady. Therefore, it's prudent to measure the insulation resistance of the test fixtures and cables

periodically with an electrometer ohmmeter to ensure their integrity. If deficiencies are discovered, either cleaning or replacement of the defective insulator is in order.

Choosing the Best Insulator

In evaluating an insulating material, consider these material properties:

- **Volume resistivity:** leakage of current directly through the material.
- **Surface resistivity:** leakage across the surface, a function primarily of surface contaminants.
- **Water absorption:** leakage dependent on the amount of water that has been absorbed by the insulator.
- **Piezoelectric or stored charge effects:** the creation of charge unbalances (and thus current flow or voltage shift) due to mechanical stress.
- **Triboelectric effects:** the creation of charge unbalance due to frictional effects when materials rub against each other.
- **Dielectric absorption:** the tendency of an insulator to store/release charge over long periods.

Table 2-2 summarizes important characteristics of insulators. **Figure 2-11** shows their resistivity ranges. Insulator characteristics are described further in the following paragraphs.

Teflon®

Teflon is the most satisfactory and commonly used insulator for the impedance levels encountered in measurements of currents greater than 10^{-14} A. It has high volume resistivity and water vapor films don't form readily on its surface. Its insulating properties, therefore, aren't severely impaired by humid air. Teflon is chemically inert, is easily machined, and can be readily cleaned. Teflon PTFE is the type of Teflon most commonly used in electronics.

Teflon's principal shortcoming is that charges appear internally when it's deformed, causing spurious voltages and currents. With ordinary care, however, these characteristics aren't serious for currents greater than 10^{-13} A.

Polystyrene

Polystyrene is much less expensive than Teflon, and was the general purpose standard before Teflon was available. It machines easily, but internal crazing often develops. This characteristic doesn't impair its insulating properties unless the cracks reach the surface. The volume resistivity of polystyrene is similar to that of Teflon, but water vapor films form on its surface when humidity becomes high, significantly reducing its surface resistance.

Kel-F®

Kel-F has volume and surface characteristics nearly as good as Teflon, it machines easily, and it doesn't craze.

Polyethylene

Polyethylene has excellent volume resistivity and surface characteristics similar to polystyrene. Because it's flexible, it's used extensively for insulating coaxial and triaxial cable. These cables are excellent for general-purpose electrometer work because the surface leakage in this application is relatively unimportant. However, polyethylene melts at a relatively low temperature, so leads into ovens should use Teflon insulation rather than polyethylene.

Glass and Ceramics

Glass and ceramics also have high volume resistivity, but poor surface properties at high humidity and often-poor piezoelectric properties. Glass or ceramic cleaned with methanol and dipped in boiling paraffin has a good, but not durable, insulating surface. Various silicone varnishes can also be baked or air-dried onto glass or ceramic surfaces, but even after this treatment, handling can easily spoil the insulators. Glass and ceramics are difficult to machine, although they can be molded. They are used principally when their mechanical properties are mandatory.

Sapphire

Sapphire is one of the best insulators. Very little charge is generated in it when it's stressed mechanically. It's used most often in measuring currents in the 10^{-18} A to 10^{-15} A range. The use of sapphire is restricted by its cost and because the material is difficult to machine and form.

Quartz

Quartz has properties similar to sapphire, but considerably higher piezoelectric output, so it's rarely used in electrometer circuits.

Other Insulating Materials

Practically all other insulating materials have unacceptably low volume resistivity or unsatisfactory surface characteristics for high impedance measurements. Vinyl, nylon, and Lucite® are markedly inferior to Teflon, polystyrene, polyethylene, sapphire, or quartz.

Keeping Insulators Clean

As with any high resistance device, mishandling can destroy the integrity of insulators. Oils and salts from the skin can degrade insulator performance, and contaminants in the air can be deposited on the insulator surface, reducing its resistance. Therefore, insulator handling should be minimized; under no circumstances should the insulator be touched with the hand or with any material that might contaminate the surface.

If the insulator becomes contaminated, either through careless handling or from deposits, it can be cleaned with a degreaser to remove oils, and de-ionized water to remove ionic contamination. After cleaning, the insulator should be allowed to dry for several hours at low humidity.

2.3 Low Current Measurements

A number of error sources can have serious impacts on low current measurement accuracy. For example, the ammeter may cause measurement errors if not connected properly. (Refer to Sections 2.6.1 and 2.6.2 for more information on how to make properly shielded connections.) The ammeter's voltage burden and input bias current may also affect measurement accuracy. The source resistance of the device under test will affect the noise performance of a feedback ammeter. External sources of error can include leakage current from cables and fixtures, as well as currents generated by triboelectric or piezoelectric effects. Section 2.3 addresses low current measurement considerations in detail and outlines methods for minimizing the effects of error sources. It also includes information on using the electrometer's coulomb function to make very low current measurements.

2.3.1 Leakage Currents and Guarding

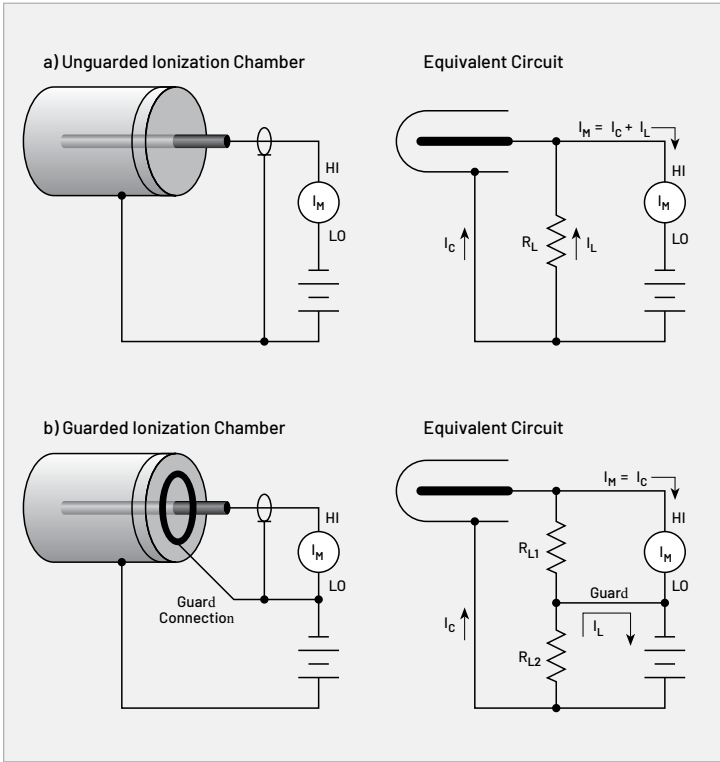
Leakage currents are generated by stray resistance paths between the measurement circuit and nearby voltage sources. Leakage current is an error current that flows (leaks) through insulation resistance when a voltage is applied. It generally becomes an issue when the impedance of the device under test is comparable to that of the insulators in the test circuit. This current can degrade the accuracy of low current measurements considerably. To reduce leakage currents, use good quality insulators, reduce the level of humidity in the test environment, and use guarding. Guarding will also reduce the effect of shunt capacitance in the measurement circuit.

Using good quality insulators when building the test circuit is one way to reduce leakage currents. Teflon, polyethylene, and sapphire are examples of good quality insulators, but avoid materials like phenolics and nylon. Humidity will affect insulator performance and degrade low current measurements. Different types of insulators will absorb varying amounts of water from the air, so it's best to choose an insulator on which water vapor doesn't readily form a continuous film. Refer to Section 2.2.2 for further discussion on choosing the best insulating materials.

Humidity also affects the sample being measured. If the material to be measured absorbs water easily, it's best to make the measurements in an environmentally controlled room. In some cases, either the insulator or the sample may have ionic contaminants, which can generate a spurious current, especially in high humidity.

Guarding is a very effective way to reduce leakage currents. A guard is a low impedance point in the circuit that's at nearly the same potential as the

FIGURE 2-12: Guarding as Applied to an Ionization Chamber



high impedance lead being guarded. The guard on the electrometer ammeter and picoammeter differs from the guard on the SMU ammeter. The use of guarding can best be explained through examples.

The Use of Guarding Using an Electrometer Ammeter or Picoammeter

The guard terminal of the electrometer ammeter or picoammeter is the LO input terminal. The guard can be used to isolate the high impedance input lead of the ammeter from leakage current due to voltage sources. **Figures 2-12** and **2-13** illustrate examples of guarding.

Figure 2-12 illustrates guarding as applied to measuring the ion current (I_C) from an ionization chamber. An unguarded ionization chamber and the corresponding equivalent circuit are shown in **Figure 2-12a**. The equivalent circuit shows that the full bias voltage appears across the insulator leakage resistance (R_L), therefore, a leakage current (I_L) will be added to the measured ion current ($I_M = I_C + I_L$). The leakage resistance is due to the insulator of the ionization chamber and the coax cable.

In **Figure 2-12b**, a metal guard ring is added to the ionization chamber. This guard circuit splits the leakage resistance into two parts. The voltage across R_{L1} is the picoammeter voltage burden, normally less than one millivolt, so the resulting current will be quite small. The full bias voltage appears across R_{L2} . A leakage current will flow around this loop, but won't affect the measurement, because it doesn't flow through the ammeter.

Guarding may also be necessary to prevent leakage current due to test fixturing. **Figure 2-13** shows a high mega-ohm resistor (R_{DUT}) supported on two insulators mounted in a metal test fixture.

FIGURE 2-13: Guarding to Reduce Leakage Currents

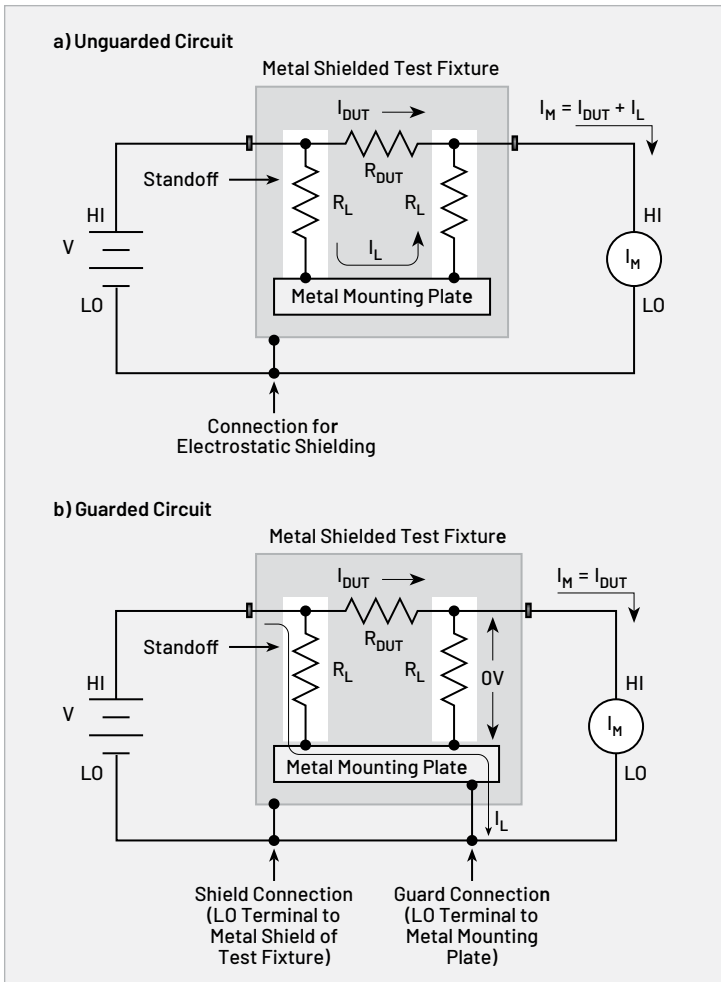
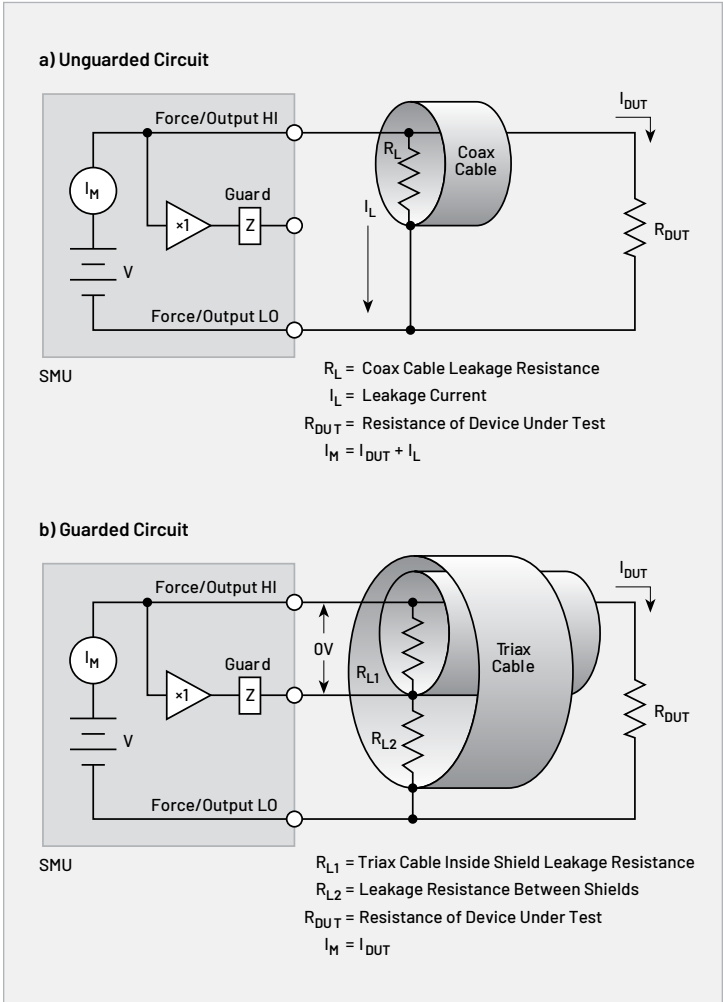


Figure 2-13a is the unguarded circuit. The leakage current (I_L) through the stand-off insulators will be added to the measured current (I_M).

As illustrated in **Figure 2-13b**, this circuit is guarded by connecting the LO of the picoammeter (I_M) to the metal mounting plate. This will put the bottom of the right insulator at almost the same potential as the top. The voltage difference is equal to the voltage burden of the picoammeter. The voltage burden is small, less than 200 μV . The top and bottom of the insulator are at nearly the same potential, so no significant current will flow

FIGURE 2-14: Guarding the Leakage Resistance of a Cable with an SMU



through it, and nearly all the current from the device under test will flow through the ammeter as desired.

The Use of Guarding with an SMU Ammeter

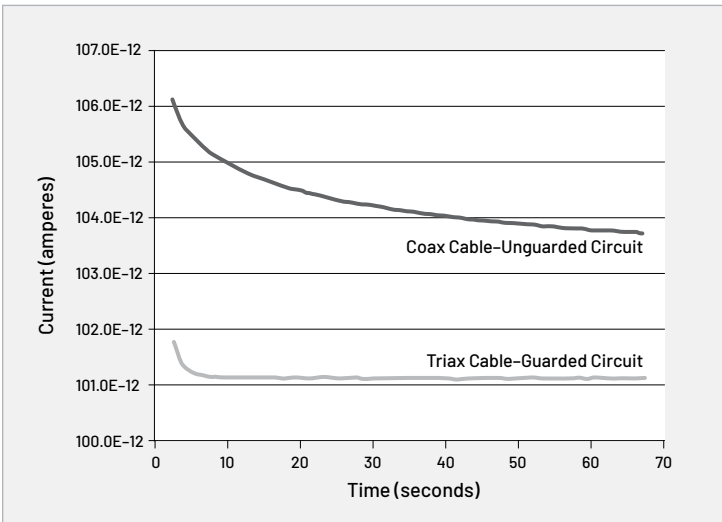
The guard terminal of an SMU is usually the inside shield of the triax connector. This guard is driven by a unity-gain, low impedance amplifier. By definition, the guard terminal is nearly at the same potential as the high impedance terminal, so the guard terminal will be at the same potential as the magnitude of the voltage source.

Figure 2-14 illustrates how a driven guard prevents the leakage resistance of a cable from degrading the low current measurements. In the unguarded circuit of **Figure 2-14a**, the leakage resistance of the coax cable is in parallel with the DUT (R_{DUT}), creating an unwanted leakage current (I_L). This leakage current will degrade very low current measurements.

In the guarded circuit shown in **Figure 2-14b**, the inside shield of the triax cable is connected to the guard terminal of the SMU. Now this shield is driven by a unity-gain, low impedance amplifier (Guard). The difference in potential between the Force/Output HI terminal and the Guard terminal is nearly 0 V, so the leakage current (I_L) is eliminated.

The dramatic difference between using coax (unguarded) vs. triax (guarded) cabling for low current SMU measurements is shown in **Figure 2-15**. Both of these curves are the result of sourcing 10 V to a 100 G Ω

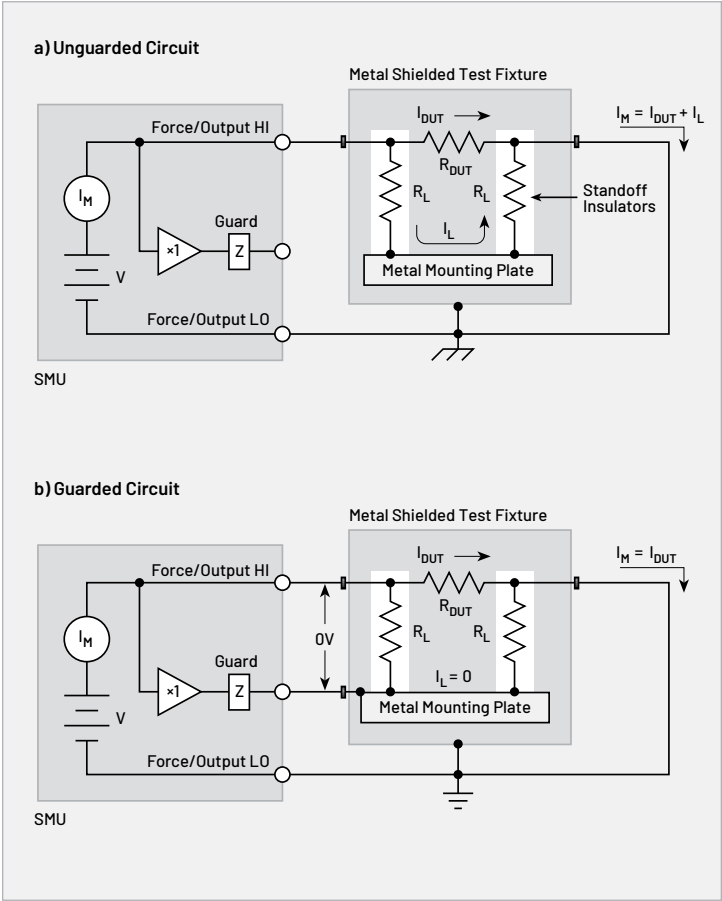
FIGURE 2-15: Effects of Guarded and Unguarded Circuit When Using an SMU to Measure a 100 G Ω Resistor by Sourcing 10 V and Reading Current as a Function of Time



resistor and measuring the current as a function of time. The current measurements generated using the coax cable show picoamps of leakage current added to the measurement. In addition, the unguarded circuit measurements do not appear to be settled, even after 70 seconds. Using the triax cable (guarding) reduces the effective cable capacitance and thus decreases the RC time constant, or settling time, of the measurement.

Figure 2-16 shows how the guard can eliminate the leakage current that may flow through the stand-off insulators in a test fixture. In **Figure 2-16a**, leakage current (I_L) flows through the stand-off insulators (R_L). This leakage current is added to the current from the DUT (I_{DUT}) and is measured by the SMU ammeter (I_M), adversely affecting the accuracy of the low current measurement.

FIGURE 2-16: Test Fixture Guarding with an SMU



In **Figure 2-16b**, the metal mounting plate is connected to the guard terminal of the SMU. The voltages at the top and the bottom of the stand-off insulator are nearly at the same potential (0 V drop), so no leakage current will flow through the standoffs and affect the measurement accuracy. For safety purposes, the metal shield must be connected to earth ground because the metal mounting plate will be at the guard potential.

2.3.2 Noise and Source Impedance

Noise can seriously affect sensitive current measurements. This section discusses how source resistance and source capacitance affect noise performance.

Source Resistance

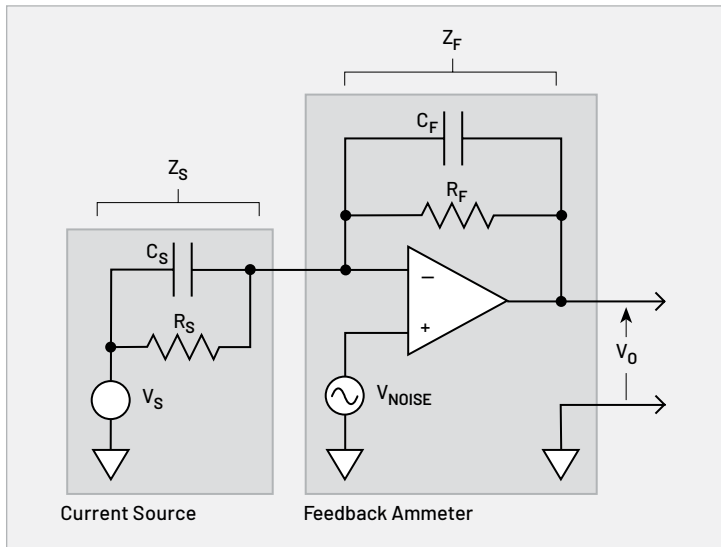
The source resistance of the DUT will affect the noise performance of a feedback ammeter. As the source resistance is reduced, the noise gain of the ammeter will increase.

Figure 2-17 shows a simplified model of a feedback ammeter. R_S and C_S represent the source resistance and source capacitance, V_S is the source voltage, and V_{NOISE} is the noise voltage of the ammeter. Finally, R_F and C_F are the feedback resistance and capacitance respectively.

The noise gain of the circuit can be given by the following equation:

$$\text{Output } V_{\text{NOISE}} = \text{Input } V_{\text{NOISE}} (1 + R_F/R_S)$$

FIGURE 2-17: Simplified Model of a Feedback Ammeter



Note that as R_S decreases in value, the output noise increases. For example, when $R_F = R_S$, the input noise is multiplied by a factor of two. Too low a source resistance can have a detrimental effect on noise performance, so there are usually minimum recommended source resistance values based on the measurement range. **Table 2-3** summarizes minimum recommended source resistance values for various measurement ranges for a typical feedback ammeter. Note that the recommended source resistance varies by measurement range because the R_F value also depends on the measurement range. Refer to the instruction manual for the instrument to be used for the appropriate minimum recommended source resistances.

TABLE 2-3: Minimum Recommended Source Resistance Values for a Typical Feedback Ammeter

Range	Minimum Recommended Source Resistance
pA	1 G Ω
nA	1 M Ω
μ A	1 k Ω
mA	1 Ω

Source Capacitance

DUT source capacitance will also affect the noise performance of a feedback type ammeter. In general, as source capacitance increases, so does the noise gain.

To see how changes in source capacitance can affect noise gain, let's again refer to the simplified ammeter model in **Figure 2-17**. The elements of interest for this discussion are the source capacitance (C_S) and the feedback capacitance (C_F). Taking into account the capacitive reactance of these two elements, our previous noise gain formula must be modified as follows:

$$\text{Output } V_{\text{NOISE}} = \text{Input } V_{\text{NOISE}} \left(1 + \frac{Z_F}{Z_S} \right)$$

Here, Z_F represents the feedback impedance made up of C_F and R_F , and Z_S is the source impedance formed by R_S and C_S . Furthermore,

$$Z_F = \frac{R_F}{\sqrt{(2\pi f R_F C_F)^2 + 1}}$$

and

$$Z_S = \frac{R_S}{\sqrt{(2\pi f R_S C_S)^2 + 1}}$$

Note that as C_S increases in value, Z_S decreases in value, thereby increasing the noise gain. Again, at the point where $Z_S = Z_F$, the input noise is amplified by a factor of two.

Most instruments using feedback ammeters for low current measurements will have a maximum recommended value for C_S . Although it is usually possible to measure at higher source capacitance values by inserting a resistor in series with the ammeter input, remember that any series resistance will increase the voltage burden by a factor of $I_{IN} \cdot R_{SERIES}$. Any series resistance will also increase the RC time constant of the measurement. A series diode, or two diodes in parallel back-to-back, can serve as a useful alternative to a series resistor for this purpose. The diodes can be small-signal types and should be in a light-tight enclosure. See Section 4.3.1 for a further discussion of the use of a series diode.

2.3.3 Zero Drift

Zero drift is a gradual change of the indicated zero offset with no input signal. Unless it's corrected by "zeroing," the resulting offset produces an error by adding to the input signal. Drift is normally specified as a function of time and/or temperature. Zero offset over a time period and temperature range will stay within the specified limits. Offset due to step changes in temperatures may exceed the specification before settling. Typical room temperature rates of change ($1^\circ\text{C}/15$ minutes) won't usually cause overshoot.

Most electrometers include a means to correct for zero drift. ZERO CHECK is used to configure most electrometers and picoammeters to display any internal voltage offsets. This feature allows fast checking and adjustment of the amplifier zero. Typically, the instrument is zero corrected while zero check is enabled. This procedure may need to be performed periodically, depending on ambient conditions. Electrometers perform this function with the touch of a button or upon command from the computer.

In a picoammeter or electrometer ammeter, note that ZERO CHECK and ZERO CORRECT functions are used to correct for internal voltage offsets. SUPPRESS or REL controls are used to correct for external current offsets. For optimum accuracy, zero the instrument on the range to be used for measurement. Refer to Section 2.3.4 for a discussion of correcting for internal bias current.

2.3.4 Generated Currents

Any extraneous generated currents in the test system will add to the desired current, causing errors. Currents can be internally generated, as in the case of instrument input bias current, or they can come from external sources such as insulators and cables. The following paragraphs discuss the various types of generated currents.

Figure 2-18 summarizes the magnitudes of a number of generated currents discussed in this section.

Internal Error Sources

The ideal ammeter should read zero when its input terminals are left open. Practical ammeters, however, do have some small current that flows when the input is open. This "zero" current measurement error comes from mul-

FIGURE 2-18: Typical Magnitudes of Generated Currents

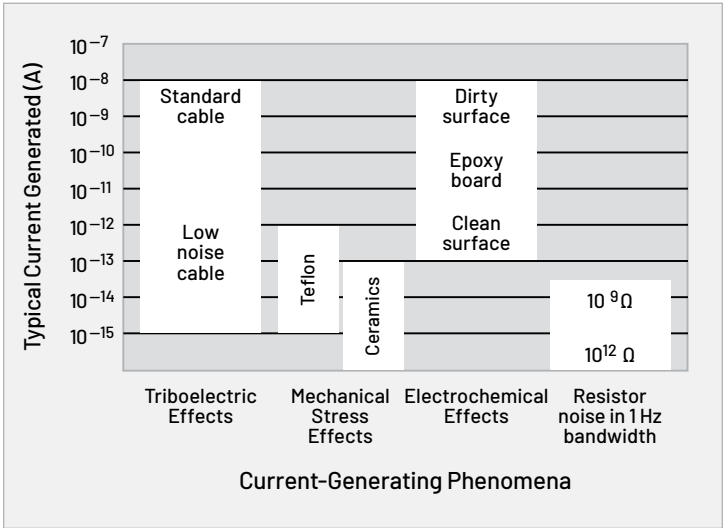
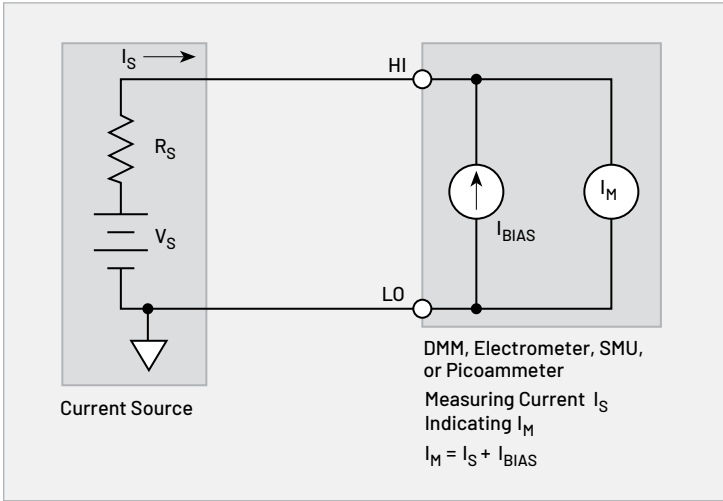


FIGURE 2-19: Effects of Input Bias Current on Current Measurement Accuracy



multiple sources; however, the main contributor on the lower current ranges is the input bias current. This current is caused by bias currents of active devices as well as by leakage currents through insulators within the instrument. Current errors can also be caused by voltage offset drifts due to time, temperature, etc., as well as other errors that are more obvious on the

higher current ranges. The error currents generated within picoammeters, electrometers, and SMUs are included in the instrument's specifications.

Figure 2-19 shows the input bias current (I_{BIAS}) adding to the source current (I_S) so the meter measures the sum of the two currents (I_M):

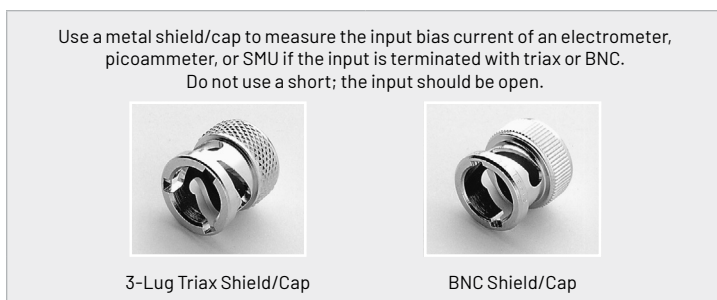
$$I_M = I_S + I_{\text{BIAS}}$$

However, depending on a particular instrument, the input bias current can add or subtract from the measurement.

The input bias current of an instrument can be measured and compared to the instrument's specifications. Here is a procedure for measuring the input bias current for electrometers, picoammeters, and SMUs.

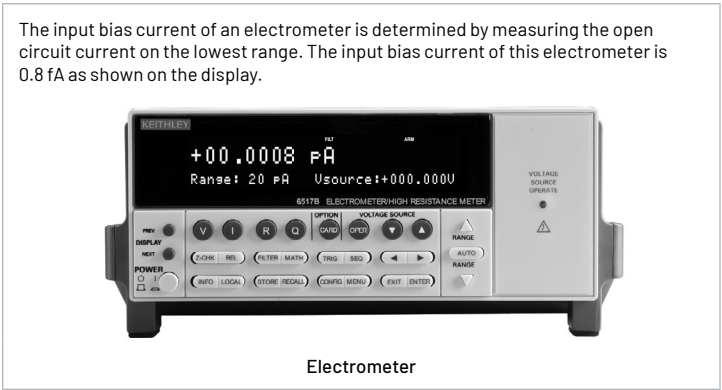
1. Turn on the instrument and allow it to warm up for 1 hour in an environment with stable temperature within the specified operating limits.
2. Place a metal cap on the input terminal to avoid noise pickup. This protective shield/cap is usually included with the sensitive instrument. Both triax and BNC metal caps are shown in **Figure 2-20**.

FIGURE 2-20: Metal Caps for Measuring Input Bias Current



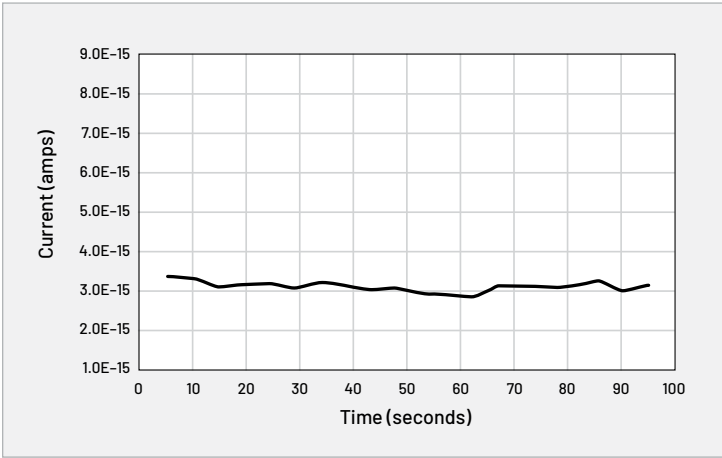
3. Select the lowest current range.
4. Allow more than five minutes for the reading to settle and then take a reading by observing the instrument's display. **Figure 2-21** shows the input bias current measurement of an electrometer with a capped input. On this particular instrument, the input bias current reads 8×10^{-16} or 0.8 fA.
5. Verify the reading is within the instrument's specification. If not, the instrument may need to be recalibrated.
6. The offset can be subtracted from measurements by using the relative, or zero, function of the ammeter. With the instrument open-circuited, allow the reading to settle and then enable the REL or ZERO function. Once the REL value is established, subsequent readings will be the difference between the actual input value and the REL value. Most instruments will enable the user to perform the REL either from the front panel or remotely.

FIGURE 2-21: Measuring the Input Bias Current of an Electrometer from the Front Panel



In some cases it may be necessary to determine the input bias current graphically. If this is the case, take a few readings as a function of time and observe the readings on a graph of current vs. time. **Figure 2-22** shows the open circuit current measurements as a function of a time for an SMU.

FIGURE 2-22: Input Bias Current Measurements as a Function of Time for an SMU



External Offset Current

External offset currents can be generated by ionic contamination in the insulators connected to the ammeter. Offset currents can also be generated externally from such sources as triboelectric and piezoelectric effects. As shown in **Figure 2-23**, the external offset current (I_{OFFSET}) also adds to the source current (I_S), and the meter again measures the sum of the two.

FIGURE 2-23: Effects of External Offset Current on Current Measurement Accuracy

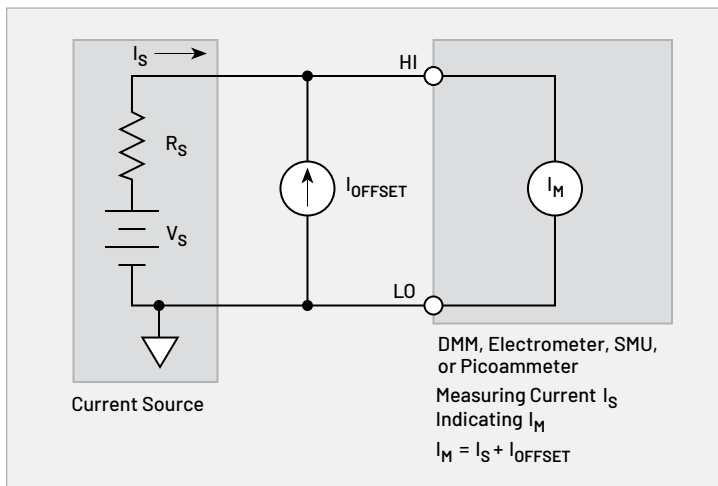
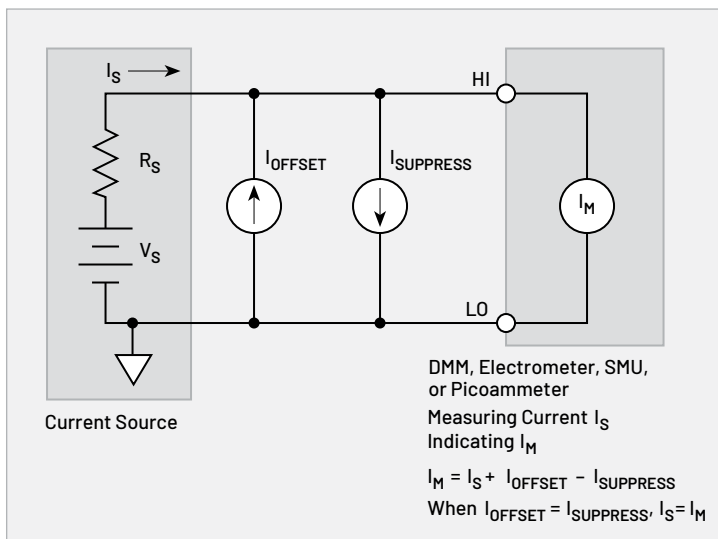


FIGURE 2-24: Using External Current Source to Suppress Offset Current



External offset currents can be suppressed with the current suppression feature (if available) of the instrument or they can be nulled by using a suitably stable and quiet external current source (I_{SUPPRESS}), as shown in **Figure 2-24**. With this arrangement, the current measured by the meter is:

$$I_M = I_S + I_{\text{OFFSET}} - I_{\text{SUPPRESS}}$$

Assuming I_{OFFSET} and I_{SUPPRESS} are equal in magnitude but opposite in polarity,

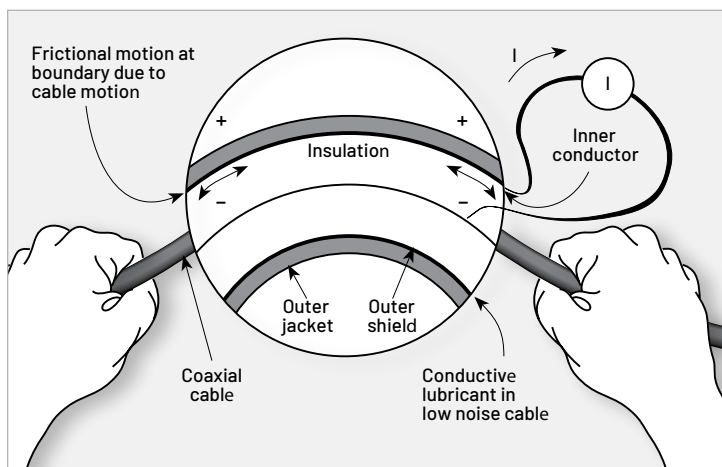
$$I_M = I_S$$

The advantage of using an external current source is that I_{OFFSET} can be as large or larger than the full-range value, and only $I_{\text{OFFSET}} - I_{\text{SUPPRESS}}$ need be small.

Triboelectric Effects

Triboelectric currents are generated by charges created between a conductor and an insulator due to friction. Here, free electrons rub off the conductor and create a charge imbalance that causes the current flow. A typical example would be electrical currents generated by insulators and conductors rubbing together in a coaxial cable, as shown in **Figure 2-25**.

FIGURE 2-25: Triboelectric Effect



"Low noise" cable greatly reduces this effect. It typically uses an inner insulator of polyethylene coated with graphite underneath the outer shield. The graphite provides lubrication and a conducting equipotential cylinder to equalize charges and minimize charge generated by frictional effects of cable movement. However, even low noise cable creates some noise when subjected to vibration and expansion or contraction, so all connections should be kept short, away from temperature changes (which would create thermal expansion forces), and preferably supported by taping or tying the cable to a non-vibrating surface such as a wall, bench, or other rigid structure.

There are a variety of other solutions to movement and vibration problems:

- Removal or mechanical decoupling of the source of vibration. Motors, pumps, and other electromechanical devices are the usual sources.
- Stabilization of the test hookup. Securely mount or tie down electronic components, wires, and cables. Shielding should be sturdy.

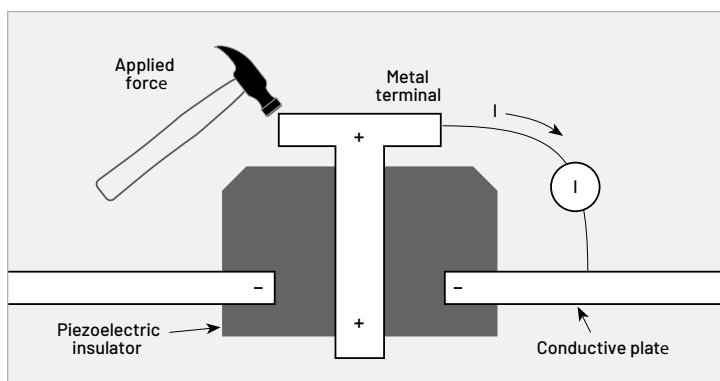
Triboelectric effects can also occur in other insulators and conductors that touch each other. Therefore, it's important to minimize contact between insulators as well as conductors in constructing test fixtures and connections for low current and high impedance.

Table 2-2 in Section 2.2.2 summarizes the triboelectric effects of various insulating materials.

Piezoelectric and Stored Charge Effects

Piezoelectric currents are generated when mechanical stress is applied to certain crystalline materials when used for insulated terminals and interconnecting hardware. In some plastics, pockets of stored charge cause the material to behave in a manner similar to piezoelectric materials. An example of a terminal with a piezoelectric insulator is shown in **Figure 2-26**.

FIGURE 2-26: Piezoelectric Effect

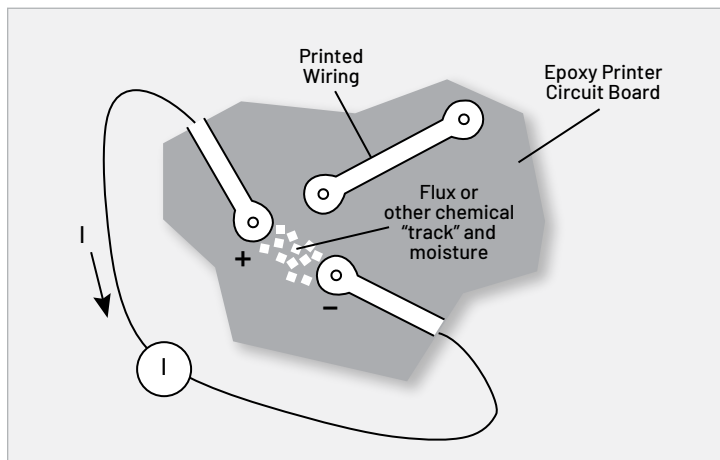


To minimize the current due to this effect, it's important to remove mechanical stresses from the insulator and use insulating materials with minimal piezoelectric and stored charge effects. Section 2.2.2 and **Table 2-2** summarize the piezoelectric properties of various insulating materials.

This effect is independent of the capacitance change between the plate and terminals. Charges are moved around, resulting in current flow.

In practice, it may be quite difficult to distinguish stored charge effects (in insulators) from piezoelectric effects. Regardless of the phenomenon involved, it's important to choose good insulating materials and make connecting structures as rigid as possible.

FIGURE 2-27: Electrochemical Effects



Contamination and Humidity

Error currents also arise from electrochemical effects when ionic chemicals create weak batteries between two conductors on a circuit board. For example, commonly used epoxy printed circuit boards, when not thoroughly cleaned of etching solution, flux or other contamination, can generate currents of a few nanoamps between conductors (see **Figure 2-27**).

Insulation resistance can be dramatically reduced by high humidity or ionic contamination. High humidity conditions occur with condensation or water absorption; ionic contamination may be the result of body oils, salts, or solder flux.

Although the primary result of these contaminants is the reduction of insulation resistance, the combination of high humidity and ionic contamination can form a conductive path or they may even act as an electrochemical cell with high series resistance. A cell formed in this manner can source picoamps or nanoamps of current for long periods of time.

To avoid the effects of contamination and humidity, select insulators that resist water absorption, and keep test environment humidity to moderate levels. Also, be sure all insulators are kept clean and free of contamination.

If insulators become contaminated, apply a cleaning agent such as methanol to all interconnecting circuitry. It's important to flush away all contaminants once they're dissolved in the solvent, so they won't be redeposited. Use only very pure solvents for cleaning; lower grades may contain contaminants that leave an electrochemical film.

Dielectric Absorption

Dielectric absorption in an insulator can occur when a voltage across that insulator causes positive and negative charges within the insulator to polarize because various polar molecules relax at different rates. When the voltage is removed, the separated charges generate a decaying current through circuits connected to the insulator as they recombine.

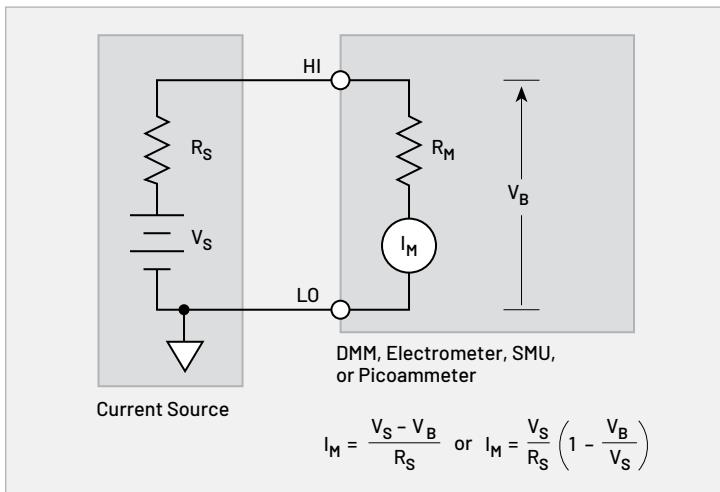
To minimize the effects of dielectric absorption on current measurements, avoid applying voltages greater than a few volts to insulators being used for sensitive current measurements. In cases where this practice is unavoidable, it may take minutes or even hours in some cases for the current caused by dielectric absorption to dissipate.

Table 2-2 in Section 2.2.2 summarizes the relative dielectric absorption of various insulating materials.

2.3.5 Voltage Burden

An ammeter may be represented by an ideal ammeter (I_M) with zero internal resistance, in series with a resistance (R_M), as shown in **Figure 2-28**. When a current source whose Thevenin equivalent circuit is a voltage (V_S) in series with a source resistance (R_S) is connected to the input of the ammeter, the current is reduced from what it would be with the ideal ammeter ($R_M = 0 \Omega$). This reduction is caused by the internal resistance (R_M), which creates an additional voltage drop called the voltage burden (V_B).

FIGURE 2-28: Effects of Voltage Burden on Current Measurement Accuracy



The voltage burden is specified for a full-scale input. Therefore, the voltage burden at a given current can be calculated by:

$$V_{B(I)} = V_B \left(\frac{I_S}{I_{FS}} \right)$$

where I_{FS} is full-scale current and I_S is the magnitude of the current source.

Taking into account the voltage burden, the measurement error can be calculated as follows:

$$I_M = \frac{V_S - V_B \left(\frac{I_S}{I_{FS}} \right)}{R_S}$$

The percent error in the measured reading due to voltage burden is:

$$\% \text{ error} = \frac{V_B \left(\frac{I_S}{I_{FS}} \right)}{V_S} \times 100\%$$

Example: In this circuit, $V_S = 0.7 \text{ V}$, $I_S = 100 \text{ } \mu\text{A}$, and $I_{FS} = 200 \text{ } \mu\text{A}$. Assuming $R_S = 10 \text{ k}\Omega$ and the voltage burden at full scale is 200 mV :

$$I_M = \frac{0.7 \text{ V} - 0.2 \text{ V} \left(\frac{100 \text{ } \mu\text{A}}{200 \text{ } \mu\text{A}} \right)}{10 \text{ k}\Omega} = 60 \text{ } \mu\text{A}$$

compared to the ideal case,

$$I_M = \frac{0.7 \text{ V}}{10 \text{ k}\Omega} = 70 \text{ } \mu\text{A}$$

Thus, the ammeter reading is $60 \text{ } \mu\text{A}$ vs. the ideal case of $70 \text{ } \mu\text{A}$ —an error of 14%.

In comparison, if a picoammeter is used and the voltage burden is $200 \text{ } \mu\text{V}$:

$$I_M = \frac{0.7 \text{ V} - 0.0002 \text{ V} \left(\frac{100 \text{ } \mu\text{A}}{200 \text{ } \mu\text{A}} \right)}{10 \text{ k}\Omega} = 69.99 \text{ } \mu\text{A}$$

Thus, the picoammeter reading is $69.99 \text{ } \mu\text{A}$ vs. the ideal measurement of $70 \text{ } \mu\text{A}$ —an error of only 0.01%.

On a particular current range, the input resistance of the ammeter can be related to the voltage burden specification and the full scale current on that range. The input resistance of a feedback picoammeter or electrometer ammeter is less than the ratio of the specified voltage burden to the full-scale current:

$$\text{Input Resistance} < \frac{\text{Voltage Burden}}{\text{Full-Scale Current}}$$

When determining the voltage burden of an SMU, the offset voltage on the voltage source range being used must be included. Therefore, it's best to use the lowest possible voltage source range in order to minimize error.

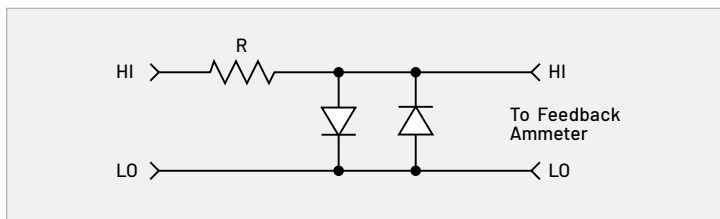
2.3.6 Overload Protection

Electrometers, picoammeters, and SMUs may be damaged if excessive voltage is applied to the input. Most instruments have a specification for the maximum allowable voltage input. In some applications, this maximum voltage may be unavoidably exceeded. Some of these applications may include leakage current of capacitors, reverse diode leakage, or insulation resistance of cables or connectors. If the component or material breaks down, all the voltage would be applied to the ammeter's input, possibly destroying it. In these cases, additional overload protection is required to avoid damaging the input circuitry of the instrument.

Electrometer or Picoammeter Overload Protection

Figure 2-29 shows a protection circuit for an electrometer ammeter or picoammeter, consisting of a resistor and two diodes (1N3595). The leakage of the 1N3595 diode is generally less than one pA even with 1 mV of forward bias, so the circuit won't interfere with measurements of 10 pA or more. This diode is rated to carry 225 mA (450 mA repeated surge). Because the voltage burden of the electrometer ammeter or picoammeter is less than 1 mV, the diodes won't conduct. With two diodes in parallel back to back, the circuit will provide protection regardless of the signal polarity.

FIGURE 2-29: Overload Protection Circuit for Electrometers and Picoammeters



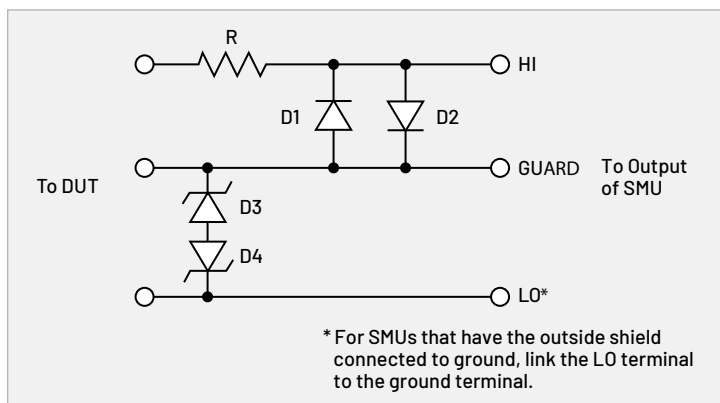
The resistor (R) must be large enough to limit the current through the diodes to prevent damage to the diodes. It also must be large enough to withstand the supply voltage. A good rule of thumb is to use a large enough resistor to cause a 1 V drop at the maximum current to be measured.

The protection circuit should be enclosed in a light-tight shield because the diodes are photosensitive. The shield should be connected to the low of the ammeter.

SMU Overload Protection (in Force Voltage, Measure Current Mode)

Figure 2-30 illustrates an overload protection circuit for an SMU in the ammeter mode. This circuit consists of two zener diodes (D3 and D4) connected between the Guard and LO (or Common) terminals, a current limiting

FIGURE 2-30: Overload Protection Circuit for the SMU in Force Voltage, Measure Current Mode



resistor(R) in series with the HI terminal, and two low leakage diodes(D1 and D2) between the HI and Guard terminals.

The two zener diodes are used to clamp the guard to LO (or the Common terminal). These should be rated slightly higher than the SMU's maximum measurable voltage. The leakage current through the zener diodes results in a voltage drop across the resistor, so low leakage zener devices are desirable.

The resistor (R) is used to limit the current through the diodes (D1 and D2). The resistance value should be large enough to limit the current flowing through the diodes to one-tenth of their forward current rating, thereby preventing diode damage. The resistor must also be rated high enough to meet the power dissipation requirements while the zeners are conducting.

If an overload occurs, one of the diodes (D1 or D2) will conduct and prevent the input from being damaged. The 1N3595 diode is a good choice for this function because it has low leakage current, typically less than 1 pA, even with a forward bias of 1 mV.

High impedance circuit construction, such as Teflon standoffs, must be used. The protection circuit should be built into a light-tight, metal-shielded enclosure with the shield connected to the LO terminal of the SMU.

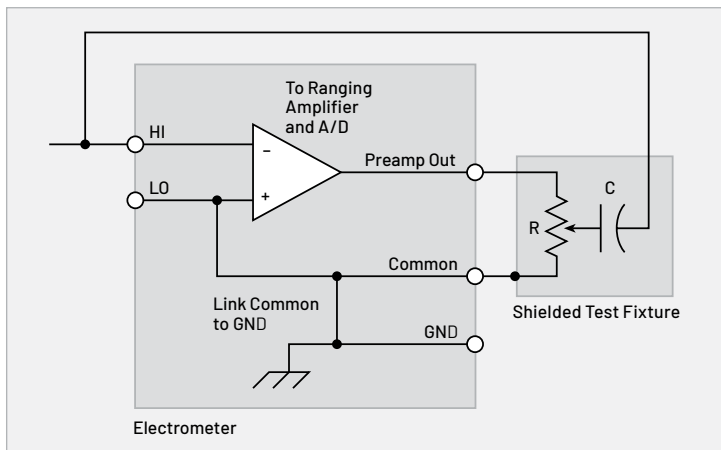
2.3.7 AC Interference and Damping

When measuring low current, electrostatic shielding (as discussed in Section 2.6.2) is the most common way to reduce noise due to AC interference. However, in some cases, shielding the device under test or the connecting cabling isn't practical. For these applications, a variable damping control may reduce the AC pickup enough to make meaningful measurements.

A damping circuit is a type of low pass filter that reduces the electrometer's AC response so the low DC current can be measured accurately. The damping circuit may already be built into the electrometer or may be an external circuit. Refer to the instrument's instruction manual for information on a particular electrometer's internal damping feature. However, it may be necessary to increase the damping with an external circuit.

Figure 2-31 illustrates an example of an external damping circuit. This circuit consists of a low leakage polystyrene or polyester capacitor (C) and a potentiometer (R). The potentiometer is connected between the preamp output and the common (or LO) terminal of the ammeter. The capacitor is connected between the HI input terminal of the ammeter and the moving arm of the potentiometer. The value of the capacitor depends on the current range of the ammeter. Higher ranges require the use of higher magnitude capacitors. However, typical values of the capacitor are in the range of hundreds of picofarads. The value of the potentiometer should be chosen to be high enough ($>50\text{ k}\Omega$) to avoid loading the preamp output, but still reduce noise effectively.

FIGURE 2-31: External Damping Circuit



Some experimentation will be needed to choose the best values for the capacitor and the resistance. Connect an oscilloscope to the analog output and observe the AC waveform on the scope. Adjust the potentiometer to make the AC signal as small as possible. If the noise can't be suppressed enough with the potentiometer, use a bigger capacitor.

The damping circuit should be built into a shielded enclosure.

2.3.8 Using a Coulombmeter to Measure Low Current

In most cases, an ammeter or picoammeter is used to measure current. However, for femtoamp-level currents, it may be better to use the cou-

lombs function of an electrometer to measure the change in charge over time, then use those charge measurements to determine the current. A further discussion of charge measurements can be found in Section 2.5.

Current Integration

The total charge (q) can be described as the integral of incremental charge elements (dq):

$$q = \int_{q_1}^{q_2} dq$$

Because current (i) is related to the charge by $dq = i dt$, the previous equation becomes:

$$q = \int_{t_1}^{t_2} i dt$$

One method for determining the charge is to measure the voltage drop across a capacitor of known value. The voltage on the capacitor is related to the charge in the following equation:

$$V = Q/C$$

where: Q = capacitor charge (coulombs)

C = capacitor value (farads)

V = voltage across the capacitor (volts)

Combining this equation with the previous ones yields:

$$Q = CV = \int_{q_1}^{q_2} dq = \int_{t_1}^{t_2} i dt$$

Once the rate of change (dt) is known, the current can easily be determined from a charge measurement. The instantaneous current (i) is simply:

$$i = \frac{dQ}{dt}$$

where the long-term average current is defined as:

$$I_{AVG} = \frac{\Delta Q}{\Delta t}$$

Thus, we can see that the current can be determined simply by making a series of charge measurements as a function of time.

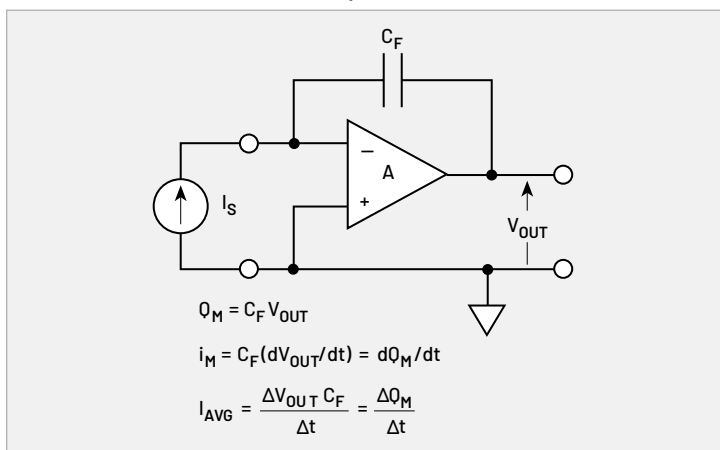
Using a Feedback Coulombmeter to Measure Current

Charge can be measured directly with a feedback coulombmeter. **Figure 2-32** shows a simplified model of a feedback type coulombmeter. The input current to the circuit is I_S , the output voltage is V_{OUT} , and the feedback capacitor is C_F .

The current (I_S) is applied to the input of the feedback coulombmeter. The circuit is an integrator, so the charge is determined by integrating the current:

$$Q_M = \int i dt$$

FIGURE 2-32: Feedback Coulombmeter Equivalent Circuit



The coulombmeter determines charge from the output voltage and the value of the feedback capacitor:

$$Q_M = C_F V_{OUT}$$

From the measured charge (Q_M), the user can calculate current:

$$i_M = C_F (dV_{OUT}/dt) = dQ_M/dt$$

The long-term average current (I_{AVG}) can be calculated from the change in output voltage over a specific time period:

$$I_{AVG} = \frac{\Delta V_{OUT} C_F}{\Delta t} = \frac{\Delta Q}{\Delta t}$$

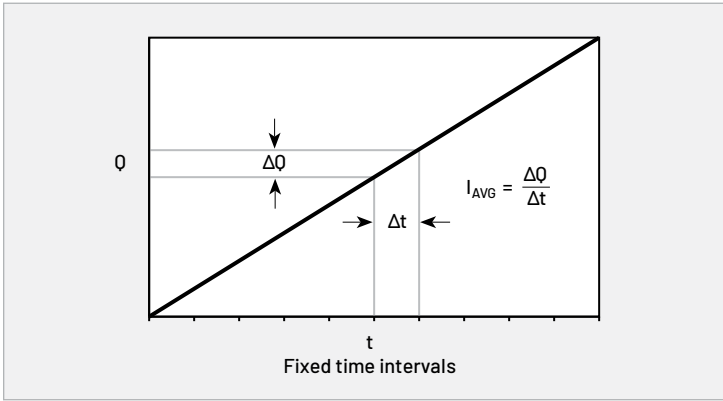
To make calculations easier, set a one-second measurement interval time in the one-shot trigger mode. The REL or zero function of the electrometer may be used to reset the readings. Some electrometers may have an auto-discharge feature to reset the charge reading to zero when it reaches a specified level.

Fixed Integration Time Period Method

The fixed integration time method shown in **Figure 2-33** can be used to determine current and is a variation of the feedback coulombmeter technique. In this instance, the increasing charge value is measured at specific time intervals of equal length. The average current (I_{AVG}) during a given period can be determined from the slope of the line and is calculated as follows:

$$I_{AVG} = \frac{\Delta Q}{\Delta t}$$

FIGURE 2-33: Fixed Integration Time Method of Determining Current from Charge



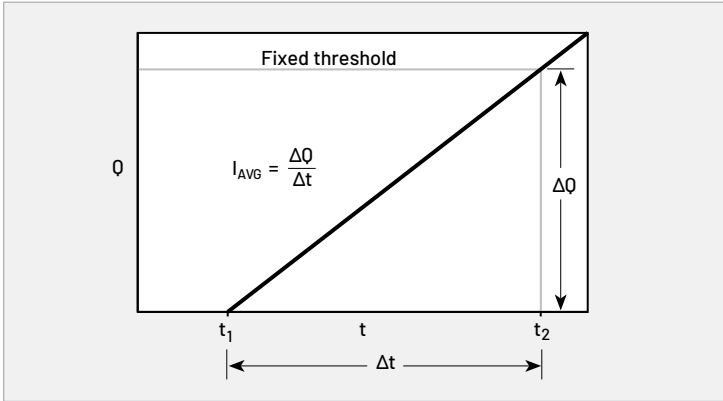
This method gives the average current during the time interval and produces readings at a steady rate determined by the integration period. This method can be accomplished automatically in software by determining the difference between successive readings.

Fixed Threshold Method

The fixed threshold method, which is shown in **Figure 2-34**, is somewhat similar to the fixed integration time method just described. In this case, however, the charge measurement begins at time t_1 and continues until the charge value reaches some predetermined threshold value at time t_2 . The current is then calculated as follows:

$$I_{AVG} = \frac{\Delta Q}{\Delta t} \text{ where } \Delta t = t_2 - t_1$$

FIGURE 2-34: Fixed Threshold Method of Determining Current from Charge



Note that the voltage coefficient of the coulombmeter capacitor has little effect on overall current measurement accuracy. As long as the threshold point and time periods are accurately known, current measurement accuracy will be quite good. However, readings won't be evenly spaced when current levels vary, and the interval between readings can be quite long when the average current for a given time period is small.

Advantages of Using a Coulombmeter to Measure Current

There are several advantages to using a coulombmeter instead of an ammeter for measuring current in certain situations:

- **Lower Current Noise:** The ammeter uses a feedback resistor, which will have significant Johnson noise. For charge measurement, this resistor is replaced by a capacitor, which theoretically has no Johnson noise. Consequently, the charge method of current measurement results in lower noise than measuring currents directly with a feedback ammeter. Thus, the charge method is preferable when current noise performance less than 1 fA p-p is required. (Refer to **Figure 2-58** in Section 2.6.5 and note that feedback resistances higher than $10^{12} \Omega$ aren't very practical.)
- **Faster Settling Times:** The speed of a feedback ammeter is limited by the time constant of its feedback circuit ($R_F C_F$). For example, for feedback resistances greater than 10 G Ω , stray capacitance limits response times to tens of milliseconds. In contrast, a feedback integrator will respond immediately and is limited only by the speed of the operational amplifier.
- **Random Pulses Can Be Integrated:** The average charge transferred per unit time of random pulse trains can be evaluated by integrating the current pulse train for a given period of time. The average current amplitudes can then be expressed as the total charge divided by the time period involved in the measurement. This technique is especially useful when averaging very small, unsteady currents. If the duty cycle is known, the pulse height can also be determined.
- **The Noise Effects of Input Shunt Capacitance are Minimized:** Noise gain is mainly determined by C_{IN}/C_F , and C_F is much larger in a coulombmeter than in an ammeter, so much larger input capacitance values can be tolerated. This characteristic is beneficial when measuring from high capacitance sources or when long connecting cables are used.

2.4 High Resistance Measurements

When resistances greater than 1 G Ω must be measured, an electrometer, SMU, or picoammeter/voltage source are usually required. An electrometer may measure high resistance by either the constant-voltage or the constant-current method. Some electrometers allow the user to choose either method. The constant-voltage method uses an ammeter and a voltage

source; the constant-current method uses an electrometer voltmeter and a current source. A description of these techniques follows.

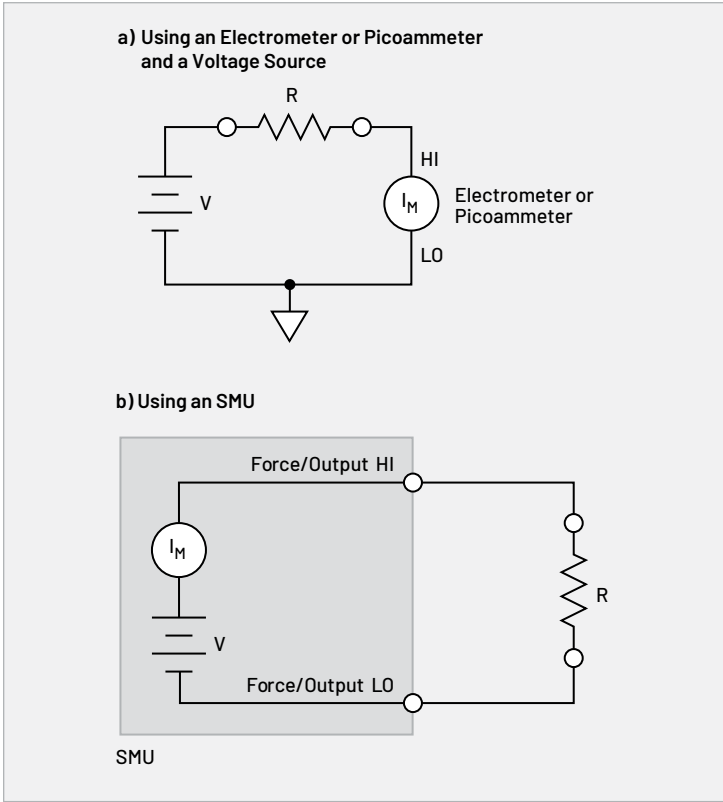
2.4.1 Constant-Voltage Method

To make high resistance measurements using the constant-voltage method, an instrument that can measure low current and a constant DC voltage source are required. Some electrometers and picoammeters have voltage sources built into the instrument and automatically can calculate the unknown resistance.

The basic configuration of the constant-voltage method using an electrometer or picoammeter is shown in **Figure 2-35a**. As shown in **Figure 2-35b**, an SMU can also be used for making high resistance measurements using the constant voltage method.

In this method, a constant voltage source (V) is placed in series with the unknown resistor (R) and an ammeter (I_M). The voltage drop across the

FIGURE 2-35: Constant-Voltage Method for Measuring High Resistance



ammeter is negligible, so essentially all the test voltage appears across R . The resulting current is measured by the ammeter and the resistance is calculated using Ohm's Law ($R = V/I$).

High resistance is often a function of the applied voltage, which makes the constant-voltage method preferable to the constant-current method. By testing at selected voltages, a resistance vs. voltage curve can be developed and a "voltage coefficient of resistance" can be determined.

Some of the applications that use this method include testing two-terminal high resistance devices, measuring insulation resistance, and determining the volume and surface resistivity of insulating materials. See Section 4 for descriptions of these applications.

The constant-voltage method requires measuring low current, so all the techniques and error sources described in Section 2.3 (Low Current Measurements) apply to this method. The two most common error sources when measuring high resistance are electrostatic interference and leakage current. As described in Section 2.6.2, electrostatic interference can be minimized by shielding the high impedance circuitry. Interferences due to leakage current can be controlled by guarding as described in Section 2.3.1.

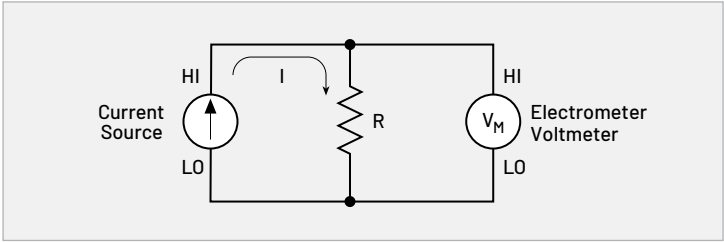
2.4.2 Constant-Current Method

High resistance measurements using the constant-current method may be made using either an electrometer voltmeter and current source or just an electrometer ohmmeter. An SMU that has a voltmeter with high input impedance and low current source ranges may also be used. Using the electrometer voltmeter with a separate current source or an SMU allows the user to make a four-wire measurement and to control the amount of current through the sample. The electrometer ohmmeter makes a two-wire resistance measurement at a specific test current, depending on the measurement range.

Using The Electrometer Voltmeter and an External Current Source

The basic configuration for the constant-current method is shown in **Figure 2-36**. Current from the source (I) flows through the unknown resistance (R) and the voltage drop is measured by the electrometer voltmeter (V_M). Using this method, resistances up to about $10^{12}\Omega$ can be measured. Even though the basic procedure seems simple enough, some precautionary measures must be taken. The input impedance of the voltmeter must be high enough compared with a source resistance to keep the loading error within acceptable limits. Typically, the input impedance of an electrometer voltmeter is about $10^{14}\Omega$. Also, the output resistance of the current source must be much greater than the unknown resistance for the measurement to be linear. The voltage across the sample depends upon the sample resistance, which makes it difficult to account for voltage coefficient when using the constant-current method. If voltage coefficient is a concern, it's best to use the constant-voltage method. When using the electrometer voltmeter to make high resistance measurements, all the techniques and error sources

FIGURE 2-36: Constant-Current Method Using a Separate Current Source and Electrometer Voltmeter



es described in Section 2.2 (Voltage Measurements from High Resistance Sources) apply to these measurements. The electrometer voltmeter and a separate current source are used when determining high resistivity of semiconductor materials using the four-point probe or van der Pauw technique. These methods of determining the resistivity of semiconductor materials are described in more detail in Section 4.4.3.

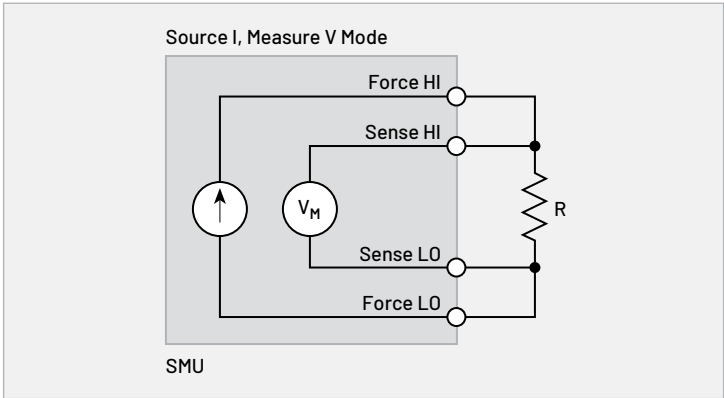
Using an SMU in the Source I, Measure V Mode

An SMU can measure high resistance in the source current/measure voltage mode by using either a two-wire (local sense) or four-wire (remote sense) method. **Figure 2-37** illustrates an SMU in four-wire mode.

The four-wire method is used to eliminate contact and lead resistance, which is especially important when measuring resistivity of semiconductor materials. These measurements usually involve measuring low voltages. The resistance of the metal probe to semiconductor contact can be quite high.

When using remote sense, the voltage difference between high force and high sense, and between low force and low sense is usually limited to

FIGURE 2-37: Using the SMU in the Four-Wire Mode to Measure High Resistance



a specified value. Exceeding this voltage difference can result in erratic measurements. Check the reference manual of the SMU for further information on this limitation.

In addition to the voltage drop limitation, some SMUs have automatic remote sensing resistors located between the HI Force and HI Sense terminals and between the LO Force and LO Sense terminals. This may further limit the use of a single SMU in remote mode for certain applications, such as semiconductor resistivity. If this is the case, use the SMU as a current source in the two-wire mode, and use a separate voltmeter(s) to measure the voltage difference. See Section 4.4.3 for further information.

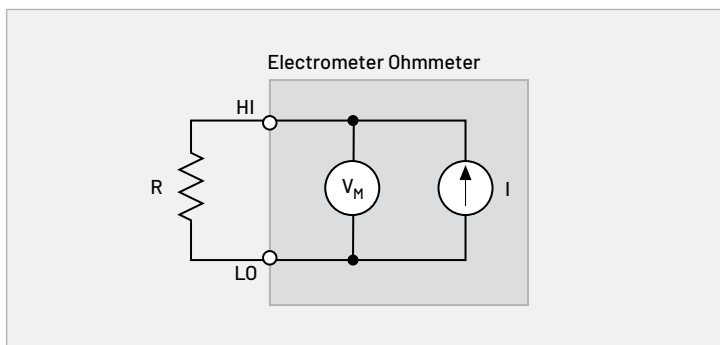
Using the Electrometer Ohmmeter

When using the electrometer ohmmeter, measurement accuracy can be affected by a variety of factors. In the following paragraphs, we will discuss the most important considerations for making accurate high resistance measurements.

Basic Configuration

Figure 2-38 shows the electrometer ohmmeter measuring a resistance (R). The ohmmeter uses an internal current source and electrometer voltmeter to make the measurement. It automatically calculates and displays the measured resistance. Notice that this is a two-wire resistance measurement compared to using the electrometer voltmeter and external current source, which can make a four-wire measurement. This is because the current source is internally connected to the voltmeter and cannot be used separately.

FIGURE 2-38: Electrometer Ohmmeter for Measuring High Resistance



Guarding

As with high impedance voltage measurements and current measurements, guarding high resistance test connections can significantly reduce the effects of leakage resistance and improve measurement accuracy.

Consider the unguarded resistance measurement setup shown in **Figure 2-39a**. Here, an electrometer ohmmeter is forcing a current (I_R) through the unknown resistance (R_S) and then measuring the voltage (V_M) across the DUT. If we assume that the meter has infinite input resistance, the measured resistance is then computed from Ohm's Law:

$$R_M = \frac{V_M}{I_R}$$

However, because the cable leakage resistance (R_L) is in parallel with R_S , the actual measured resistance (R_M) is reduced, as shown in the parallel equivalent circuit of **Figure 2-39b**. The measured resistance now becomes:

$$R_M = R_S \left(\frac{R_L}{R_S + R_L} \right)$$

The loading effects of cable resistance (and other leakage resistances) can be virtually eliminated by driving the cable shield with a unity-gain amplifier, as shown in **Figure 2-39c**. The voltage across R_L is essentially zero, so all the test current (I_R) now flows through R_S , and the source resistance value can be accurately determined. The leakage current (I_G) through the cable-to-ground leakage path (R_G) may be considerable, but that current is supplied by the low impedance output of the $\times 1$ amplifier rather than by the current source (I_R).

Settling Time

The settling time of the circuit is particularly important when making high resistance measurements. The settling time of the measurement is affected by the shunt capacitance, which is due to the connecting cable, test fixturing, and the DUT. As shown in **Figure 2-40**, the shunt capacitance (C_{SHUNT}) must be charged to the test voltage by the current (I_S). The time period required for charging the capacitor is determined by the RC time constant (one time constant, $\tau = R_S C_{SHUNT}$), and the familiar exponential curve of **Figure 2-41** results. Thus, it becomes necessary to wait four or five time constants to achieve an accurate reading. When measuring very high resistance values, the settling time can range up to minutes, depending on the amount of shunt capacitance in the test system. For example, if C_{SHUNT} is only 10 pF, a test resistance of 1 T Ω will result in a time constant of 10 seconds. Thus, a settling time of 50 seconds would be required for the reading to settle to within 1% of final value.

In order to minimize settling times when measuring high resistance values, keep shunt capacitance in the system to an absolute minimum by keeping connecting cables as short as possible. Also, guarding may be used to decrease settling times substantially. Finally, the source voltage, measure current method of resistance measurement is generally faster because of reduced settling times.

FIGURE 2-39a: Effects of Cable Resistance on High Resistance Measurements

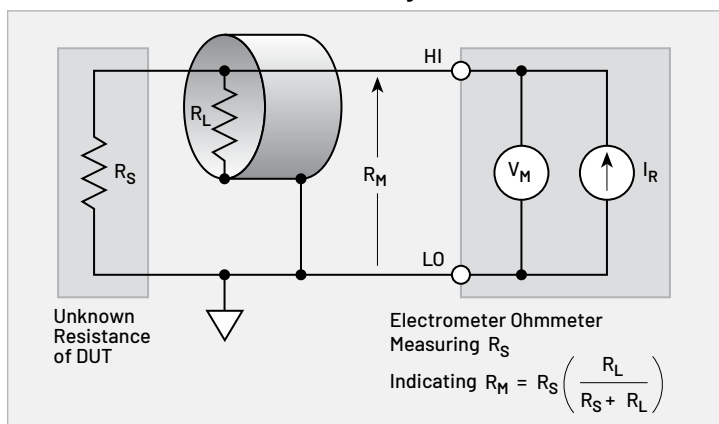


FIGURE 2-39b: Equivalent Circuit of Figure 2-39a Showing Loading Effect of Cable Leakage Resistance R_L .

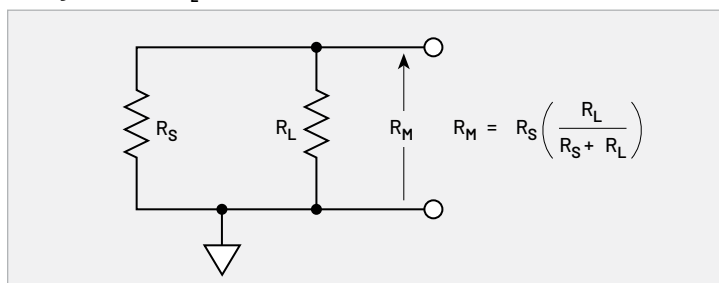


FIGURE 2-39c: Guarding Cable Shield to Eliminate Leakage Resistance

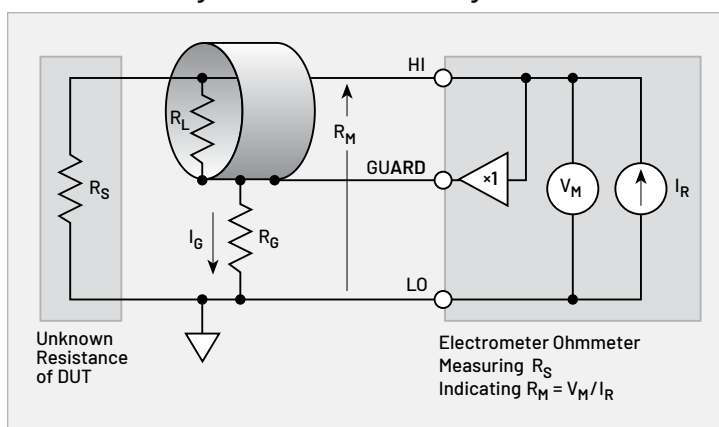


FIGURE 2-40: Settling Time is the Result of $R_S C_{SHUNT}$ Time Constant

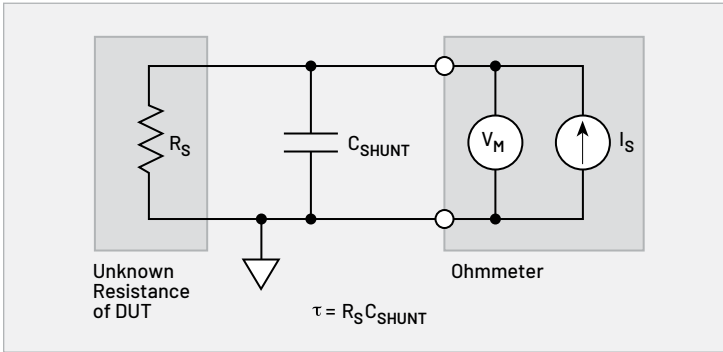
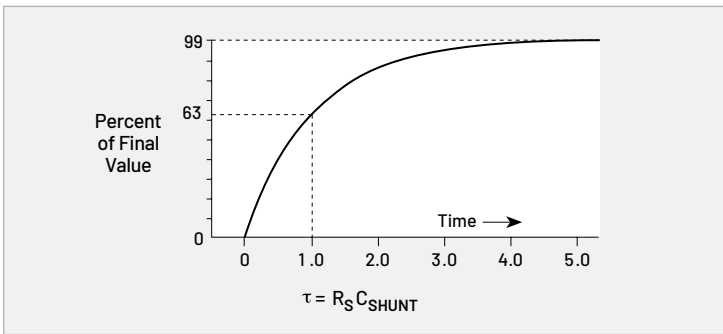


FIGURE 2-41: Exponential Settling Time Caused by Time Constant of Shunt Capacitance and Source Resistance



2.5 Charge Measurements

Charge is the time integral of current, $q = \int i dt$. Charge is often measured on a quantity of particles, on a surface, or on a component such as a capacitor. Sometimes, the charge is measured on a continuous basis, such as when using the coulombmeter to measure very low current, as discussed in Section 2.3.8.

An electrometer makes an ideal coulombmeter because it has very low input bias current and high input resistance. The coulombmeter function of the electrometer measures charge by integrating the input current. An integrating capacitor is used in the feedback loop of the input stage. Refer to Section 1.5.3 for a more detailed discussion of the coulombmeter circuit of the electrometer.

2.5.1 Error Sources

Charge measurements made with an electrometer are subject to a number of error sources, including input bias current, voltage burden, generated currents, and low source impedance.

Input Bias Current

With an electrometer, the input bias current is very low. However, at low charge levels, even this small current may be a significant error factor. Over long time periods, the instrument will integrate the bias current, which will be seen as a long-term drift in the charge measurement. Typical input bias current is 4 fA, which will cause a change in the charge measurement of 4 fC per second. If the input bias current is known, it's possible to compensate for this error simply by subtracting the charge drift due to input bias current from the actual reading. However, determining the offset current of the entire system may be difficult.

Voltage Burden

The voltage burden of a feedback coulombmeter is generally quite low (<100 μ V), just as it is with a feedback picoammeter. However, if the instantaneous peak current is >10 μ A, the voltage burden can exceed this level momentarily. In an overload condition, the voltage burden can reach many volts, depending on the input value.

If the source voltage is at least 10 mV, the typical electrometer in the coulombs mode will integrate the current accurately. If the source voltage is much lower, the voltage burden may become a problem, and the input stage noise will be amplified so much that accurate measurements aren't possible.

Generated Currents

Generated currents from the input cable or induced currents due to insufficient shielding can cause errors in charge measurements, especially with charge levels of 100 pC or less. To minimize generated currents, use low noise cable and electrostatically shield all connections and the DUT.

Source Impedance

The magnitude of the source impedance can affect the noise performance of the feedback coulombmeter. **Figure 2-42** shows a generalized feedback circuit connected to a source impedance. In a coulombmeter, the feedback impedance is a capacitor. From this diagram, the noise gain of the coulombmeter can be calculated from the following equation:

$$\text{Output Noise} = \text{Input Noise} \times (1 + Z_F / Z_S)$$

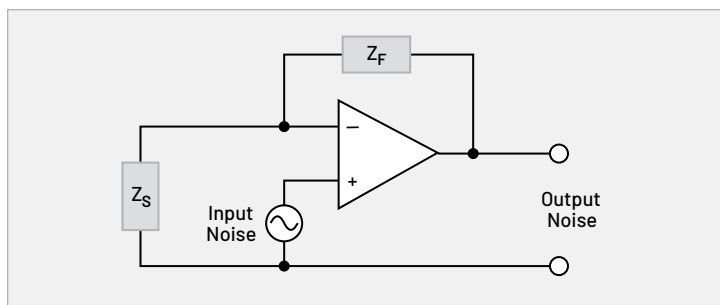
where: Z_S is the source impedance

Z_F is the feedback impedance of the coulombmeter

Input Noise is the noise of the input stage of the electrometer

In general, as Z_F becomes larger, the noise gain becomes larger. Refer to the electrometer's manual or specifications for the value of the feedback impedance for a particular instrument.

FIGURE 2-42: Generalized Feedback Circuit



2.5.2 Zero Check

Unlike a voltage measurement, a charge measurement can be a destructive measurement. In other words, the process of making the measurement may remove the charge stored in the device under test.

When measuring the charge on a device such as a capacitor, it's important to disable the zero check of the electrometer first, and then connect the capacitor to the high impedance input terminal. Otherwise, some of the charge will be lost through the zero check impedance and won't be measured by the electrometer. That's because when zero check is enabled, the input resistance of the electrometer is about 10 M Ω .

Opening the zero check internal switch will produce a sudden change in charge reading known as "zero hop." To eliminate the effects of zero hop, take a reading just after the zero check is disabled, then subtract this value from all subsequent readings. An easy way to do this is to enable the REL function after zero check is disabled, which nulls out the charge reading caused by the hop.

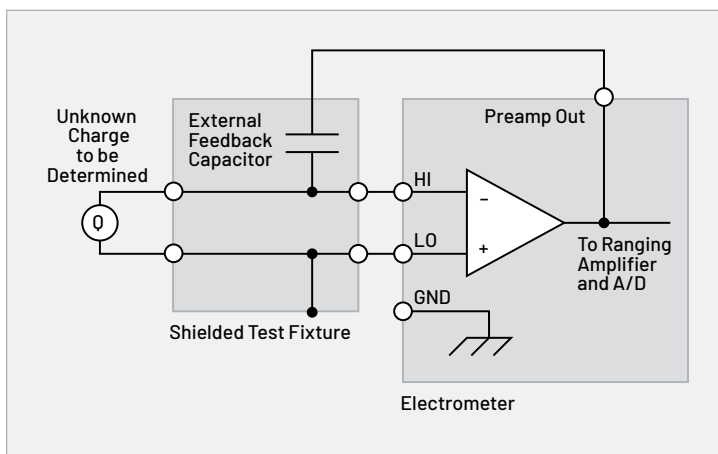
2.5.3 Extending the Charge Measurement Range of the Electrometer

The charge measurement range of most electrometers can be extended using external feedback. The external feedback mode allows an external device to be used as the feedback element of the electrometer. Placing the electrometer in the volts mode and then enabling external feedback switches the feedback circuit from an internal network to a feedback circuit connected to the preamp output.

To extend the coulombs ranges, an external capacitor is used as the feedback element.

As illustrated in **Figure 2-43**, an external feedback capacitor is placed between the preamp output terminal and the HI input terminal of the electrometer. To prevent electrostatic interference, the capacitor is placed in a shielded test fixture.

FIGURE 2-43: Connections for Using External Feedback Capacitor



When in the external feedback mode, the electrometer will display the voltage across the feedback element. The unknown charge can be calculated from the following formula:

$$Q = CV$$

where: Q = charge (coulombs)

C = capacitance of the external feedback capacitor (farads)

V = voltage on display of electrometer (volts)

For example, using an external feedback capacitor of $10\ \mu\text{F}$ and measuring $5\ \text{V}$ on the display of the electrometer, the calculated charge is $50\ \mu\text{C}$.

The capacitance of the feedback element should be at least $10\ \text{pF}$ to avoid errors due to stray capacitance and noise gain.

To ensure low leakage current and low dielectric absorption, the feedback capacitor should be made of a suitable dielectric material such as polystyrene, polypropylene, or Teflon.

More information on the measurement procedure can be found in the instruction manual of the electrometer.

2.6 General High Impedance Measurement Considerations

So far, we have discussed considerations specific to voltage, current, resistance, and charge measurements. The following paragraphs examine considerations that apply to all types of electrometer and SMU measurements on high resistance sources.

2.6.1 Making Connections

To avoid measurement errors, it's critical to make proper connections from the electrometer, SMU, or picoammeter to the device under test. **Always connect the high resistance terminal of the meter to the highest resistance point of the circuit under test.**

Figure 2-44 shows an electrometer connected to a current source that consists of a voltage source in series with a resistor. An AC powered source usually has a significant level (often several volts) of line frequency common mode voltage. As shown in **Figure 2-45**, this will cause a current (i) to flow through the low to ground capacitance of the electrometer (I_M). This circuit

FIGURE 2-44: Connecting the HI Terminal of the Ammeter to High Resistance

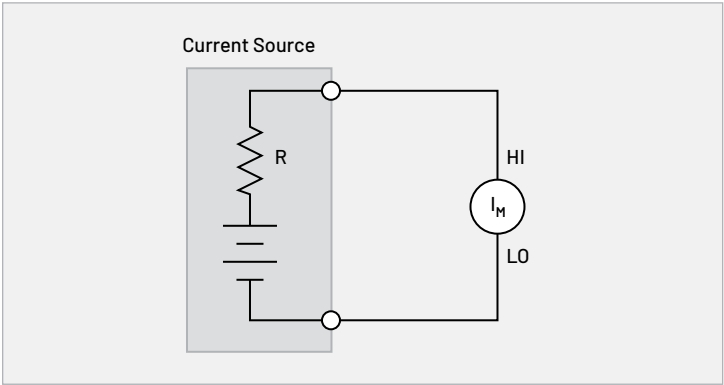


FIGURE 2-45: Proper Connection

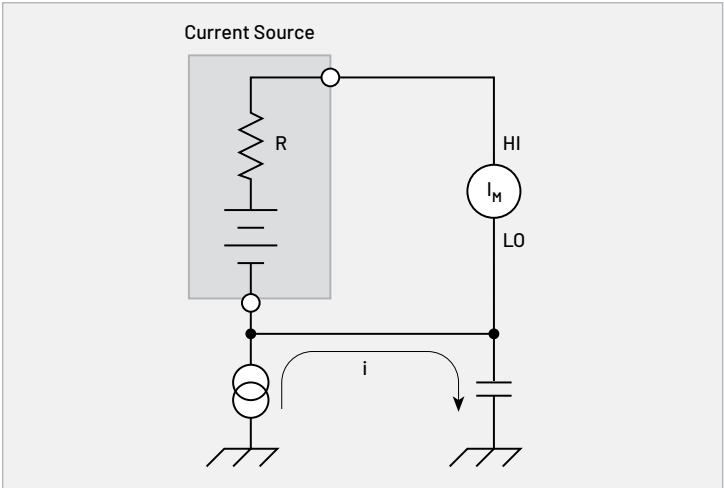
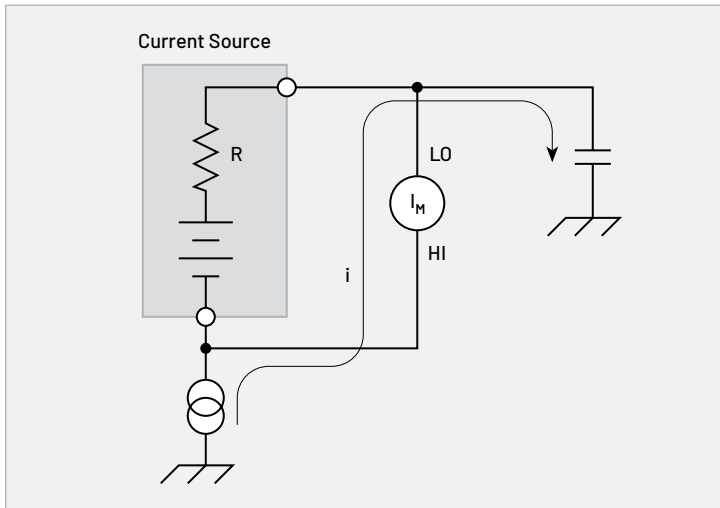


FIGURE 2-46: Improper Connection



is connected properly, so this current doesn't flow through the electrometer measurement circuitry and, therefore, doesn't cause any measurement errors. However, when the HI terminal of the electrometer is connected to the low impedance power supply, this AC current (i) flows through the electrometer (I_M), as illustrated in **Figure 2-46**. This current may affect the measurement accuracy, especially at low signal levels.

See Section 2.6.6 for details on appropriate cabling and connector types for electrometer measurements.

2.6.2 Electrostatic Interference and Shielding

Electrostatic coupling or interference occurs when an electrically charged object approaches the input circuit under test. At low impedance levels, the effects of the interference aren't noticeable because the charge dissipates rapidly. However, high resistance materials don't allow the charge to decay quickly, which may result in unstable measurements. Typically, electrostatic interference is an issue when making current measurements ≤ 1 nA or resistance measurements ≥ 1 G Ω . The erroneous readings may be due to either DC or AC electrostatic fields, so electrostatic shielding will help minimize the effects of these fields.

Electrostatic Interference

DC fields can produce noisy readings or undetected errors. These fields can be detected when movement near an experiment (such as the movement of the person operating the instrument or others in the immediate vicinity) causes fluctuations on the electrometer's display. To perform a quick check for interference, place a piece of charged plastic, such as a

comb, near the circuit. A large change in the meter reading indicates insufficient shielding.

AC fields can be equally troublesome. These are caused most often by power lines and RF fields. If the AC voltage at the input is large, part of this signal is rectified, producing an error in the DC signal being measured. This can be checked by observing the analog output of the electrometer or picoammeter with an oscilloscope. A clipped waveform indicates a need to improve electrostatic shielding. **Figure 2-47** illustrates a clipped waveform taken from the 2 V analog output of an electrometer. In this example, the amount of clipping reduced the DC current reading by nearly 50%.

FIGURE 2-47: Clipped Waveform from the Analog Output of an Electrometer Caused by AC Pickup

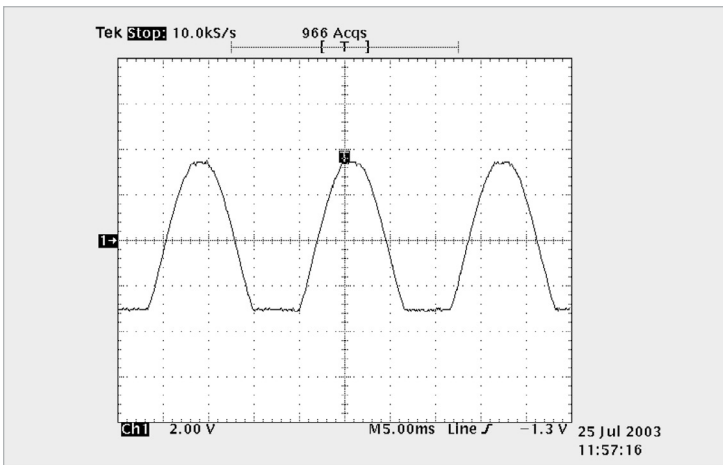
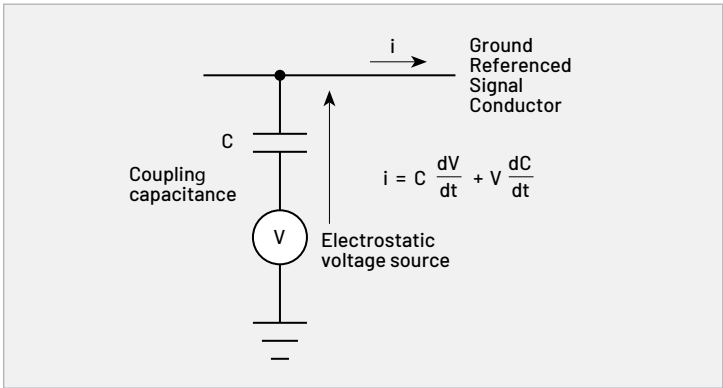


FIGURE 2-48: Electrostatic Coupling



For an SMU, check for AC pickup by connecting the oscilloscope between the guard terminal and common terminal.

Figure 2-48 shows an example of AC electrostatic coupling. An electrostatic voltage source in the vicinity of a conductor, such as a cable or trace on a PC board, generates a current proportional to the rate of change of the voltage and of the coupling capacitance. This current can be calculated with the following equation:

$$i = C \, dV/dt + V \, dC/dt$$

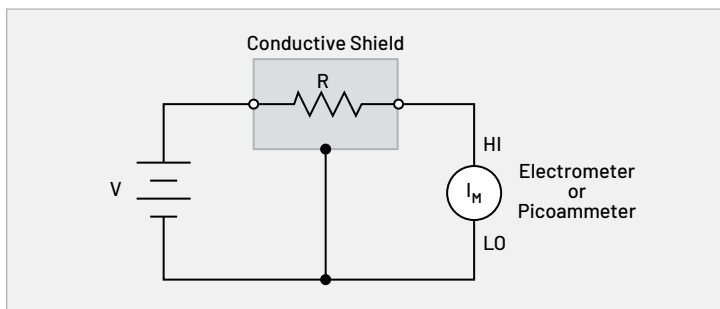
For example, two conductors, each with 1 cm² area and spaced 1 cm apart by air, will have almost 0.1 pF of capacitance. With a voltage difference of 100 V between the two conductors and a vibration causing a change of capacitance of 0.01 pF/second (a 10% fluctuation between them), a current of 1 pA AC will be generated.

Shielding

To reduce the effects of the fields, a shield can be built to enclose the circuit being measured. The easiest type of shield to make is a simple metal box or meshed screen that encloses the test circuit. Shielded boxes are also available commercially.

Figure 2-49 illustrates an example of shielding when making measurements using an electrometer or picoammeter. Made from a conductive material, the shield is always connected to the low impedance input of the electrometer or picoammeter or to the output LO (or common) terminal of the SMU. If circuit LO is floating above ground, observe special safety precautions to prevent anyone from touching the shield. These safety precautions are discussed in Section 2.6.8.

FIGURE 2-49: Shielding a High Impedance Device



The cabling between the HI terminal of the meter and the device under test also requires shielding. Capacitive coupling between an electrostatic noise source and the signal conductors or cables can be greatly reduced by surrounding those conductors with a metal shield connected to LO, as shown in **Figure 2-50**. With this shield in place, the noise current generated by the electrostatic voltage source and the coupling capacitance flows

FIGURE 2-50: Electrostatic Shielding

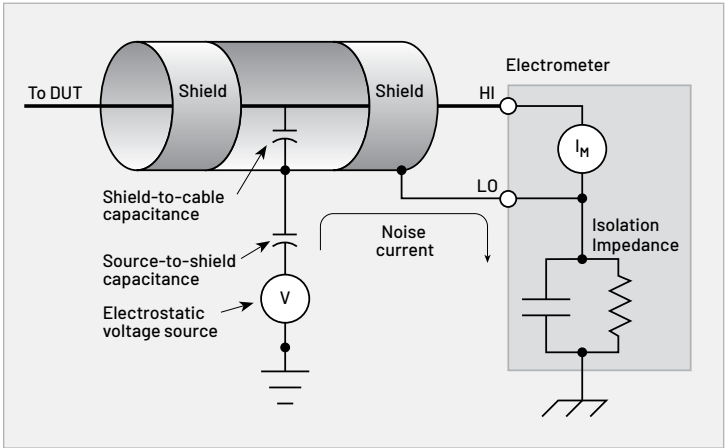
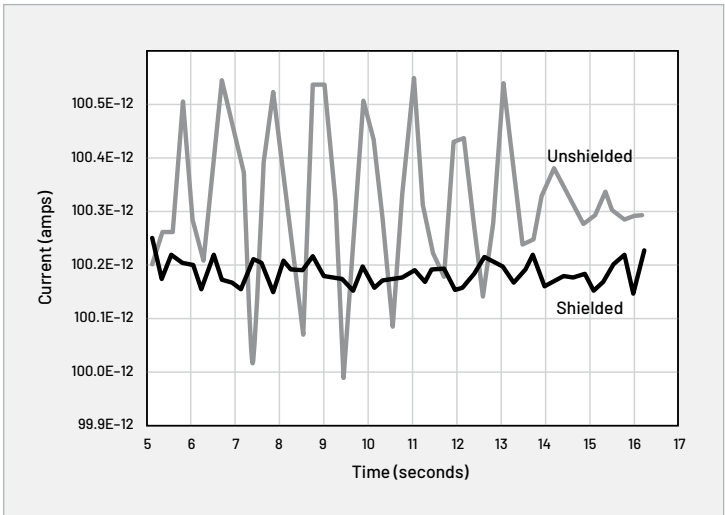


FIGURE 2-51: Shielded vs. Unshielded Current Measurements of 100 G Ω Resistor



through the shield to ground rather than through the HI terminal of the electrometer.

Figure 2-51 illustrates the dramatic difference between unshielded and shielded current measurements of a 100 G Ω resistor biased at 1 V. The unshielded measurements are much noisier than the shielded measurements.

To summarize, follow these guidelines to minimize error currents due to electrostatic coupling:

- Keep all charged objects (including people) and conductors away from sensitive areas of the test circuit.
- Avoid movement and vibration near the test area.
- Shield the device under test by surrounding it with a metal enclosure and connect the enclosure electrically to the test circuit common terminal.

Shielding vs. Guarding

Shielding usually implies the use of a metallic enclosure to prevent electrostatic interference from affecting a high impedance circuit. Guarding implies the use of an added low impedance conductor, maintained at the same potential as the high impedance circuit, which will intercept any interfering voltage or current. A guard doesn't necessarily provide shielding. Guarding is described further in Section 2.2.1 for voltmeters, Section 2.3.1 for ammeters, and Section 2.4.2 for ohmmeters.

2.6.3 Environmental Factors

A stable test environment is essential to making accurate low level measurements. This section addresses important environmental factors that may affect the accuracy of low level measurements.

Temperature and Temperature Stability

Varying temperatures can affect low level measurements in several ways, including causing thermal expansion or contraction of insulators and producing noise currents. Also, a temperature rise can cause an increase in the input bias current of the meter. As a general rule, JFET gate leakage current doubles for every 10°C increase in temperature, but most electrometers are temperature compensated to minimize input current variations over a wide temperature range.

To minimize errors due to temperature variations, operate the entire system in a thermally stable environment. Keep sensitive instruments away from hot locations (such as the top of a rack) and allow the complete system to achieve thermal stability before making measurements. Use the instrument's zero or suppress feature to null offsets once the system has achieved thermal stability. Repeat the zeroing process whenever the ambient temperature changes. To ensure optimum accuracy, zero the instrument on the same range as that to be used for the measurement.

Humidity

Excess humidity can reduce insulation resistance on PC boards and in test connection insulators. A reduction in insulation resistance can, of course, have a serious effect on high impedance measurements. In addition, humidity or moisture can combine with any contaminants present to create electrochemical effects that can produce offset currents.

To minimize the effects of moisture, reduce the humidity in the environment (ideally <50%). Be sure all components and connectors in the test

system are clean and free of contamination. When cleaning, use only pure solvents to dissolve oils and other contaminants, then rinse the cleaned area with fresh methanol or deionized water. Allow cleaned areas to dry for several hours before use.

Light

Some components such as diodes and transistors are excellent light detectors. Consequently, these components must be tested in a light-free environment. To ensure measurement accuracy, check the test fixture for light leaks at doors and door hinges, tubing entry points, and connectors or connector panels.

Ionization Interference

Current measurements made at very low levels (<100 fA) may be affected by ionization interference from sources such as alpha particles. A single alpha particle generates a track of from 30,000 to 70,000 positive and negative ions per cm, which may be polarized and moved about by ambient electric fields. Also, ions that strike a current-sensing node may generate a "charge hop" of about 10 fC per ion.

There are several ways to minimize noise in the test system due to ionization interference. First, minimize the volume of air inside the shield around sensitive input nodes. Also, keep sensitive nodes away from high intensity electric fields.

RFI (Radio Frequency Interference)

Interference from radio frequency sources can affect any sensitive electrometer measurement. This type of interference may be indicated by a sudden change in the reading for no apparent reason.

A non-linear device or junction in the input circuit can rectify the RF energy and cause significant errors. Sources of such RFI are nearby transmitters, contactors, solenoid valves, and even cell phones and portable two-way radios.

Once the source is identified, the RF energy may be reduced or eliminated by shielding and adding snubber networks or filters at appropriate points. Consult Section 3.2.5 for further discussion of RFI.

2.6.4 Speed Considerations

This section discusses speed considerations when making high impedance measurements. This includes an analysis of time and frequency relationships and the effects of input capacitance on rise time and noise.

Time and Frequency Relationships

Although this handbook stresses DC measurements, an analysis of noise and instrument response speed requires a brief discussion of time and frequency relationships in electronic circuits.

A steady-state DC signal applied to a voltmeter presents no conceptual difficulty. However, if the signal has a time-varying component such as an AC signal superimposed on the DC signal, the meter will tend to follow the varying signal and show the instantaneous magnitude of the input. As the frequency of the AC component increases, the DC meter response decreases, until at some frequency only the average input voltage will be displayed. The frequency at which the voltmeter's response to an AC signal drops to 70% is often denoted as the "3dB point" (f_{3dB}). Digital multimeters have a bandwidth of roughly half the conversion rate (readings per second) at the display. The analog output has a much wider bandwidth unless it's reconstructed from digital information.

Bandwidth describes the instrument's ability to respond to time varying signals over a range of frequencies. Another measure of the instrument's response is its ability to respond to a step function; the typical measure of response is the rise time of the instrument. Bandwidth or rise time may be used to describe the instrument's response to time-varying signals.

Rise time of an analog instrument (or analog output) is generally defined as the time necessary for the output to rise from 10% to 90% of the final value when the input signal rises instantaneously from zero to some fixed value. This relationship is shown in **Figure 2-52**. In **Figure 2-52a**, a step function with an assumed rise time of zero is shown; **Figure 2-52b** shows the instrument's response and the associated rise time. Rise time, frequency response, and the RC time constant of a first order system are related. The 3dB point is given by the relationship:

$$f_{3dB} = \frac{1}{2\pi RC}$$

Rise time (t_r) is related to the RC time constant as follows:

$$t_r = t_{90} - t_{10}$$

where: $t_{90} = 2.3RC$

$$t_{10} = 0.1RC$$

Thus, $t_r = 2.2RC$.

For example, the rise time of a circuit with a source resistance of 1 TΩ and capacitance of 100 pF will be approximately:

$$t_r = (2.2)(10^{12})(100 \times 10^{-12}) = 220 \text{ seconds}$$

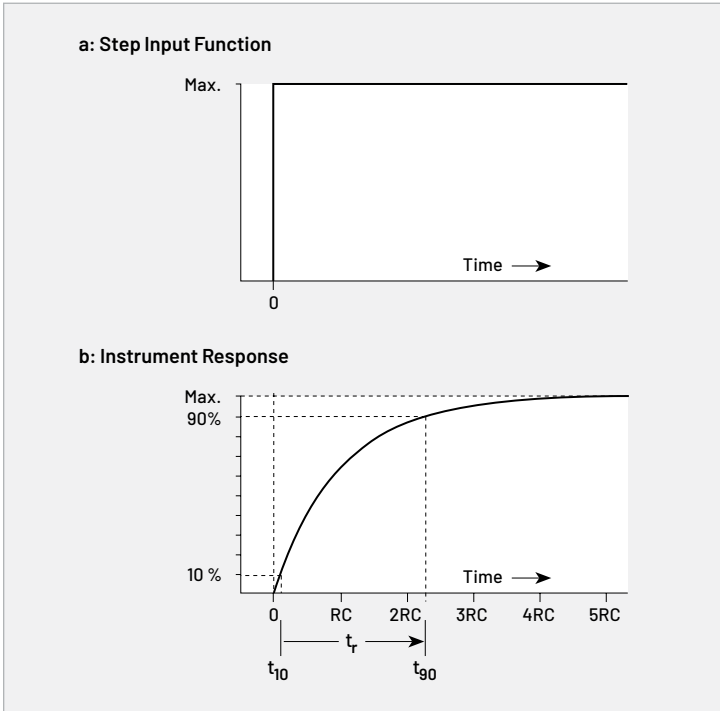
Using this with the above relationship between RC and f_{3dB} , we see that:

$$t_r = \frac{2.2}{2\pi f_{3dB}} \quad \text{or} \quad t_r = \frac{0.35}{f_{3dB}}$$

Thus, the 1 TΩ source resistance and 100 pF capacitance limit the bandwidth to:

$$f_{3dB} = \frac{0.35}{t_r} = \frac{0.35}{220} = 0.0016 \text{ Hz}$$

FIGURE 2-52: Instrument Response to Step Input



Rise time affects the accuracy of the measurement when it's of the same order of magnitude as the period of the measurement. If the length of time allowed before taking the reading is equal to the rise time, an error of approximately 10% will result, because the signal will have reached only 90% of its final value. To reduce the error, more time must be allowed. To reduce the error to 1%, about two rise times must be allowed, but reducing the error to 0.1% would require roughly three rise times (or nearly seven time constants).

Beyond the 0.1% error level (and occasionally the 1% level), second-order effects come into play. For example, more than four rise times are generally required to settle to within 0.01% of final value, due to dielectric absorption in insulators and other second-order effects.

In summary, an analog instrument's response (or the analog output response of most digital instruments) to a changing input signal is a function of its bandwidth, because frequency response and rise time are directly related. To ensure accurate measurements, sufficient settling time must be allowed for the source, the connection to the instrument, and the instrument itself to settle after the input signal is applied.

Effects of Input Capacitance on Rise Time and Noise

Voltage Measurements

In voltage measurements from high impedance sources (**Figure 2-53**), capacitance (C_{IN}) across the voltmeter (V_M) must be charged through R_S . The equation for the output voltage as a function of time is:

$$V_M = V_S(1 - e^{-t/R_S C_{IN}})$$

where: V_M = voltmeter reading at t seconds

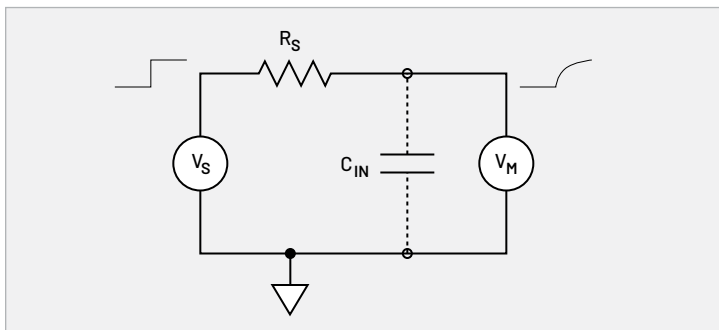
V_S = step function source

t = time in seconds after step occurs

R_S = equivalent series resistance in ohms

C_{IN} = equivalent shunt capacitance in farads
(instrument plus cable capacitance)

FIGURE 2-53: Shunt Capacitance Effect of High Impedance Voltage Measurement



Thus, the familiar exponential curve of **Figure 2-54** results, in which it becomes necessary to wait four or five time constants to achieve an accurate reading. In the case of large resistors and capacitance, the rise time can range up to minutes. Although increased shunt capacitance causes rise time to increase, it does filter out noise produced in the source and interconnecting cable simply by reducing the effective bandwidth of the voltmeter.

Shunt Current Measurements

The effects of input capacitance on current measurements using a shunt type ammeter (**Figure 2-55**) are similar to those for voltage measurements. A shunt ammeter can be modeled as a voltmeter with a resistor across the input. The circuit shows that the input capacitance (C_{IN}) must be charged to $I_S R_S$ volts, at an exponential rate of the $R_S C_{IN}$ time constant. Note that C_{IN} is the sum of the source, connecting cable, and meter capacitance.

FIGURE 2-54: Exponential Response to Step Input

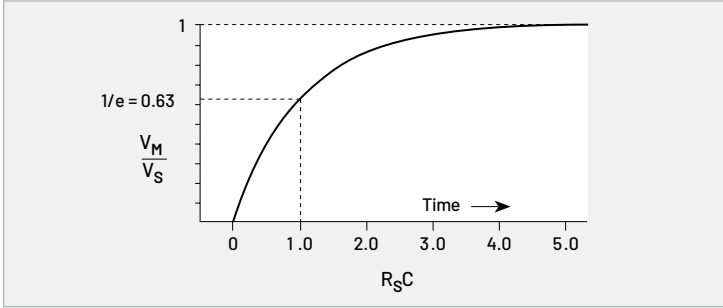
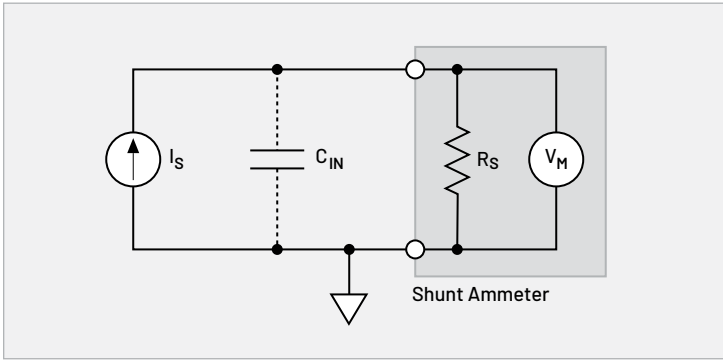


FIGURE 2-55: Shunt Type Ammeter



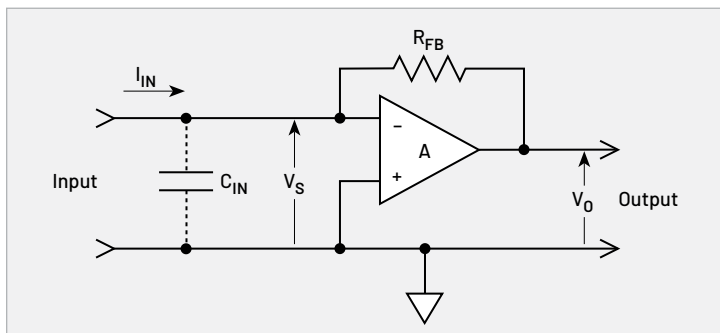
Feedback Current Measurements

The effect of input capacitance on current meters employing negative feedback is different than the effect on the shunt ammeter. The circuit for this mode is shown in **Figure 2-56**.

If A , the gain of the amplifier, is large, then $V_O = -I_{IN}R_{FB}$. In such an arrangement, C_{IN} doesn't shunt R_{FB} , and has only a fraction of the effect it would have with a shunt picoammeter. The resulting speed-up comes from the reduction of the input impedance of the picoammeter due to negative feedback. In other words, only $V_S = -V_O/A$ volts is developed across C_{IN} instead of the V_O that would occur in a shunt picoammeter. Thus, even large values of capacitance shunting the input will have negligible effect on rise time.

Rise time in a feedback picoammeter is a function of the physical or stray capacitance shunting the feedback resistance (R_{FB}). Electrometers, SMUs, and picoammeters can be used with relatively large values of source capacitance. It's important to realize that increasing values of input shunt capacitance (the parallel combination of source, cable and input capacitances) will degrade the signal-to-noise ratio of a given measurement.

FIGURE 2-56: Feedback Electrometer Ammeter

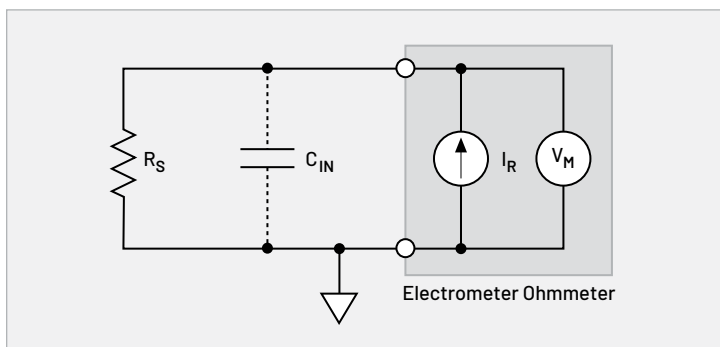


See Sections 2.3.2 and 4.3.1 for more information on noise and source impedance.

Resistance Measurements (Constant-Current Method)

Input capacitance (C_{IN}) also affects resistance measurements (**Figure 2-57**) in the same manner. Again, C_{IN} must be charged by the current (I_R), hence, the same equation applies. (See Section 2.4.2 for more information on the constant-current method.)

FIGURE 2-57: Constant-Current Resistance Measurement



Instrument Rise Time Summary

For most measurements of high resistance sources, rise time considerations require minimizing the capacitance shunting the meter input. Earlier, it was shown that doing so also minimizes noise gain. In broader terms, the source impedance should be large compared to the feedback impedance of the meter.

The most effective method of minimizing input capacitance is to connect the electrometer, SMU, or picoammeter to the signal source with a shielded cable that is as short as possible. When measuring a voltage from

a high source resistance, or when measuring high resistance, guarding can minimize the effects of input capacitance by driving the inner shield of a triax cable or an enclosure surrounding the input with a potential to minimize the effective capacitance, as discussed in Section 2.2.1.

2.6.5 Johnson Noise

The fundamental limit to measurement is Johnson noise in the source resistance. In any resistance, thermal energy produces motion of charged particles. This charge movement results in noise, which is often called Johnson or thermal noise. The power (P) available from this motion is given by:

$$P = 4kTB$$

where: k = Boltzmann's constant (1.38×10^{-23} J/K)

T = absolute temperature in K

B = noise bandwidth in Hz

Metallic conductors approach this theoretical noise limit, while other materials produce somewhat higher noise. Johnson voltage noise (E) developed in a resistor (R) is:

$$E = \sqrt{4kTRB} \text{ volts, rms}$$

and Johnson current noise (I) developed by a resistor (R) is:

$$I = \frac{\sqrt{4kTRB}}{R} \text{ amperes, rms}$$

Statistical considerations show that peak-to-peak noise will be within five times the rms noise more than 99% of the time; therefore, the rms level is commonly multiplied by five to convert to peak-to-peak. At room temperature (300 K), the previous equations become:

$$E_{p-p} = 6.4 \times 10^{-10} \sqrt{RB}$$

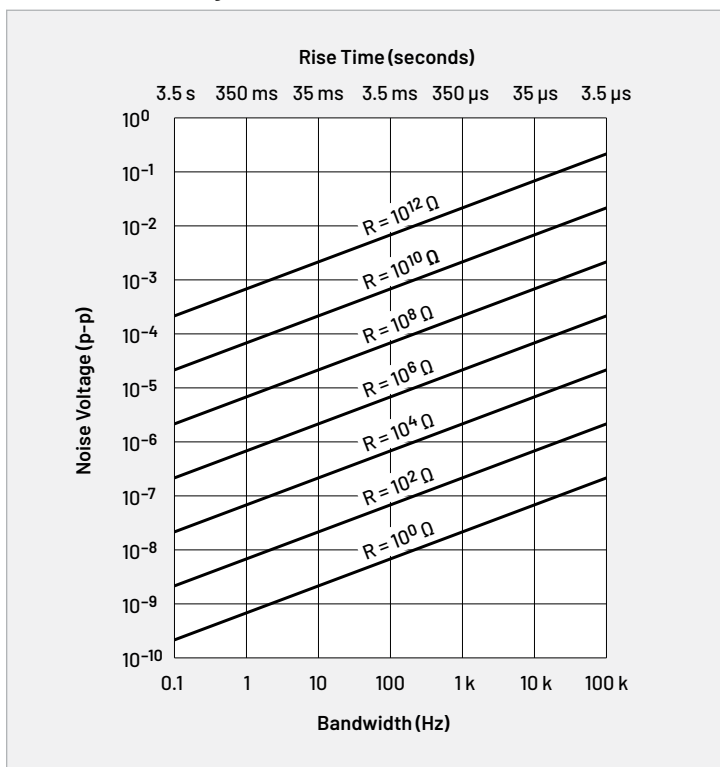
$$I_{p-p} = 6.4 \times 10^{-10} \sqrt{\frac{B}{R}}$$

All real voltage and current sources contain an internal resistance; therefore, they exhibit Johnson noise. **Figure 2-58** shows Johnson noise voltage versus source resistance for various bandwidths (or rise times) at room temperature.

For current measurements, **Figure 2-59** shows the current noise generated by various resistances at various bandwidths. Note that current noise decreases with increasing resistance, but voltage noise increases.

Johnson noise imposes a theoretical limit to achievable voltage or current resolution. The previous equations suggest several means for reducing Johnson noise. It might be possible to reduce the bandwidth, the source temperature, or the source resistance.

FIGURE 2-58: Noise Voltage vs. Bandwidth at Various Source Resistances



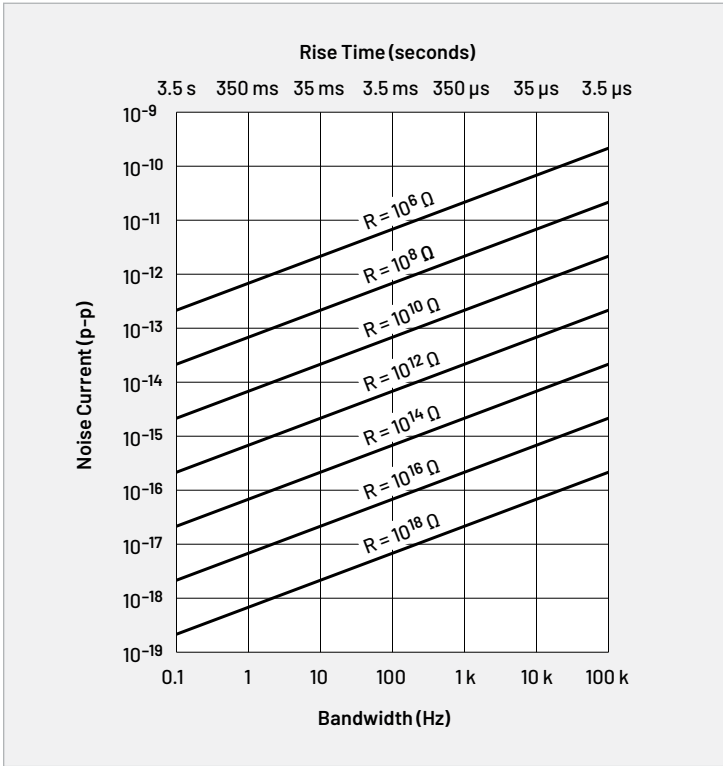
Bandwidth

Johnson noise is uniformly distributed over a wide frequency range, so reducing the noise bandwidth effectively decreases the noise in the measurement. Note that noise bandwidth isn't necessarily the same as signal bandwidth. The high frequency noise cutoff point is approximately equal to the smallest of:

- $\pi/2$ times the upper 3dB frequency limit of the analog DC measuring circuitry
- $0.35/t_r$, where t_r is the instrument's 10%-90% rise time
- 1 Hz if an analog panel meter is used for readout or
- j/t_{INT} where t_{INT} is the integration period of the A/D converter in a digital instrument, and where j is a function of the A/D converter bandwidth, which is usually in the range of 0.2 to 0.4.

The low frequency noise cut-off point is a function of the signal observation window, regardless of the aperture time of the A/D.

FIGURE 2-59: Noise Current vs. Bandwidth at Various Source Resistances



In high resistance circuits, the noise bandwidth is often limited by the time constant of the source resistance and input capacitance, and this value represents the smallest of the above alternative noise bandwidth calculations. In this case, noise bandwidth is:

$$\begin{aligned}
 B_{\text{NOISE}} &= \frac{\pi}{2} (f_{3\text{dB}}) \\
 &= \frac{\pi}{2} \left(\frac{1}{2\pi R_{\text{EFFECTIVE}} C_{\text{IN}}} \right) \\
 &= \frac{1}{4 R_{\text{EFFECTIVE}} C_{\text{IN}}}
 \end{aligned}$$

where $R_{\text{EFFECTIVE}}$ is the source resistance in parallel with the input resistance of the measuring device, and C_{IN} is the sum of all capacitance shunting the input to the instrument (input capacitance, cable capacitance, etc.) Note that this analysis assumes a simple first-order system with one dominant time constant.

To reduce noise, the bandwidth (B) may be reduced artificially by averaging a number of digital readings with a computer, or by internal digital filtering. Using low pass filters before the readout device may also reduce bandwidth. There is a practical limit to reducing bandwidth because very long-term measurements become susceptible to other errors, such as time and temperature drift. See Section 2.6.9 Ways to Reduce Noise for High Impedance Measurements.

Temperature

Reducing the temperature of the signal source from room temperature (300 K) to -270°C (3 K) decreases noise voltage by a factor of about ten. Similarly, a reduction from room temperature to liquid nitrogen levels (77 K) reduces noise by a factor of two. In some applications, the inconvenience and expense of cryogenic operation may be justified and feasible. However, most experiments are designed to operate within a certain temperature range, which in turn determines the noise to be expected from the source.

Source Resistance

After the bandwidth and temperature, the remaining factor in determining the system noise is the effective source resistance. The effective source resistance includes the device under test as well as the measurement instrument. Changing the source resistance is usually impractical for noise reduction. However, if a change can be made, the equations show that R should be lowered to decrease voltage noise or raised to decrease current noise.

In voltage measurements, the voltage source resistance is in parallel with the voltmeter input resistance (see **Figure 2-1**). The input resistance is normally much larger than the source resistance; hence, the source resistance value usually determines the Johnson noise voltage.

In current measurements, the source resistance and the sensing resistance both contribute noise. The effective resistance is the parallel combination of the source resistance and the feedback (or shunt) sensing resistance. Feedback ammeters with high value sensing resistors in the feedback loop have lower Johnson current noise and thus greater sensitivity than shunt ammeters with lower resistance shunts.

Excess Current Noise

The Johnson noise of a resistor is related only to the resistance, the temperature, and the bandwidth. When current passes through a resistor, the noise will increase above the calculated Johnson noise. This increase in noise is sometimes referred to as "excess current noise." A wirewound resistor is nearly ideal and the noise increase is negligible. Metal film resistors have somewhat greater noise and carbon composition resistors are significantly noisier still. In all cases, this excess noise is directly proportional to the current through the resistor.

2.6.6 Device Connections

Although instrument accuracy is of great importance when making low level measurements, the integrity of device connections is equally important. The complete signal path from connectors, through the cables, and into the test fixture must degrade the measured signal as little as possible. The following paragraphs discuss cable and test fixture requirements and types of connectors generally used when making low level measurements.

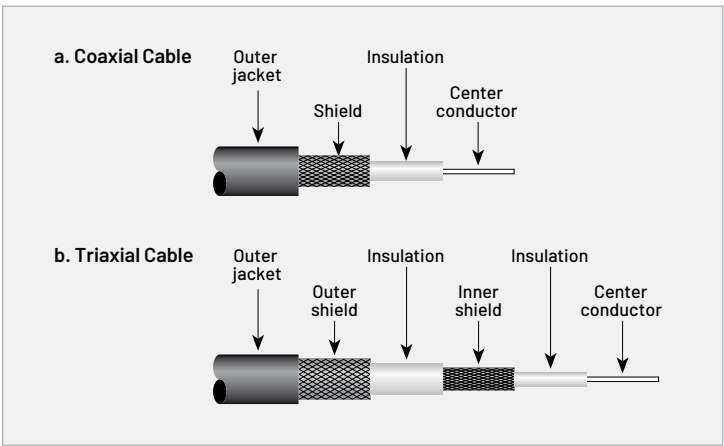
Cable Requirements

Although DMMs often use unshielded test leads, such connection schemes are generally inadequate for low level measurements made with picoammeters, electrometers, and SMUs. These instruments generally use either coaxial or triaxial cables.

A coaxial cable consists of a single conductor surrounded by a shield (**Figure 2-60a**); a triaxial cable adds a second shield around the first (**Figure 2-60b**). With triax cable, the inner shield can be driven at guard potential in order to reduce cable leakage and minimize circuit rise times. The outer shield is usually connected to chassis ground or, in some cases, to the common terminal. In either case, the outer shield must not be allowed to float more than 30 Vrms (42.4 V peak) above chassis ground for safety considerations. Always use a cable with a tightly woven shield to protect against electrostatic interference.

Both coaxial and triaxial cables are available in low noise versions, which should be used for low level measurements. Low noise cables have internal graphite coatings to minimize current generated by triboelectric effects. (See Section 2.3.4.) In some cases, ordinary coaxial cable such as RG-58 may be adequate, although both leakage and noise currents will be higher than with low noise cables.

FIGURE 2-60: Coaxial and Triaxial Cables



When measuring high resistance, the insulation resistance of the cable is important. Good quality triaxial cables use polyethylene insulators and have a typical conductor-to-shield insulation resistance of about 1 T Ω /ft. Refer to Section 2.2.2 for more information on insulation characteristics.

Parameters like cable resistance, capacitance, and leakage currents change as cable length increases. Thus, it's important to keep all connecting cables as short as possible. For example, a ten-foot cable with 1 T Ω /ft resistance and 100 pF/ft capacitance will have an insulation resistance of 100 G Ω and a capacitance of 1000 pF.

Connector Types

Two general types of connectors are used for electrometer, picoammeter, and SMU measurements. The BNC connector shown in **Figure 2-61** is a type of coaxial connector. It includes a center conductor and shell or shield connection; the triax connector shown in **Figure 2-62** includes a center conductor, an inner shield, and an outer shield.

The center conductor of the BNC connector is connected to input HI; the outer shell is input LO. Note that the shell may be connected directly to chassis ground at the instrument.

The center conductor of the triax connector is connected to HI. The inner shield is either LO or guard; the outer shield is usually connected to chassis ground. However, with some SMUs, the outside shield is connected to the LO terminal (common) and is allowed to float off ground. See the discussion that follows for more information on triaxial cable and guarding.

To maintain high insulation resistance, use proper insulating material between the various conductors of all connectors. Towards that goal, most quality BNC and triax connectors use Teflon® insulation between conductors.

Triaxial Cabling and Guarded Connections

As discussed previously, connecting a guard voltage to the shield of a coaxial cable can present a safety hazard if the guard voltage is >30 Vrms. Triaxial cabling avoids this problem by surrounding the guard shield with an outer shield connected to earth ground or LO.

For unguarded operation of an electrometer, triaxial cabling is normally connected as follows:

- Center Conductor: High impedance lead (HI)
- Inner Shield: Low impedance lead (LO)
- Outer Shield: Ground (GND)

This arrangement provides the capability of safely carrying two signals, neither of which is at ground potential, while maintaining high impedance integrity by shielding both leads and maintaining a high resistance between each conductor and ground.

FIGURE 2-61: BNC Connector

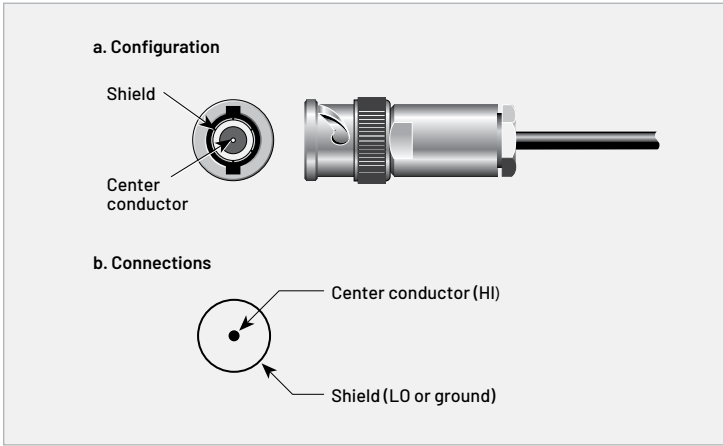
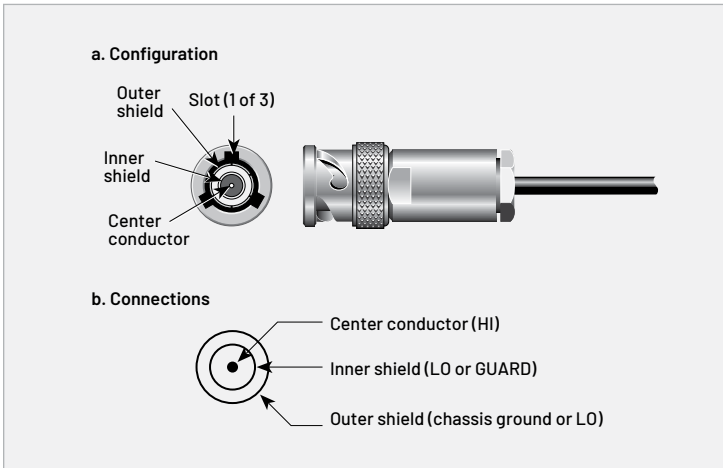


FIGURE 2-62: Three-Slot Triaxial Connector



When an electrometer is in the guarded mode or if an SMU is used, a triaxial cable is connected in the following manner:

- Center Conductor: HI
- Inner Shield: GUARD
- Outer Shield: Ground or LO

With an electrometer, the guard connection is useful when measuring high resistance or when measuring voltage from a high source resistance. It's not needed when measuring low current, because the guard in a feed-back ammeter circuit of an electrometer is always LO. Newer electrome-

ters provide internal switching to change between guarded and unguarded connections.

When using an SMU to measure low current, the guard terminal is used to reduce leakage current of the cable and test fixturing.

Test Fixture Requirements

Test fixtures used for low level measurements have several important requirements:

- *Insulation Resistance:* The insulation resistance of all connectors, internal wiring, terminals, and sockets should be as high as possible. Generally, a good-quality fixture will use Teflon insulation in all connectors and sockets.
- *Shielding and Guarding:* The fixture should provide adequate shielding for sensitive circuits. For high impedance measurements, provisions should be included to carry guard into the fixture as close to the DUT as possible.
- *Light:* A light-tight fixture is a necessity when testing light-sensitive components.
- *Special Fixture Requirements:* Special applications, such as high resistance or very low current measurements, often require fixtures designed with good insulation characteristics, which may only be possible by using special materials, such as sapphire.

2.6.7 Analog Outputs

Some electrometers have two analog outputs, a 2 V analog output as well as a preamplifier, or unity gain output. The 2 V analog output is useful for connecting to recorders; the preamp output is useful for buffering, guarding, and external feedback. This section discusses these outputs and possible loading errors when using these outputs. Refer to Section 2.6.8 for details on using the analog output with a floating input.

2 V Analog Output

The typical analog output is ± 2 V for a full scale input signal. Depending on the instrument design and function, the output may be inverting or non-inverting. The output resistance may range from 1 Ω to 10 k Ω . Any device connected to the output, such as an oscilloscope or digitizer, will have a finite input resistance and will attenuate the analog output. See the section below on *Loading Errors* for more information.

Preamplifier Output

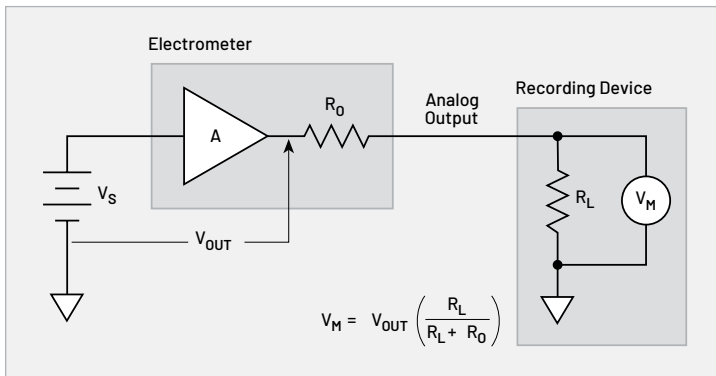
The preamp output follows the signal amplitude applied to the input terminal of the electrometer. The preamp out is the guard voltage for volts and ohms (constant-current method only). It's useful for buffering the input signal. It may be inverting or non-inverting, depending on the function selected.

Loading Errors

Although the output resistance of a typical analog output is low, it isn't zero, so consider the possibility of loading by external instrumentation. In principle, the concepts of analog output loading are identical to those for source loading, discussed in Section 2.2.1.

Figure 2-63 demonstrates how loading can affect the accuracy of the analog output. A voltage to be measured (V_S) is applied to the electrometer input. The signal is amplified by an amplifier (A) with output resistance (R_O), then connected to a recording device. The input resistance of the recording device (R_L) and the analog output resistance (R_O) form a voltage divider that attenuates the output signal. For a typical analog output resistance of 1 k Ω , the recording device must have an input resistance of at least 1 M Ω if error due to loading is to be kept under 0.1%. This error can be calculated using the equation shown in **Figure 2-63**.

FIGURE 2-63: Analog Output Loading



2.6.8 Floating Input Signals

The majority of electrometer or picoammeter applications involve an input signal referred to earth ground. However, in some applications, it's necessary that the electrometer or picoammeter be biased off ground. Examples of such applications include the flame ionization detector of a gas chromatograph and the Faraday cup in a mass spectrometer.

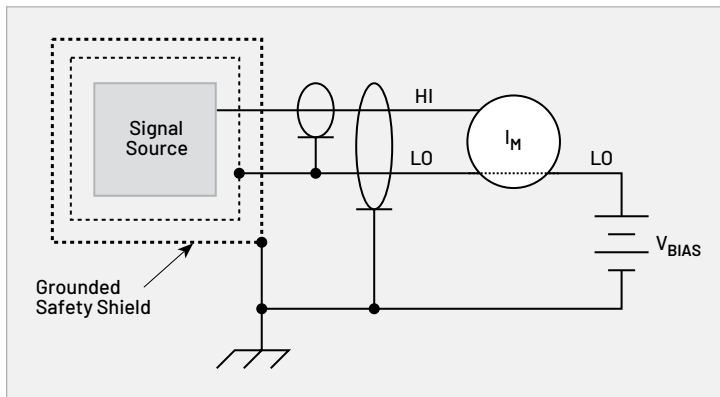
In a typical low level test setup, shielding is required to reduce noise, as shown in **Figure 2-64**. In most cases, this "noise" shield is connected to the LO input terminal of the meter. If the LO terminal must be biased more than 30 V with respect to earth ground, the noise shield will be at a hazardous voltage and will pose a shock hazard. To avoid shock hazards with floating circuits, a second grounded safety shield must be added to enclose the noise shield completely.

Most picoammeters and electrometers use a triaxial input connector with the outside shield connected to earth ground for safety. By using

a triaxial connector at the safety shield and a triaxial cable between the test setup and the instrument, a completely shielded and safe system will result. Note: Maximum float voltage ratings must be observed to prevent breakdown between the inner conductors and the outer grounded shield.

For SMUs with the outside shield connected to LO and for picoammeters with a coax input, the LO input should not be allowed to float more than 30 Vrms (42 V peak) from ground.

FIGURE 2-64: Safety Shielding with Floating Circuits



2.6.9 Ways to Reduce Noise When Making High Impedance Measurements

When making optimal high impedance measurements, noise can be reduced by electrostatic shielding the test circuit, avoiding noise sources, adding a series diode to high capacitive DUT circuits, averaging readings, and optimizing line cycle integration time.

Shielding the Test Circuit

When making high impedance measurements, it is very important to use shielding to prevent errors due to electrostatic interference.

To minimize errors due to electrostatic coupling:

- Shield the DUT and connect the enclosure electrically to the test circuit common. Refer to Section for 2.6.2 for more information.
- Use shielded cables.
- Keep all charged objects (including people) and conductors away from sensitive areas of the test circuit.
- Avoid movement and vibration near the test area.

Avoiding Noise Sources

Remove offending noise sources or keep the measuring instrument, cabling and DUT away from these noise sources. Avoid using the test circuitry in an

electrically noisy environment. Power off any unused instruments in the lab or near the sensitive measurement circuitry.

Adding Series Diode to High Capacitive Circuits

In general, as the source capacitance of the DUT and test circuit increases, so does the noise gain of the measurement circuit. Most sensitive ammeters have a maximum recommended source capacitance. A series diode can prevent noisy readings by reducing the noise gain. Refer to Sections 2.3.2 and 4.3.1 for more information.

Averaging Readings

To reduce noise, measurement bandwidth may be reduced artificially by averaging a number of readings with a computer or by using internal digital filtering (averaging) of the instrument to further reduce noise. The square root of the number of readings averaged gives you the amount of noise reduction per the following equation:

$$\text{Noise Reduction} = \sqrt{\# \text{ Rdgs Averaged}}$$

For example, if 100 readings are averaged, then the noise is reduced by a factor of 10.

Optimizing Line Cycle Integration Time

A common form of external noise is 50 Hz or 60 Hz line cycle noise. This interference is a noise signal that can be coupled to the test signal. Line cycle noise can result in inaccurate and fluctuating readings.

Line cycle integration is used to minimize the effects of line cycle noise by averaging out the noise by integrating the measurement over an integral number of power line cycles (PLCs). The value of the PLC offers a trade-off between the speed and noise rejection of the readings. Refer to Section 3.2.8 for a detailed description of using line cycle integration.

2.7 High Impedance Measurement Optimization Summary

Section 2 describes various high impedance measurement techniques, sources of error, and ways to reduce them. In summary, here are some steps to ensure making optimal high impedance measurements:

- **Choose an instrument sensitive enough to measure low current: picoammeter, electrometer, or SMU with low current measurement ranges.** Understand the manufacturer's instrument specifications, including the accuracy. Allow the instrument to warm up for at least an hour and make sure the instrument has periodic recalibration to ensure it is within specification.
- **Ensure cables, connectors, and test fixtures are appropriate for high impedance measurements.** If necessary, verify the insulation resistance specification of these components from the manufacturer or directly measure the insulation resistance or leakage current of the test circuit with no DUT connected. Use shielded cables.

- **Allow sufficient settling time for the readings.** To verify there is sufficient settling time for the device under test, make a graph of the measurement as a function of time and observe the time it takes for the DUT reading to settle. Use this time as the measurement delay time of the DUT.
- **Apply the proper measurement techniques to minimize measurement errors.** Some of these techniques include shielding, guarding, avoiding vibration, measuring the input bias current, etc.
- **Verify the capabilities of the test system.** Verification procedures for individual instruments are usually described in the reference manual of the instrument. One of the first steps in setting up a system for making high impedance measurements is to determine the offset and/or leakage current of the entire measurement system. This identifies the noise floor limit of the entire system and sets a starting point for troubleshooting potential problems and making improvements to the system, if necessary. The remainder of this section outlines simple verification tests and error symptoms by measurement type, along with possible causes and ways to minimize these errors.

Low Current Measurements

To check the ammeter, verify its input bias current using the following steps:

1. Turn on the power and allow the meter to warm up for the time specified in the user manual.
2. Place a shield cap over the input connector and link the low impedance input terminal to ground.
3. For an electrometer or picoammeter, the zero check should be enabled. For an SMU, the voltage source should be set to zero volts.
4. Set the ammeter to the most sensitive current range, zero or REL the meter, and then disable the zero-check switch (for picoammeter or electrometer) or turn on the output (for SMU).
5. After several seconds, the meter reading should settle to within a few digits. The indicated current is the input bias current. If it exceeds the instrument specifications, repeat the test after the instrument has warmed up overnight. If the current is still excessive, the instrument should be repaired.

Never verify a feedback ammeter by short-circuiting the input (like a voltmeter is verified). Shorting the input will increase the noise gain (see Section 2.3.2). The ammeter's voltage burden divided by the low impedance short will generate a non-zero current measured by the feedback ammeter. The resulting measurements will be meaningless.

Table 2-4 lists potential error symptoms of low current measurements along with possible causes and ways to minimize these errors.

High Resistance Measurements

Resistance measurements should be verified using a resistor of known value that is similar in magnitude to the resistance under test. The verification measurements should be made using the same voltage as the test voltage of the DUT. **Table 2-5** lists some potential error sources and ways to minimize them. Also, review error sources in **Table 2-4** if using the source voltage, measure current method.

Voltage Measurements from a High Resistance Source

The simplest way to make a rough check of an electrometer voltmeter is to connect a single battery across the input. Be sure to try both polarities. **Table 2-6** describes potential error sources and ways to minimize them when making voltage measurements from high impedance sources.

Charge Measurements

The coulombs function can be checked using a low leakage capacitor and a voltage source. A capacitor of known value in the range of 100 pF to 1000 pF can be charged to a known voltage via a battery. The capacitor is then connected to the electrometer input after setting the meter to coulombs and disabling the zero check function. Conversely, this procedure can be used to determine the value of the capacitor, if unknown.

TABLE 2-4: Potential Error Symptoms of Low Current Measurements

Error Symptom	Possible Causes	How to Minimize or Avoid	Refer to Section
Noisy readings	Electrostatic coupling	Use shielding and avoid nearby movements. Use low noise cabling.	2.6.2
	Source resistance too low	Use shunt ammeter.	2.3.2
	Source capacitance too high	Add series resistance or series diode.	2.3.2
	Temperature variations of test circuit. Offset current drift.	Operate measurement system in a thermally stable environment. Allow system to warm up to achieve thermal stability.	2.3.3 2.6.3
	Noisy input signal. Johnson noise.	Use filtering. Reduce temperature of DUT, if possible.	2.6.5
	50 Hz or 60 Hz interference	Identify and remove offending source, if possible. Use shielding. Increase PLC setting of ammeter.	2.6.2 3.2.8
Offset current	Input bias current of meter	Use instrument with low input bias current such as an electrometer. Zero or REL the offset. Check instrument calibration.	2.3.4
	Cable, connectors, or test fixtures	First measure the input bias current of the ammeter. Then, verify the open circuit offset current of the entire system by adding one piece of cabling and/or connector at a time and repeating the open circuit current measurements. Use cabling appropriate for high impedance applications.	
	Offset current drift due to temperature changes in the test circuit	Stabilize the temperature of the entire test circuit including the ammeter.	
	Excessive humidity	Keep humidity to moderate levels, <50% R.H., the lower the better.	
Gain error at low voltage	Voltage burden too high	Use feedback ammeter. Use higher range.	2.3.5
Readings too high	Insufficient settling time	Allow a longer time delay before taking a reading.	2.6.4
One or two digits of resolution	Range selected is too high or instrument not sensitive enough	Select a lower current range or use autoranging. Use a more sensitive ammeter. Check instrument specifications.	

TABLE 2-5: Potential Error Symptoms of High Resistance Measurements

Error Symptom	Possible Causes	How to Minimize or Avoid	Refer to Section
Readings too low	Fixture in parallel with DUT	Use fixture and cables with higher insulation resistance. Guarding will effectively increase shunt resistance.	2.3.1
	Low voltmeter input resistance (Force I, Measure V method)	Use force voltage, measure current method.	2.2.1
	Offset current	Suppress or REL the current offset with test voltage off. Use alternating voltage method.	2.3.4 4.4.2
	Insufficient settling time	Increase measurement time to ensure a settled reading.	2.6.4 2.4.1
	Contamination and humidity	Select insulators that resist water absorption, keep humidity to moderate levels, keep all insulators clean and free of contamination.	2.3.4 2.6.3
Noisy readings	Electrostatic coupling	Shield test circuit and avoid movement and fluctuating voltages nearby.	2.6.2
	50 Hz or 60 Hz interference	Identify and remove offending source, if possible. Shield. Increase PLC setting of ammeter.	2.6.2 3.2.8
Negative readings, or readings that are much higher or lower than expected	Background currents due to dielectric absorption, triboelectric charge or piezoelectric effects	Use offset correction technique such as the alternating polarity method or the alternating voltage method.	4.4.2 2.3.4
	Current measure range too high	Use more sensitive current range or use auto ranging	

TABLE 2-6: Potential Error Symptoms of Voltage Measurements from a High Resistance Source

Error Symptom	Possible Causes	How to Minimize or Avoid	Refer to Section
Readings too low (loading error)	Shunt resistance loading	Use fixtures and cables with higher insulation resistance. Guarding will effectively increase shunt resistance.	2.2.1
	Offset current loading	Input bias current of meter is too high. Use electrometer.	2.2.1
Noisy readings	Electrostatic coupling	Use shielding. Avoid movement and fluctuating voltages nearby.	2.6.2
	Noisy input signal, Johnson noise	Use filtering. Reduce temperature of DUT, if possible.	2.6.5

SECTION 3

**Measurements from
Low Resistance
Sources**

3.1 Introduction

Low voltage and low resistance measurements are often made on devices and materials with low source impedance. Section 1 described instruments for measuring low voltage and low resistance. Section 3 describes how to use these instruments to make accurate measurements, including a discussion of various error sources and ways to minimize their effect on measurement integrity:

- 3.2 Low Voltage Measurements: Discussion of potential error sources and how to minimize their impact on low voltage measurement accuracy. These error sources include offset voltages, noise and common-mode current, and reversal errors.
- 3.3 Low Resistance Measurements: Topics include lead resistance, thermoelectric EMFs, non-ohmic contacts, device heating, dry circuit testing, and measuring inductive devices.
- 3.4 Low Impedance Measurement Optimization Summary

3.2 Low Voltage Measurements

Significant errors may be introduced into low voltage measurements by offset voltages (DC) and noise sources (AC) that can normally be ignored when measuring higher voltage levels. Electrical noise is simply any undesired signal that corrupts a desired signal to produce an erroneous measurement. Noise can cause a steady-state offset that results in inaccurate readings or it can result in erratic measurements that are difficult to read. In either case, noise can seriously degrade measurement accuracy and cause significant errors. The following paragraphs discuss various error sources and ways to minimize them.

3.2.1 Effects of Error Sources on Voltage Measurements

Ideally, when a voltmeter is connected to a relatively low impedance circuit in which no voltages are present, it should read zero. However, a number of error sources in the circuit may be seen as a non-zero DC voltage offset. These sources include thermoelectric EMFs and offsets in the voltmeter input circuit.

As shown in **Figure 3-1**, any DC offset voltage (V_{OFFSET}) will add to or subtract from the source voltage (V_S) so that the voltage measured by the meter (V_M) becomes:

$$V_M = V_S \pm V_{\text{OFFSET}}$$

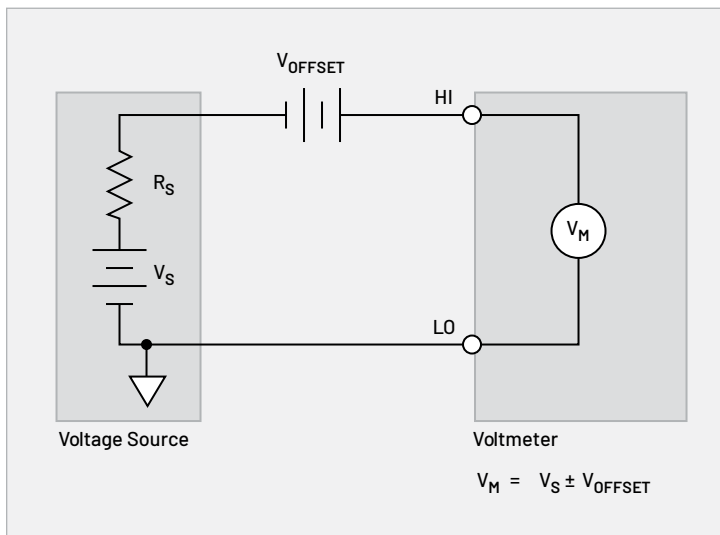
The relative polarities of the two voltages will determine whether the offset voltage adds to or subtracts from the source voltage. For example, assume $V_S = 5 \mu\text{V}$ and $V_{\text{OFFSET}} = 250 \text{ nV}$. If the voltage polarities are in opposition, the voltmeter reading will be:

$$V_M = (5 \times 10^{-6}) - (250 \times 10^{-9})$$

$$V_M = 4.75 \times 10^{-6}$$

$$V_M = 4.75 \mu\text{V} \text{ (an error of } -5\%)$$

FIGURE 3-1: Effects of Offset Voltages on Voltage Measurement Accuracy



Steady offsets can generally be nulled out by shorting the ends of the test leads together, then enabling the instrument's zero (REL) feature. Note, however, that cancellation of offset drift may require frequent re-zeroing, particularly in the case of thermoelectric EMFs.

Error sources can also be AC voltage that can cause erratic voltmeter readings. Some of these error sources include Johnson noise, 1/f noise, and line cycle interference.

3.2.2 Thermoelectric EMFs

Thermoelectric voltages (thermoelectric EMFs) are the most common source of errors in low voltage measurements. These voltages are generated when different parts of a circuit are at different temperatures and when conductors made of dissimilar materials are joined together, as shown in **Figure 3-2**. A few microvolts of thermal voltages can be generated by temperature gradients in the test circuit caused by fluctuating temperatures in the lab or a draft near the sensitive circuitry. The Seebeck coefficients (Q_{AB}) of various materials with respect to copper are summarized in **Table 3-1**.

Constructing circuits using the same material for all conductors minimizes thermoelectric EMF generation. For example, connections made by crimping copper sleeves or lugs on copper wires results in copper-to-copper junctions, which generate minimal thermoelectric EMFs. Also, connections must be kept clean and free of oxides. Crimped copper-to-copper connections, called "cold welded," do not allow oxygen penetration and may have a Seebeck coefficient of $\leq 0.2 \mu\text{V}/^\circ\text{C}$. Cu-CuO connections may have a coefficient as high as $1 \text{ mV}/^\circ\text{C}$.

FIGURE 3-2: Thermoelectric EMFs

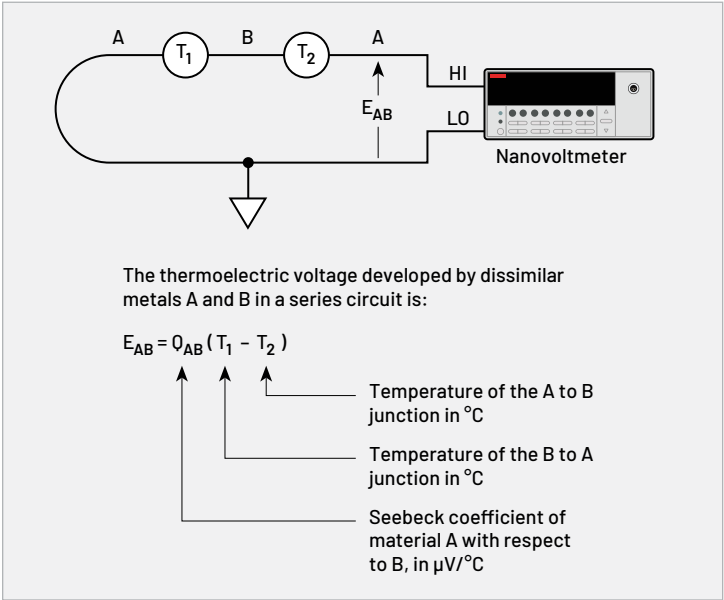


TABLE 3-1: Seebeck Coefficients

Paired Materials*	Seebeck Coefficient, Q_{AB}
Cu - Cu	$\leq 0.2 \mu V/^{\circ}C$
Cu - Ag	$0.3 \mu V/^{\circ}C$
Cu - Au	$0.3 \mu V/^{\circ}C$
Cu - Pb/Sn	$1-3 \mu V/^{\circ}C$
Cu - Si	$400 \mu V/^{\circ}C$
Cu - Kovar	$\sim 40-75 \mu V/^{\circ}C$
Cu - CuO	$\sim 1000 \mu V/^{\circ}C$

* Ag = silver Au = gold Cu = copper CuO = copper oxide
Pb = lead Si = silicon Sn = tin

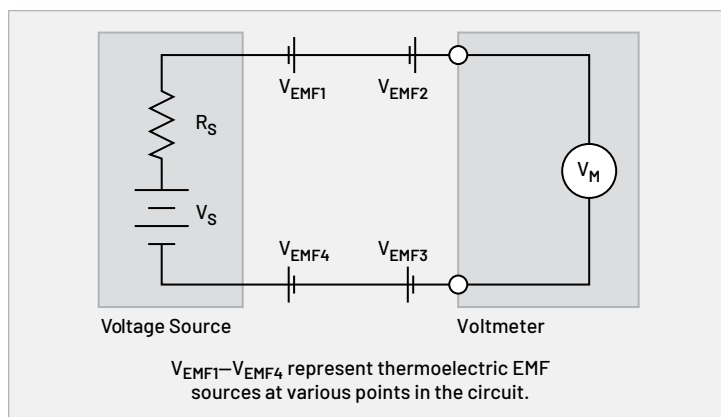
Minimizing temperature gradients within the circuit also reduces thermoelectric EMFs. A technique for minimizing such gradients is to place corresponding pairs of junctions in close proximity to one another and to provide good thermal coupling to a common, massive heat sink. Electrical insulators having high thermal conductivity must be used, but given that most electrical insulators don't conduct heat well, special insulators such as hard anodized aluminum, beryllium oxide, specially filled epoxy resins, sapphire, or diamond must be used to couple junctions to the heat sink.

Allowing test equipment to warm up and reach thermal equilibrium in a constant ambient temperature also minimizes thermoelectric EMF effects. The instrument zero feature can compensate for any remaining thermoelectric EMF, provided it is relatively constant. To keep ambient temperatures constant, equipment should be kept away from direct sunlight, exhaust fans, and similar sources of heat flow or moving air. Wrapping connections in insulating foam (e.g., polyurethane) also minimizes ambient temperature fluctuations caused by air movement.

Connections to Avoid Thermoelectric EMFs

Connections in a simple low voltage circuit, as shown in **Figure 3-3**, will usually include dissimilar materials at different temperatures. This results in a number of thermoelectric EMF sources, all connected in series with the voltage source and the meter. The meter reading will be the algebraic sum of all these sources. Therefore, it is important that the connection between the signal source and the measuring instrument doesn't interfere with the reading. The following paragraphs provide tips on making good connections to minimize thermoelectric voltages.

FIGURE 3-3: Connections from Voltage Source to Voltmeter



If all the connections can be made of one metal, the amount of thermoelectric EMF added to the measurement will be negligible. However, this may not always be possible. Test fixtures often use spring contacts, which may be made of phosphor-bronze, beryllium-copper, or other materials with high Seebeck coefficients. In these cases, a small temperature difference may generate a large enough thermoelectric voltage to affect the accuracy of the measurement.

If dissimilar metals cannot be avoided, an effort should be made to reduce the temperature gradients throughout the test circuit by use of a heat sink or by shielding the circuit from the source of heat.

Measurements of sources at cryogenic temperatures pose special problems because the connections between the sample in the cryostat and the voltmeter are often made of metals with lower thermal conductivity than copper, such as iron, which introduces dissimilar metals into the circuit. In addition, because the source may be near zero Kelvin while the meter is at 300 K, there is a very large temperature gradient. Matching the composition of the wires between the cryostat and the voltmeter and keeping all dissimilar metal junction pairs at the same temperature allows making very low voltage measurements with good accuracy.

Reversing Sources to Cancel Thermoelectric EMFs

When measuring a small voltage, such as the difference between two standard cells or the difference between two thermocouples connected back-to-back, the error caused by stray thermoelectric EMFs can be canceled by taking one measurement, then carefully reversing the two sources and taking a second measurement. The average of the difference between these two readings is the desired voltage difference.

In **Figure 3-4**, the voltage sources, V_a and V_b , represent two standard cells (or two thermocouples). The voltage measured in **Figure 3-4a** is:

$$V_1 = V_{\text{emf}} + V_a - V_b$$

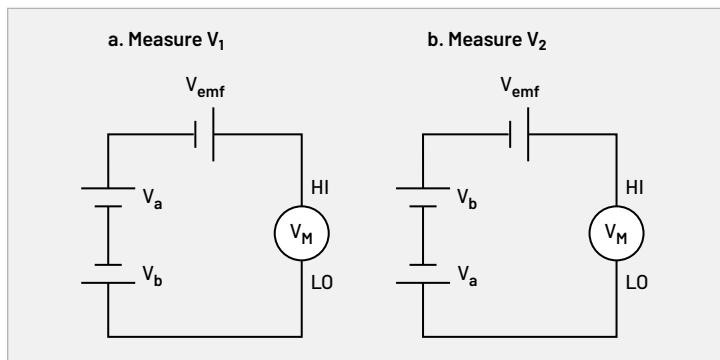
The two cells are reversed in **Figure 3-4b** and the measured voltage is:

$$V_2 = V_{\text{emf}} + V_b - V_a$$

The average of the difference between these two measurements is:

$$\frac{V_1 - V_2}{2} = \frac{V_{\text{emf}} + V_a - V_b - V_{\text{emf}} - V_b + V_a}{2} \text{ or } V_a - V_b$$

FIGURE 3-4: Reversing Sources to Cancel Thermoelectric EMFs



Notice that this measurement technique effectively cancels out the thermoelectric EMF term (V_{emf}), which represents the algebraic sum of all thermoelectric EMFs in the circuit except those in the connections

between V_a and V_b . If the measured voltage is the result of a current flowing through an unknown resistance, then either the current-reversal method or the offset-compensated ohms method may be used to cancel the thermoelectric EMFs. These methods are described in Section 3.3.2.

3.2.3 Internal Offsets

Nanovoltmeters will rarely indicate zero when no voltage is applied to the input, because there are unavoidable voltage offsets present in the input of the instrument. A short circuit can be connected across the input terminals and the output can then be set to zero, either by front panel zero controls or by computer control. If the short circuit has a very low thermoelectric EMF, this can be used to verify input noise and zero drift with time. Clean, pure copper wire will usually be suitable. However, the zero established in this manner is useful only for verification purposes and is of no value in the end application of the instrument.

If the instrument is being used to measure a small voltage drop resulting from the flow of current through a resistor, the following procedure will result in a proper zero. First, the instrument should be allowed to warm up for the specified time, usually one to two hours. During this time, the connections should be made between the device under test and the instrument. No current should be supplied to the device under test to allow the temperature gradients to settle to a minimum, stable level. Next, the zero adjustment should be made. In some instruments, this is done by pressing REL (for Relative) or ZERO button. The instrument will now read zero. When the test current is applied, the instrument will indicate the resulting voltage drop.

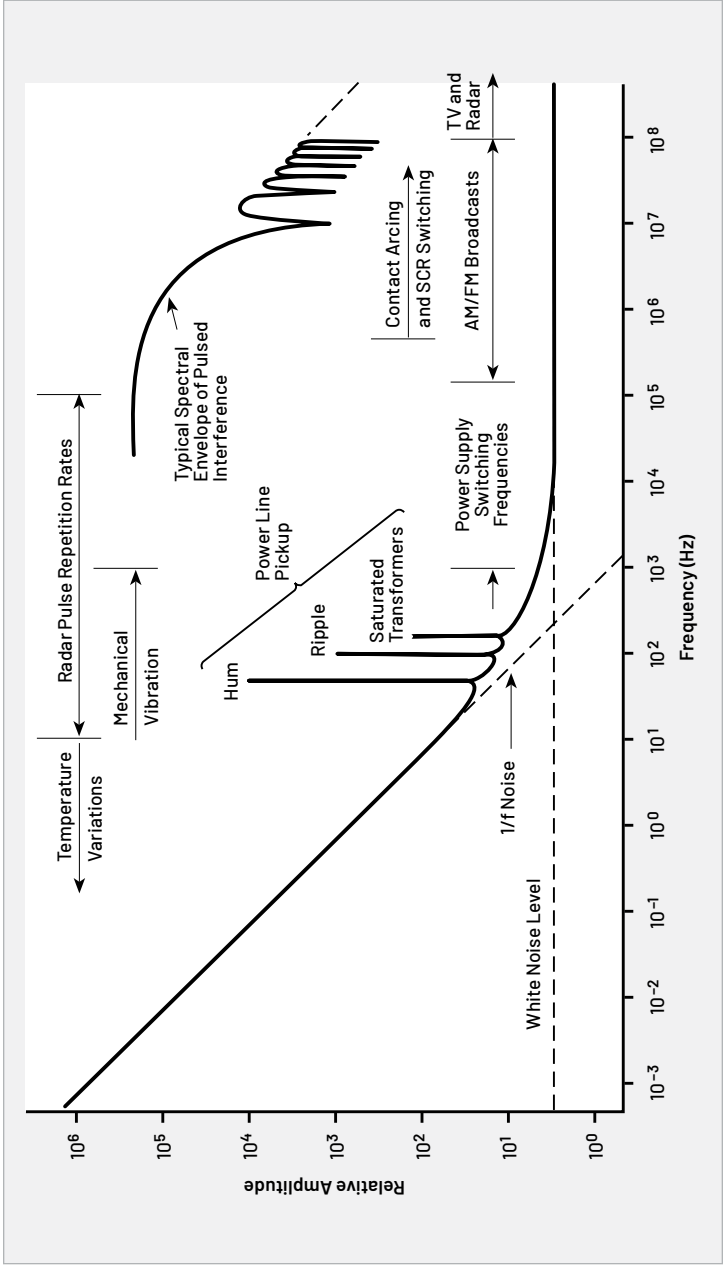
In some applications, the voltage to be measured is always present and the preceding procedure cannot be used. For example, the voltage difference between two standard cells is best observed by reversing the instrument connections to the cells and averaging the two readings. This same technique is used to cancel offsets when measuring the output of differential thermocouples. This is the same method used to cancel thermoelectric EMFs and is described in more detail in the paragraph entitled, "Reversing Sources to Cancel Thermoelectric EMFs." See **Figure 3-4**.

3.2.4 Zero Drift

Zero drift is a change in the meter reading with no input signal (measured with the input shorted) over a period of time. The zero drift of an instrument is almost entirely determined by the input stage. Most nanovoltmeters use some form of chopping or modulation of the input signal to minimize the drift.

The zero reading may also vary as the ambient temperature changes. This effect is usually referred to as the temperature coefficient of the voltage offset.

FIGURE 3-5: Voltage Noise Frequency Spectrum



In addition, an instrument may display a transient temperature effect. After a step change in the ambient temperature, the voltage offset may change by a relatively large amount, possibly exceeding the published specifications. The offset will then gradually decrease and eventually settle to a value close to the original value. This is the result of dissimilar metal junctions in the instrument with different thermal time constants. Although one junction will adjust to the new ambient temperature quickly, another changes slowly, resulting in a temporary change in voltage offset.

To minimize voltage offsets due to ambient temperature changes in junctions, make measurements in a temperature controlled environment and/or slow down temperature changes by thermally shielding the circuit.

3.2.5 RFI/EMI

RFI (Radio Frequency Interference) and EMI (Electromagnetic Interference) are general terms used to describe electromagnetic interference over a wide range of frequencies across the spectrum. **Figure 3-5** shows the general frequency spectrum of these interference sources in comparison with other noise signals such as $1/f$ and thermal noise.

RFI or EMI can be caused by sources such as TV or radio broadcast signals or it can be caused by impulse sources, as in the case of high voltage arcing (see **Figure 3-5**). In either case, the effects on the measurement can be considerable if enough of the unwanted signal is present.

RFI/EMI interference may manifest itself as a steady reading offset or it may result in noisy or erratic readings. A reading offset may be caused by input amplifier overload or DC rectification at the input.

FIGURE 3-6: Shielding to Attenuate RFI/EMI Interference

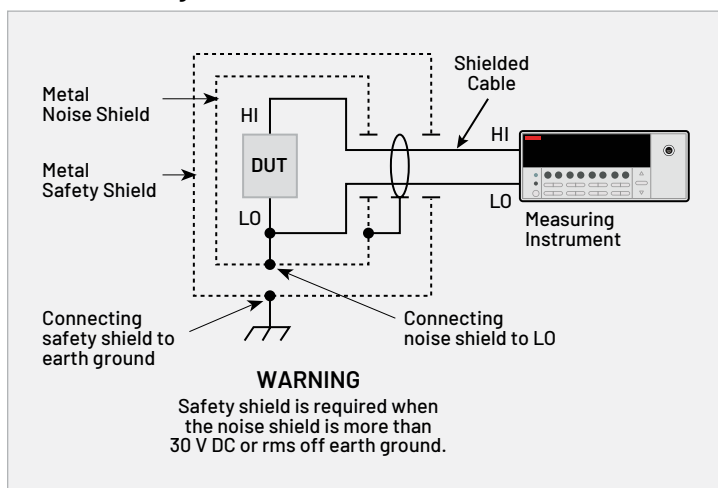
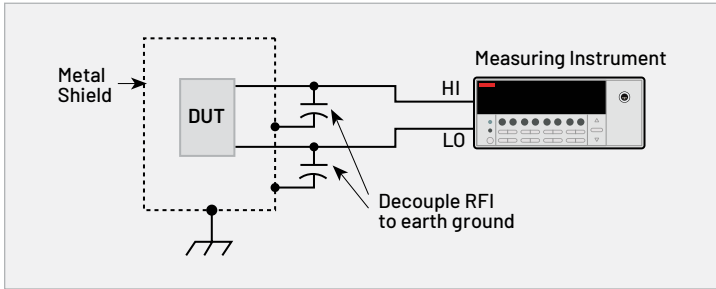


FIGURE 3-7: Shielded Connections to Reduce RFI/EMI



RFI and EMI can be minimized by taking several precautions when making sensitive measurements. The most obvious precaution is to keep all instruments, cables, and DUTs as far from the interference source as possible. Shielding the test leads and the DUT (**Figure 3-6**) will often reduce interference effects to an acceptable level. Noise shields should be connected to input LO. In extreme cases, a specially constructed screen room may be necessary to attenuate the troublesome signal sufficiently.

If all else fails to prevent RF interference from being introduced into the input, external filtering of the device input paths may be required, as shown in **Figure 3-7**. In many cases, a simple one-pole filter may be sufficient; in more difficult cases, multiple-pole notch or band-stop filters may be required. In particular, multiple capacitors of different values may be connected in parallel to provide low impedance over a wide frequency range. Keep in mind, however, that such filtering may have other detrimental effects, such as increased response time on the measurement.

3.2.6 Johnson Noise

The ultimate limit of resolution in an electrical measurement is defined by Johnson or thermal noise. This noise is the voltage associated with the motion of electrons due to their thermal energy at temperatures above absolute zero. All voltage sources have internal resistance, so all voltage sources develop Johnson noise.

A plot of thermal noise voltage as a function of resistance and bandwidth at a temperature of 290 K is shown in **Figure 3-8**. This voltage is related to the temperature, noise bandwidth, and the source resistance. The noise voltage developed by a metallic resistance can be calculated from the following equation:

$$V = \sqrt{4kTBR}$$

where: V = rms noise voltage developed in source resistance

k = Boltzmann's constant, 1.38×10^{-23} joule/K

T = absolute temperature of the source in kelvin

B = noise bandwidth in hertz

R = resistance of the source in ohms

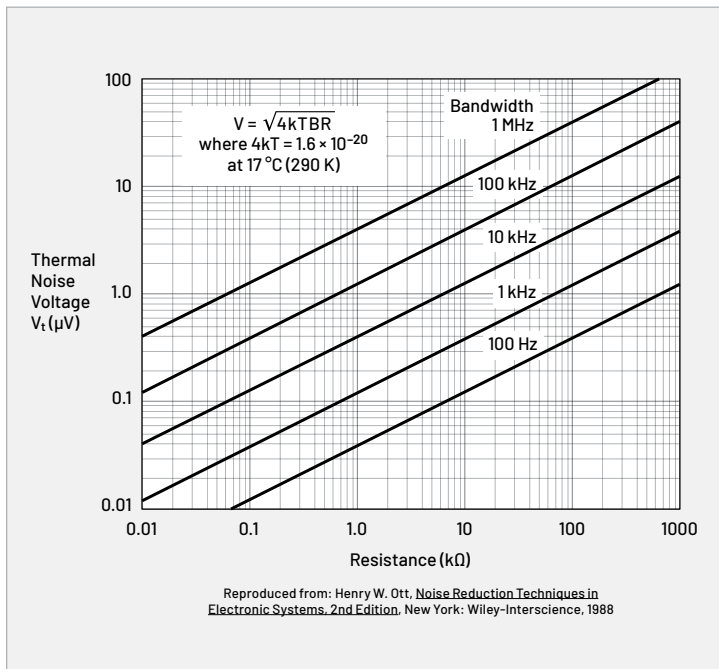
For example, at room temperature (290 K), a source resistance of 10 k Ω with a measurement bandwidth of 5 kHz will have almost 1 μ V rms of noise.

Johnson noise may be reduced by lowering the temperature of the source resistance and by decreasing the bandwidth of the measurement. Cooling the sample from room temperature (290 K) to liquid nitrogen temperature (77 K) decreases the voltage noise by approximately a factor of two.

If the voltmeter has adjustable filtering and integration, the bandwidth can be reduced by increasing the amount of filtering and/or by integrating over multiple power line cycles. Decreasing the bandwidth of the measurement is equivalent to increasing the response time of the instrument, and as a result, the measurement time is much longer. However, if the measurement response time is long, the thermoelectric EMFs associated with the temperature gradients in the circuit become more important. Sensitive measurements may not be achieved if the thermal time constants of the measurement circuit are of the same order as the response time. If this occurs, distinguishing between a change in signal voltage and a change in thermoelectric EMFs becomes impossible.

Johnson noise is discussed in more detail in Section 2.6.5.

FIGURE 3-8: Thermal Noise Voltage as a Function of Resistance and Bandwidth



3.2.7 1/f Noise

In contrast with Johnson noise or white noise, the spectral density of 1/f noise, also known as flicker or pink noise, varies inversely with frequency. Although 1/f noise is present in all electronic devices, it is most often associated with carbon-composition resistors and semiconductor devices. Because 1/f noise is at a maximum at low frequencies, this type of noise may seriously affect low voltage measurements.

To reduce the effects of 1/f noise in a measurement circuit that uses a current source to stimulate the device, use the current-reversal method (Refer to Section 3.3.2 for information on the current-reversal method.) Make sure the reversals are faster than the drift of the system. Typically, the corner frequency, f_c , of the 1/f noise graph is between 10 Hz–25 Hz. The frequency, f_c , is where the 1/f noise and thermal noise graphs intersect. When using the current-reversal method to minimize or reduce 1/f noise, the time to measure both the positive and negative currents reversals must be ≤ 40 ms. Measurement integration at 1 PLC (16.67 ms at 60 Hz or 20 ms at 50 Hz) should be sufficient, but experimentation may be necessary.

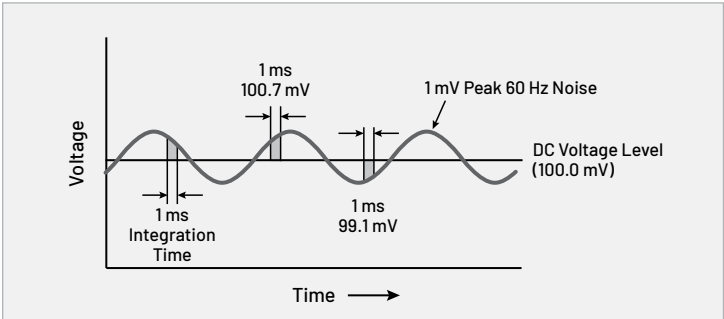
3.2.8 Line Cycle Interference and Line Cycle Integration

The most common form of external noise is 50 Hz or 60 Hz line cycle “pick-up” noise. This interference is a noise signal that may be coupled to the test signal from other instruments in the measurement system or other electrical equipment. This noise, superimposed on the sensitive DC signal being measured, may result in highly inaccurate and fluctuating measurements. Millivolts of noise can be a common occurrence near fluorescent lights.

Figure 3-9 illustrates the effects of pickup noise on voltmeter readings. In this example, line cycle interference may cause a voltmeter with a 1 ms integration time to read 100.7 mV or 99.1 mV instead of the actual signal of 100 mV DC.

To minimize the effects of 50 Hz or 60 Hz line pickup, use line cycle integration. Line cycle noise will “average out” when the measuring instru-

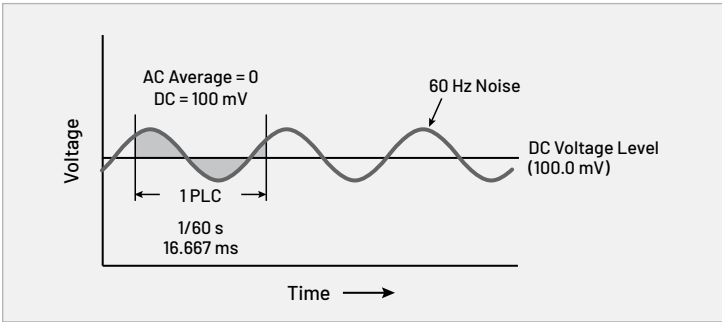
FIGURE 3-9: Line Cycle Noise Superimposed on 100 mV DC Signal



ment's integration time is equal to an integral number of power line cycles. By integrating over one power line cycle, as depicted in **Figure 3-10**, the positive and negative noise contributions cancel out each other, and the AC component of the signal averages to zero leaving only the DC component to be measured. By default, most instruments will use integration of one line cycle (1 PLC), however this parameter is often user defined to improve measurement accuracy, if needed.

In general, the longer the measurement time, the lower the noise.

FIGURE 3-10: Line Cycle Integration Averages Out 60 Hz Noise



Some experimentation may be needed to choose an optimal PLC since there is a trade-off between noise and speed. This trade-off is illustrated in the voltage vs time graph of **Figure 3-11**. The graph illustrates 40 readings taken at 0.1, 1, and 10 PLC. The readings were taken from a voltmeter with a shorted input.

Figure 3-11: Voltage vs Time Graph of 40 Readings Taken at 0.1PLC, 1 PLC, and 10 PLC.

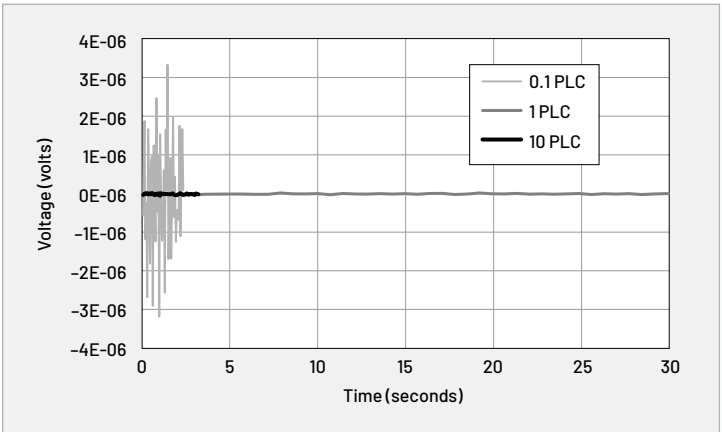


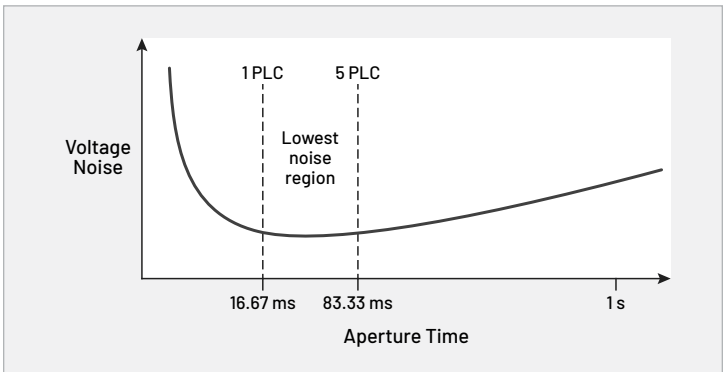
Table 3-2 summarizes the total test time and standard deviation of the 40 voltage readings taken at the different PLC values. The standard deviation of the 1 and 10 PLC readings was of similar magnitude, but the total test time of 10 PLC was 10 times longer than 1 PLC. The total test time between 0.1 and 1 PLC were similar but the noise of the 0.1 PLC readings was about 100 times greater. In this case, 1 PLC provided the optimal speed vs time trade-off.

Table 3-2. Total Test Time and Standard Deviation of 40 Readings Taken at 0.1, 1, and 10 PLC.

PLC	Total Test Time (s)	Standard Deviation
0.1	2.3	1.6E-6
1	3.1	1.9E-8
10	30	1.3E-8

Too long a measurement time can also be detrimental to the measurement because of system and internal instrument drift. **Figure 3-12** shows a parabola-like shaped curve of the voltage noise vs measurement time of a nanovoltmeter. In this graph the optimized measurement window is between 1 PLC and 5 PLC. The voltage noise begins to increase when the measurement time exceeds 83.33 ms (5 PLC).

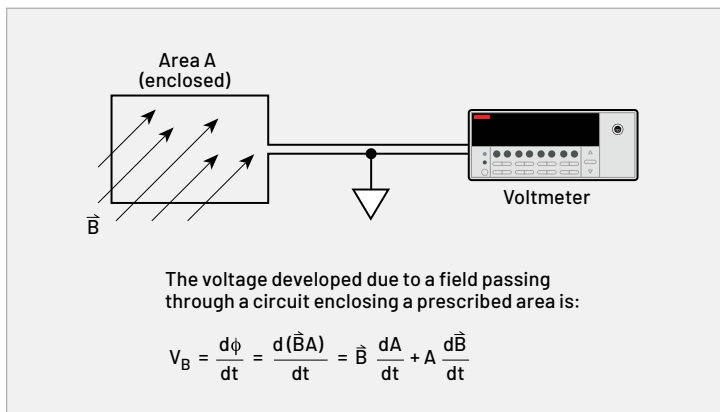
Figure 3-12. Voltage Noise vs Measurement Time



3.2.9 Magnetic Fields

Magnetic fields generate error voltages in two circumstances: 1) if the field is changing with time, and 2) if there is relative motion between the circuit and the field. Voltages in conductors can be generated from the motion of a conductor in a magnetic field, from local AC currents caused by components in the test system, or from the deliberate ramping of the magnetic field, such as for magneto-resistance measurements. Even the earth's relatively weak magnetic field can generate nanovolts in dangling leads, so leads must be kept short and rigidly tied down.

FIGURE 3-13: Low Voltages Generated by Magnetic Fields



Basic physics shows that the amount of voltage a magnetic field induces in a circuit is proportional to the area the circuit leads enclose and the rate of change in magnetic flux density, as shown in **Figure 3-13**. The induced voltage (V_B) is calculated as follows:

$$V_B = \frac{d\phi}{dt} = \frac{d(\vec{B}A)}{dt} = \vec{B} \frac{dA}{dt} + A \frac{d\vec{B}}{dt}$$

where: V_B = induced voltage

A = loop area

$\vec{B}$ = magnetic flux density

$\phi = \vec{B}A$ = magnetic flux

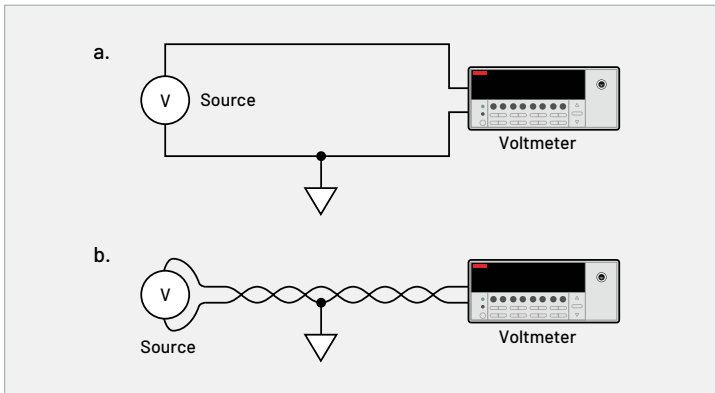
The induced voltage is proportional both to the magnitude of A and $\vec{B}$, as well as to the rate of change in A and $\vec{B}$, so there are two ways to minimize the induced voltage:

- Keep both A and $\vec{B}$ to a minimum by reducing loop area and avoiding magnetic fields, if possible; and
- Keep both A and $\vec{B}$ constant by minimizing vibration and movement, and by keeping circuits away from AC and RF fields.

To minimize induced magnetic voltages, leads must be run close together and magnetically shielded and they should be tied down to minimize movement. Mu-metal, a special alloy with high permeability at low magnetic flux densities and at low frequencies, is a commonly used magnetic shielding material.

Figure 3-14 shows two ways of locating the leads from the source to the voltmeter. In **Figure 3-14a**, a large area is enclosed; thus, a large voltage is developed. In **Figure 3-14b**, a much smaller area is enclosed because the leads are twisted together, and the voltage induced is considerably

FIGURE 3-14: Minimizing Interference from Magnetic Fields



reduced. Twisted pair also cancels magnetically induced voltages because each adjacent twist couples a small but alternating polarity (equal) voltage.

Conductors that carry large currents should also be shielded or run as twisted pairs to avoid generating magnetic fields that can affect nearby circuits. In addition to these techniques, AC signals from magnetic fields can be filtered at the input of the instrument. If possible, the signal source and the instrument should be physically relocated further away from the interfering magnetic field.

3.2.10 Ground Loops

Noise and error voltages also arise from ground loops. When there are two connections to earth, such as when the source and measuring instruments are both connected to a common ground bus, a loop is formed as shown in **Figure 3-15a**. A voltage (V_G) between the source and instrument grounds will cause a current (I) to flow around the loop. This current will create an unwanted voltage in series with the source voltage. From Ohm's Law:

$$V_G = IR$$

where V_G = ground loop interfering voltage, R = the resistance in the signal path through which the ground loop current flows, and I = the ground loop current. A typical example of a ground loop can be seen when a number of instruments are plugged into power strips on different instrument racks. Frequently, there is a small difference in potential between the ground points. This potential difference can cause large currents to circulate and create unexpected voltage drops.

The cure for such ground loops is to ground all equipment at a single point. The easiest way of accomplishing this is to use isolated power sources and instruments, then find a single, good earth-ground point for the entire system. Avoid connecting sensitive instruments to the same ground system used by other instruments, machinery, or other high power

FIGURE 3-15a: Multiple Grounds (Ground Loops)

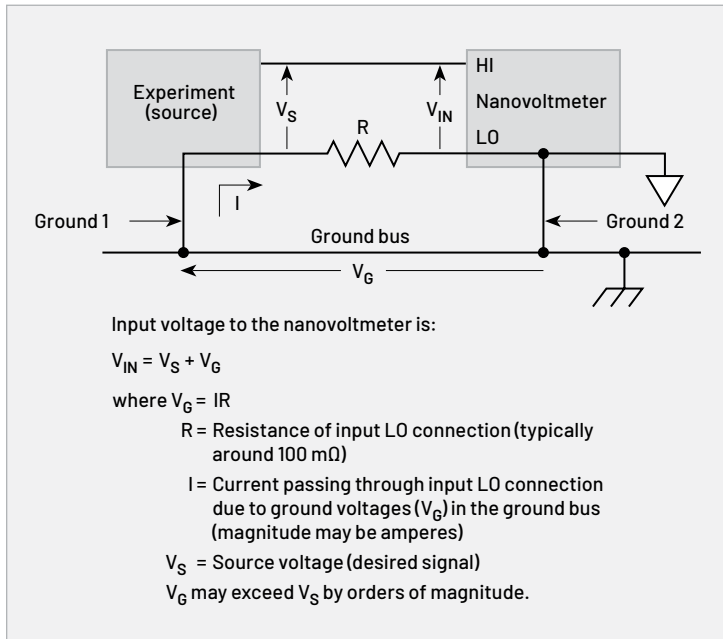
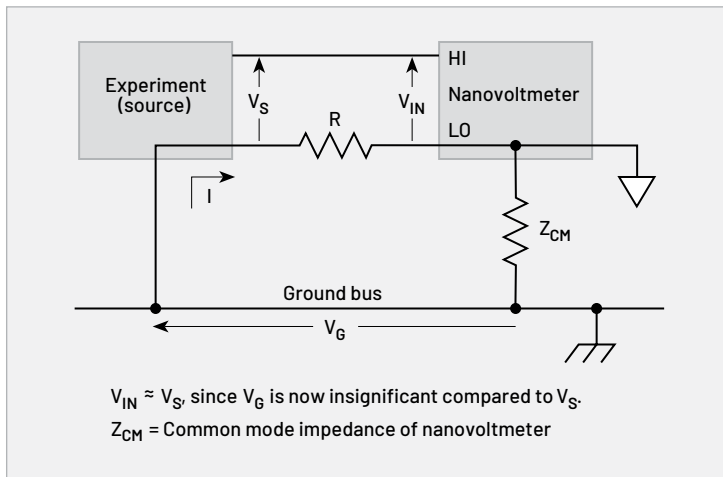


FIGURE 3-15b: Reduced Ground Loops



equipment. As shown in **Figure 3-15b**, ground loops can also be reduced by using a voltmeter with high common mode impedance (Z_{CM}), also known as common mode isolation.

3.2.11 Ways to Reduce Noise for Low Impedance Measurements

The best ways to reduce the impact of external noise sources are to isolate the measurement setup from the noise sources whenever possible, use proper shielding, and use filtering.

Eliminating Noise Sources

If possible, avoid operating the voltmeter and test circuitry in electrically noisy environments. Keep the measuring instrument, connecting cables and the device under test as far away as possible from noise sources such as power cables and power transformers.

Shielding

Proper shielding of all signal paths and sources being measured is important to minimize noise pickup in a low-level measurement application. In order to minimize noise, a closed metal shield, surrounding the DUT, may be necessary (see **Figure 3-6**). The shield should be connected to input LO in most cases, although better noise performance may result with the shield connected to chassis ground in some situations. Use only shielded cables for the test connections.

Using Filtering to Reduce Noise

Even with careful shielding and test methods, external noise may still be a problem. Also, noise generated by the DUT itself such as thermal and $1/f$ noise may be a factor. In such cases, it may be necessary to use the voltmeter's analog or digital filters to reduce noise to an acceptable level.

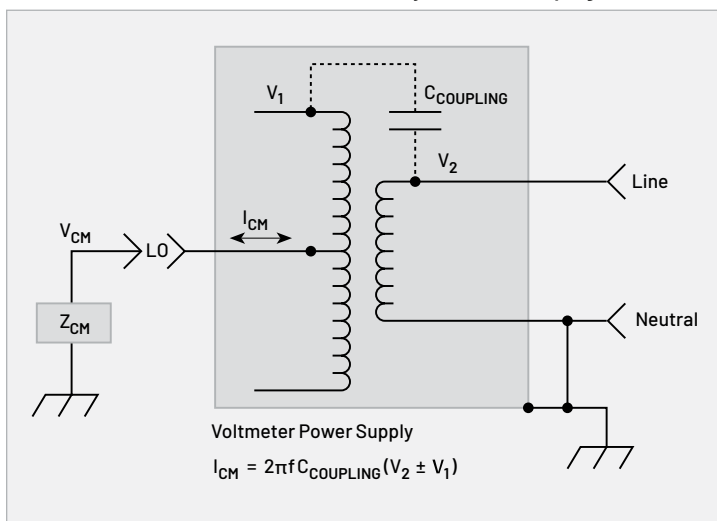
Although filtering reduces reading noise, there are certain disadvantages to using it: reading rates may be reduced, and instrument step response times (response to sudden changes in the input signal) are increased. In general, increasing the amount of filtering slows down the overall measurement speed because of lower reading rates and longer step response times.

Often, some compromise between acceptable noise levels and reading rates and settling times is required. Some experimentation may be required to determine the optimum amount of filtering.

3.2.12 Common-Mode Current and Reversal Errors

Excessive common-mode current can significantly affect low-level voltage measurements. Although common-mode currents are most often associated with noise problems, they can result in large DC offsets in some cases. In the following paragraphs, we will briefly discuss the basic principles behind errors generated by common-mode currents and ways to avoid lead reversal errors.

FIGURE 3-16: Common Mode Current Generation by Power Line Coupling



Common-Mode Current

Common-mode current is the current that flows between the instrument's LO terminal and chassis or earth ground. As shown in **Figure 3-16**, common-mode current (I_{CM}) is caused by capacitive coupling ($C_{COUPLING}$) from the power line through the power transformer. The amplitude of the common-mode current is defined as:

$$I_{CM} = 2\pi f C_{COUPLING} (V_2 \pm V_1)$$

where f is the power line frequency.

Note that the common-mode current flows through the impedance (Z_{CM}), which is present between input LO and chassis ground. As a result, the amplitude of voltage (V_{CM}) depends on the magnitude of Z_{CM} as well as the value of I_{CM} .

Common-Mode Reversal Errors

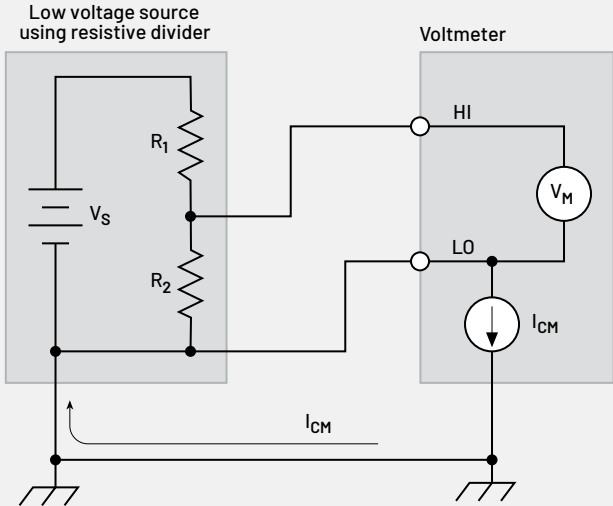
Reversing leads can result in errors caused by common-mode currents. As shown in **Figure 3-17**, many low voltage sources have internal resistive dividers, which attenuate an internal voltage source to the desired level. For example, the output voltage from the source is defined as:

$$V_{OUTPUT} = V_S \left(\frac{R_2}{R_1 + R_2} \right)$$

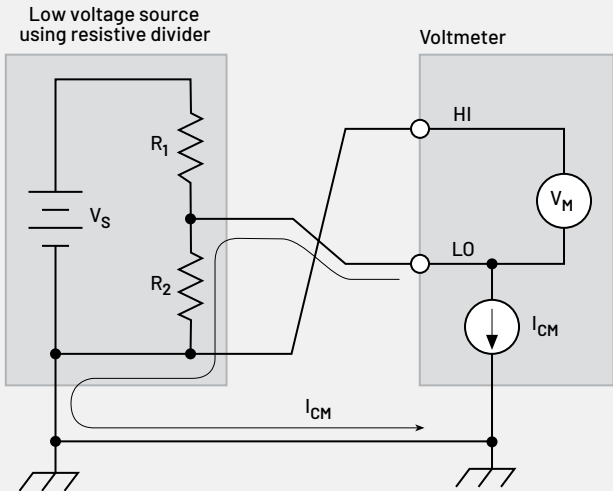
With the correct connection scheme shown in **Figure 3-17a**, the low or chassis side of the voltage source is connected to input LO of the measuring instrument. Any common-mode current (I_{CM}) that may be present flows from the voltmeter input LO to instrument chassis common, through

FIGURE 3-17: Effects of Reversing Leads on Common Mode Errors

a. With proper connections, I_{CM} generates no noise or offset.



b. With reversed connections, I_{CM} generates noise and possible offset.



earth ground to voltage source ground. Note that no common-mode current flows through either of the two divider resistors of the voltage source when this connection scheme is used.

If the input leads of the voltmeter are reversed, we have the situation shown in **Figure 3-17b**. Now, the common-mode current (I_{CM}) flows through R_2 , developing a voltage drop, which is added to the voltage to be measured. This added voltage is mainly power line frequency and its effect on the voltmeter reading will depend upon the normal-mode rejection capability of the meter. The reading may become noisy or it may have a constant offset. In some cases, the sensitivity of the meter may be reduced, because the input stages are overloaded.

To minimize common-mode reversal errors, choose an instrument with the lowest possible common-mode current. If possible, the voltage source being measured should be isolated from ground.

3.3 Low Resistance Measurements

In addition to all the low voltage measurement considerations described in Section 3.2, low resistance measurements are subject to other error sources, including lead resistance, non-ohmic contacts, and device heating. This section describes these error sources and methods to eliminate or minimize them. Other measurement considerations, including dry circuit testing and testing inductive devices, are also described.

3.3.1 Lead Resistance and Four-Wire Method

Resistance measurements are often made using the two-wire method shown in **Figure 3-18**. The test current is forced through the test leads and the resistance (R) being measured. The meter then measures the voltage across the resistance through the same set of test leads and computes the resistance value accordingly.

The main problem with the two-wire method as applied to low resistance measurements is that the total lead resistance (R_{LEAD}) is added to the measurement. Given that the test current (I) causes a small but significant voltage drop across the lead resistances, the voltage (V_M) measured by the meter won't be exactly the same as the voltage (V_R) directly across the test resistance (R), and considerable error can result. Typical lead resistances lie in the range of 1 m Ω to 10 m Ω , so it's very difficult to obtain accurate two-wire resistance measurements when the resistance under test is lower than 10 Ω to 100 Ω (depending on lead resistance).

Due to the limitations of the two-wire method, the four-wire (Kelvin) connection method shown in **Figure 3-19** is generally preferred for low resistance measurements. These measurements can be made using a DMM, micro-ohmmeter, a separate current source and voltmeter, or an SMU. With this configuration, the test current (I) is forced through the test resistance (R) through one set of test leads, while the voltage (V_M) across the DUT is measured through a second set of leads called sense leads.

FIGURE 3-18: Two-Wire Resistance Measurement

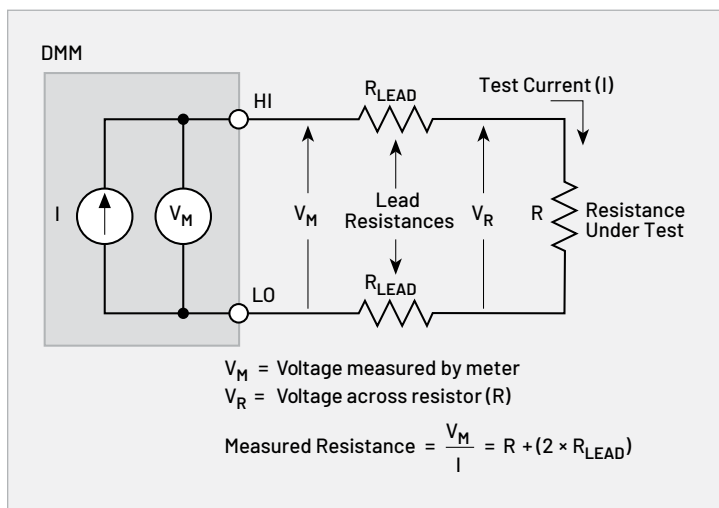
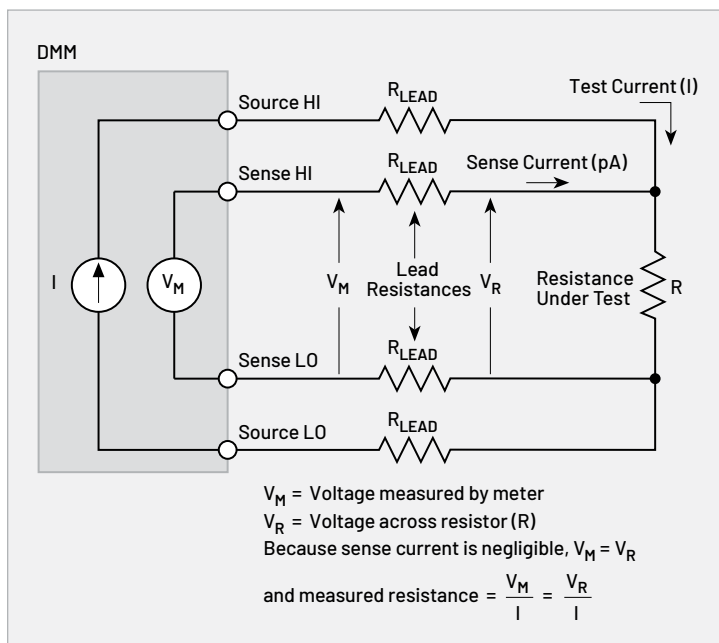


FIGURE 3-19: Four-Wire Resistance Measurement



Although some small current may flow through the sense leads, it is usually negligible and can generally be ignored for all practical purposes. The voltage drop across the sense leads is negligible, so the voltage measured by the meter (V_M) is essentially the same as the voltage (V_R) across the resistance (R). Consequently, the resistance value can be determined much more accurately than with the two-wire method. Note that the voltage-sensing leads should be connected as close to the resistor under test as possible to avoid including the resistance of the test leads in the measurement.

3.3.2 Thermoelectric EMFs and Offset Compensation Methods

Thermoelectric voltages, as described in Section 3.2.2, can seriously affect low resistance measurement accuracy. The current-reversal method, the delta method, and the offset-compensated ohms method are three common ways to overcome these unwanted offsets.

Current-Reversal Method

Thermoelectric EMFs can be canceled by making two measurements with currents of opposite polarity, as shown in **Figure 3-20**. In this diagram, a voltmeter with a separate bipolar current source is used. With the positive current applied as in **Figure 3-20a**, the measured voltage is:

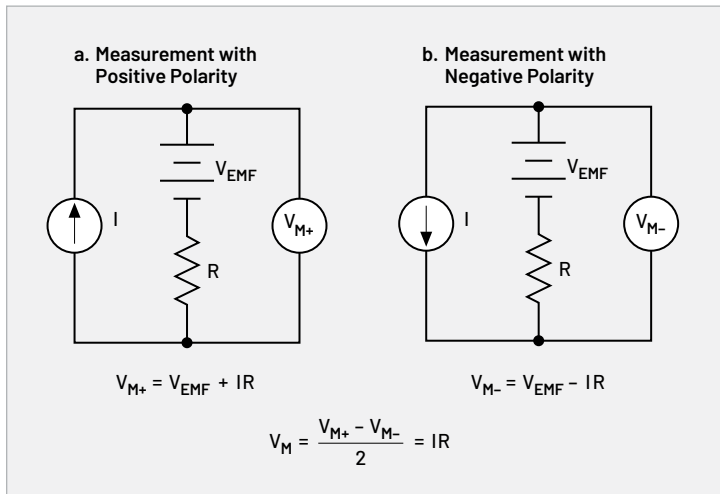
$$V_{M+} = V_{EMF} + IR$$

Reversing the current polarity as shown in **Figure 3-20b** yields the following voltage measurement:

$$V_{M-} = V_{EMF} - IR$$

The two measurements can be combined to cancel thermoelectric EMFs:

FIGURE 3-20: Canceling Thermoelectric EMFs with Current Reversal



$$V_M = \frac{V_{M+} - V_{M-}}{2} = \frac{(V_{EMF} + IR) - (V_{EMF} - IR)}{2} = IR$$

The measured resistance is computed in the usual manner:

$$R = \frac{V_M}{I}$$

Note that the thermoelectric voltage (V_{EMF}) is completely canceled out by this method of resistance calculation.

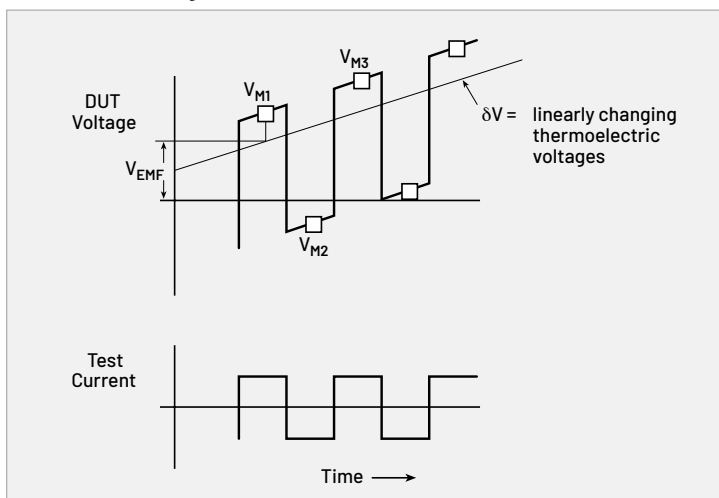
For the current-reversal method to be effective, it's important to use a low noise voltmeter with a response speed that is fast compared with the thermal time constant of the circuit under test. If the response speed is too slow, any changes in the circuit temperature during the measurement cycle will cause changes in the thermoelectric EMFs that won't be completely canceled, and some error will result.

Delta Method

When the thermoelectric voltages are constant with respect to the measurement cycle, the current-reversal method will successfully compensate for these offsets. However, if changing thermoelectric voltages are causing inaccurate results, then the delta method should be used. The delta method is similar to the current-reversal method in terms of alternating the current source polarity, but it differs in that it uses three voltage measurements to make each resistance calculation. This method can best be explained through an illustration and mathematical computations.

Figure 3-21 shows the voltage drop of a DUT as a function of time with an alternating polarity current applied. A voltage measurement

FIGURE 3-21: Canceling Thermoelectric EMFs with Delta Method



(V_{M1} , V_{M2} , V_{M3} , etc.) is taken each time the polarity is changed. Each voltage measurement includes a constant thermal voltage offset (V_{EMF}) and a linearly changing voltage offset (δV). The thermal voltage drift may be approximated as a linear function over short periods, so the rate of change of voltage as a function of time (δV) can also be treated as a constant. The first three voltage measurements include the following voltages:

$$V_{M1} = V_1 + V_{EMF}$$

$$V_{M2} = V_2 + V_{EMF} + \delta V$$

$$V_{M3} = V_3 + V_{EMF} + 2\delta V$$

where: V_{M1} , V_{M2} , and V_{M3} are voltage measurements

V_{M1} is presumed to be taken at time = 0

V_1 , V_2 , and V_3 are the voltage drop of the DUT due to the applied current

V_{EMF} is the constant thermoelectric voltage offset at the time the V_{M1} measurement is taken

δV is the thermoelectric voltage change

Cancellation of both the thermoelectric voltage offset (V_{EMF}) term and the thermoelectric voltage change (δV) term is possible through mathematical computation using three voltage measurements. First, take one-half the difference of the first two voltage measurements and call this term V_A :

$$V_A = \frac{V_{M1} - V_{M2}}{2} = \frac{(V_1 + V_{EMF}) - (V_2 + V_{EMF} + \delta V)}{2} = \frac{(V_1 - V_2)}{2} - \frac{\delta V}{2}$$

Then, take one-half the difference of the second (V_{M2}) and third (V_{M3}) voltage measurements and call this term V_B :

$$V_B = \frac{V_{M3} - V_{M2}}{2} = \frac{(V_3 + V_{EMF} + 2\delta V) - (V_2 + V_{EMF} + \delta V)}{2} = \frac{(V_3 - V_2)}{2} - \frac{\delta V}{2}$$

Both V_A and V_B are affected by the drift in the thermoelectric EMF, but the effect on V_A and V_B is equal and opposite. The final voltage reading is the average of V_A and V_B and is calculated as:

$$V_{Final} = \frac{V_A - V_B}{2} = \frac{(V_1 + V_3 - 2V_2)}{4}$$

Notice that both the V_{EMF} and δV terms are canceled out of the final voltage calculation.

In the delta method, each data point is the moving average of three voltage readings. This additional averaging of the voltage measurements means that the data resulting from the delta method has lower noise than the data derived when the current-reversal method is used to calculate it, even when both sets of data are taken over the same time period.

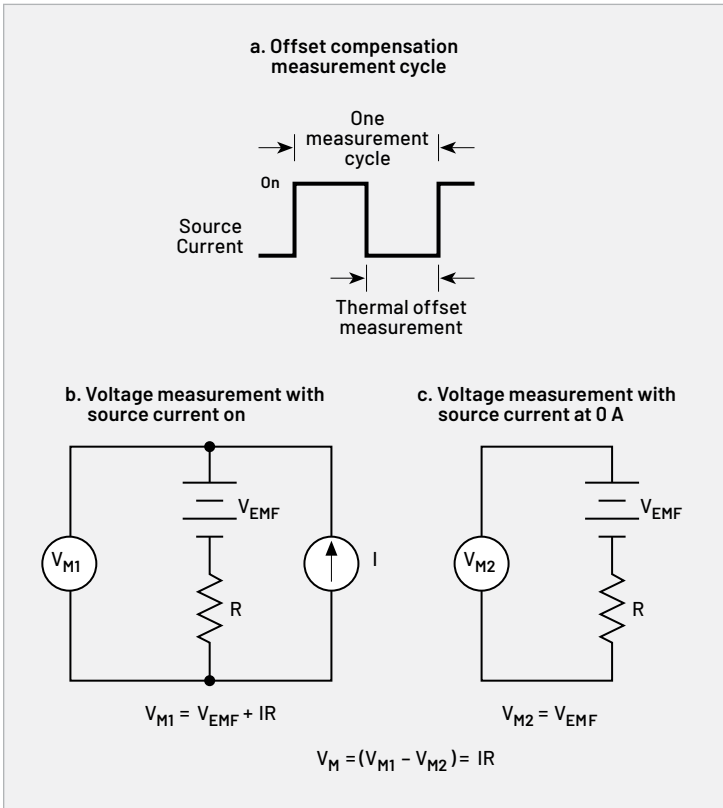
The success of the delta method depends on the linear approximation of the thermal drift, which must be viewed over a short period. Compensating successfully for changing thermoelectric voltages dictates that the measurement cycle time must be faster than the thermal time constant of the DUT. Therefore, an appropriately fast current source and voltmeter must be used for the delta method to be successful. Refer to Section 4.7.2 for information on specific test equipment.

Offset-Compensated Ohms Method

Another offset-canceling method used by micro-ohmmeters and many DMMs is the offset-compensated ohms method. This method is similar to the current-reversal method except that the measurements are alternated between a fixed source current and zero current.

As shown in **Figure 3-22a**, the source current is applied to the resistance being measured during only part of the cycle. When the source

FIGURE 3-22: Offset-Compensated Ohms Measurement



current is on, the total voltage measured by the instrument (**Figure 3-22b**) includes the voltage drop across the resistor as well as any thermoelectric EMFs, and it is defined as:

$$V_{M1} = V_{EMF} + IR$$

During the second half of the measurement cycle, the source current is set to zero amps and the only voltage measured by the meter (**Figure 3-22c**) is any thermoelectric EMF present in the circuit:

$$V_{M2} = V_{EMF}$$

Given that V_{EMF} is accurately measured during the second half of the cycle, it can be subtracted from the voltage measurement made during the first half of the cycle, so the offset-compensated voltage measurement becomes:

$$V_M = V_{M1} - V_{M2}$$

$$V_M = (V_{EMF} + IR) - V_{EMF}$$

$$V_M = IR$$

and,

$$R = \frac{V_M}{I}$$

Again, note that the measurement process cancels the thermoelectric EMF term (V_{EMF}).

3.3.3 Non-Ohmic Contacts

Non-ohmic contacts are evident when the potential difference across the contact isn't linearly proportional to the current flowing through it. Non-ohmic contacts may occur in a low voltage circuit as a result of oxide films or other non-linear connections. A non-ohmic connection is likely to rectify any radio frequency energy (RFI) present, causing an offset voltage to appear in the circuit. (A further discussion on RFI can be found in Section 3.2.5.) There are several ways to check for non-ohmic contacts and methods to reduce them.

If using a micro-ohmmeter or DMM to make low resistance measurements, change the range to check for non-ohmic contacts. Changing the measurement range usually changes the test current as well. A normal condition would indicate the same reading but with higher or lower resolution, depending on whether the instrument was up or down ranged. If the reading is significantly different, this may indicate a non-ohmic condition.

If using a separate current source and voltmeter to make low resistance measurements, each instrument must be checked for non-ohmic contacts. If the current source contacts are non-ohmic, there may be a significant difference in the compliance voltage when the source polarity is reversed. If the voltmeter contacts are non-ohmic, they may rectify any AC pickup present and cause a DC offset error. If this is the case, the

offset compensated ohms method is preferred to the current-reversal method for canceling offsets.

To prevent non-ohmic contacts, choose an appropriate contact material, such as indium or gold. Make sure the compliance voltage is high enough to avoid problems due to source contact non-linearity. To reduce error due to voltmeter non-ohmic contacts, use shielding and appropriate grounding to reduce AC pickup.

3.3.4 Device Heating

Device heating can be a consideration when making resistance measurements on temperature-sensitive devices such as thermistors or nanoscale devices. The test currents used for low resistance measurements are often much higher than the currents used for high resistance measurements, so power dissipation in the device can be a consideration if it is high enough to cause the device's resistance value to change.

Recall that the power dissipation in a resistor is given by this formula:

$$P = I^2R$$

From this relationship, we see that the power dissipated in the device increases by a factor of four each time the current doubles. Thus, one way to minimize the effects of device heating is to use the lowest current possible while still maintaining the desired voltage across the device being tested. If the current cannot be reduced, use a narrow current pulse and a fast responding voltmeter.

Most micro-ohmmeters and DMMs don't have provisions for setting the test current. It is generally determined by the range. In those cases, alternate means must be found to minimize device heating. One simple but effective way to do so is to use the instrument's one-shot trigger mode during measurements. When in this mode, the instrument will apply only a single, brief current pulse to the DUT during the measurement cycle, thereby minimizing errors caused by device heating. Some instruments, like SMUs, may have a pulsed mode for source current to minimize heating effects.

3.3.5 Dry Circuit Testing

Many low resistance measurements are made on devices such as switches, connectors, and relay contacts. If these devices are to be used under "dry circuit" conditions, that is, with an open-circuit voltage less than 20 mV and a short-circuit current less than 100 mA, the devices should be tested in a manner that won't puncture any oxide film that may have built up on the contacts. If the film is punctured, the measured contact resistance will be lower than if the film remains intact, compromising the validity of the test results.

To avoid oxidation puncture, such measurements are usually made using dry circuit testing, which typically limits the voltage across the DUT to 20 mV or less. Some micro-ohmmeters and DMMs have this capability

built in, as shown in **Figure 3-23**. In this micro-ohmmeter, a precision shunt resistor (R_{SH}) is connected across the source terminals to clamp or limit the voltage across the DUT to <20 mV. The remaining aspects of the circuit are very similar to the conventional four-wire measurement method: V and R_{REF} make up the current source, which forces current through the unknown resistance (R). This current should be no more than 100 mA. The value of the unknown resistance is computed from the sense voltage (V_M), the voltage across clamping resistor (V_{SH}), the known value of R_{SH} , and the source current.

If dry circuit testing is to be done with a separate current source and voltmeter or an SMU, the compliance voltage, or source limit, on the current source must be limited to 20 mV or less. If it isn't possible to limit the compliance voltage to this level, a compliance limiting resistor must be used, as shown in **Figure 3-24**. In this circuit, R_C is the resistor used to limit the voltage to 20 mV and R is the unknown resistance.

FIGURE 3-23: Dry Circuit Testing

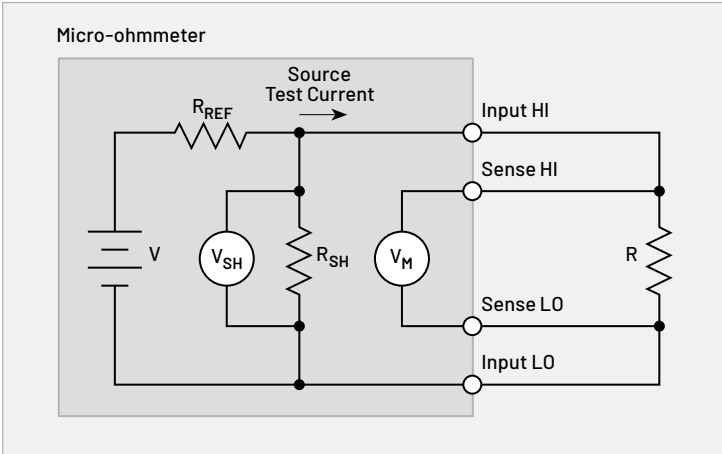
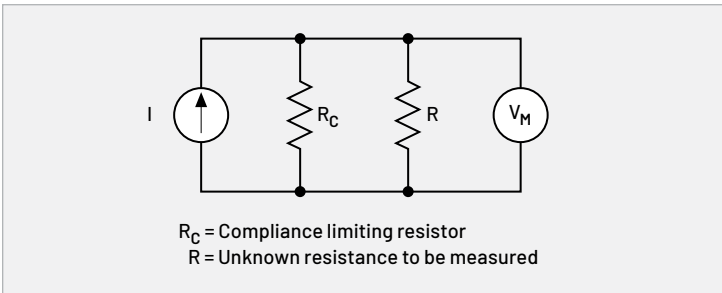


FIGURE 3-24: Dry Circuit Testing Using Current Source and Voltmeter



The value of R_C must be chosen to limit the voltage at a given test current. For example, if the voltage limit is 20 mV and the test current is 200 μ A, R_C can be calculated as:

$$R_C = 20 \text{ mV} / 200 \text{ } \mu\text{A} = 100 \text{ } \Omega$$

If the unknown resistance (R) is 250 m Ω , then R_C will cause a 0.25% error in the measured resistance.

The exact value of the unknown resistance (R) can then be calculated by the following equation:

$$R = \frac{(R_{\text{MEASURED}} \times R_C)}{(R_C - R_{\text{MEASURED}})}$$

where R_{MEASURED} is the calculated resistance measurement from the measured voltage (V_M) and the source current (I).

3.3.6 Testing Inductive Devices

Inductive devices usually have a small resistance in addition to the inductance. This small resistance is normally measured with a DMM or a micro-ohmmeter. However, the measurements are often difficult because of the interaction between the inductance and the measuring instrument. This is particularly true with high L/R ratios.

Some of the problems that may result include oscillations, negative readings and generally unstable readings. An oscilloscope picture of an unstable measurement of a 200 H inductor is shown in **Figure 3-25**.

When problems occur, try to take measurements on more than one range and check if the values correspond.

FIGURE 3-25: Unstable Measurement of 200 H Inductor



If possible, do not use offset compensation (pulsed current) because inductive reaction to the current pulse may cause unstable measurements or make autoranging difficult. Try using a higher resistance range when possible.

Check for oscillations by connecting an oscilloscope in parallel with the device and the meter. Sometimes, a diode across the inductor may settle down the oscillations by reducing the inductive kick.

3.4 Measurement Optimization Summary for Low Impedance Measurements

Section 3 describes various low impedance measurement techniques, sources of error and ways to reduce them. In summary, here are some steps to ensure making optimal low impedance measurements:

- **Choose an instrument sensitive enough to measure low voltage or low resistance: sensitive DMM, micro-ohmmeter, nanovoltmeter, or a combination of a sensitive voltmeter with a current source.** Understand the manufacturer's instrument specifications including the accuracy. Allow the instrument to warm-up for at least an hour and make sure the instrument has periodic recalibration to ensure it is within specification.
- **Ensure cables, connectors, and test fixtures are appropriate for low impedance measurements.** Use shielded cables and follow suggestions for minimizing thermoelectric EMFs in the test circuit.
- **Apply the proper measurement techniques to minimize measurement errors.** Some of these techniques include avoiding thermoelectric EMFs, eliminating noise sources, using offset compensation, increase filtering and line cycle integration, etc.
- **Verify the capabilities of the test system.** Verification procedures for individual instruments are usually described in the reference manual of the instrument. One of the first steps in setting up a system for making low voltage and/or low resistance measurements is to determine the offsets of the measurement system. This identifies the noise floor limit of the entire system and sets a starting point for troubleshooting potential problems and making improvements to the system, if necessary. The remainder of this section outlines simple verification tests and error symptoms for both low voltage and low resistance measurements along with possible causes and ways to minimize these errors.

Low Voltage Measurements

To verify the performance of a nanovoltmeter, it is best to follow the instructions described in the instrument's manual. However, here are steps for a simple way to check the performance of a sensitive voltmeter:

1. Turn on the power and allow the instrument to warm up for the time specified in the service manual.
2. Place an appropriate short on the input of the voltmeter as suggested in the manual of the instrument.
3. Set the voltmeter to the most sensitive voltage range and zero or REL the meter.
4. Verify the voltage is within specification.

Table 3-3 lists potential error symptoms of low voltage measurements along with possible causes and ways to minimize these errors.

Low Resistance Measurements

Low resistance measurements should be verified with a low resistance of known value that is similar in magnitude to the resistance under test, if possible. **Table 3-4** lists potential error symptoms of low resistance measurements along with possible causes and ways to minimize these errors. In addition, error sources for low voltage measurements should be reviewed.

TABLE 3-3: Potential Error Symptoms of Low Voltage Measurements

Error Symptom	Possible Causes	How to Minimize or Avoid	Refer to Section
Noisy readings	Thermoelectric EMFs	Construct circuits using same type of conductive materials. Minimize temperature gradients. Allow test equipment to warm up.	3.2.2
	Line Cycle Interference	Identify and remove offending source, if possible. Use noise shields. Use integer value of line cycle integration (e.g., 1, 2, etc.).	3.2.8
	RFI/EMI	Keep sensitive test circuitry away from interference source. Use noise shields.	3.2.5
	Johnson Noise	Lower temperature of source resistance. Increase instrument filtering. Increase integration time.	3.2.6
	Magnetic Fields	Reduce loop area of test leads. Minimize vibration of test circuit.	3.2.9
	Ground Loops	Ground all equipment at a single point.	3.2.10
DC voltage offsets	Internal voltmeter offsets	Use proper zeroing techniques. Make sure voltmeter is in calibration.	3.2.3
	Thermoelectric EMFs	Construct circuits using same type of conductive materials. Minimize temperature gradients. Allow test equipment to warm up.	3.2.2
	RFI/EMI	Keep sensitive test circuitry away from interference source. Use noise shields.	3.2.5
One or two digits of resolution	Range selected is too high or instrument not sensitive enough	Select a lower voltage range, or use autoranging. Use a more sensitive voltmeter. Check instrument specifications.	

TABLE 3-4: Potential Error Symptoms of Low Resistance Measurements

Error Symptom	Possible Causes	How to Minimize or Avoid	Refer to Section
Readings too high	Lead Resistance	Use four-wire method.	3.3.1
	Ohmic Contacts	Use appropriate contact material. Use four-wire method.	3.3.1
	Thermoelectric EMFs	Use an offset compensation method.	3.3.2
	Self Heating Effects	Reduce test current. Use pulsed current.	3.3.4
Readings too high or too low	Thermoelectric EMFs	Use an offset compensation method.	3.3.2
	Non-Ohmic Contacts	Use appropriate contact material. Use four-wire method.	3.3.1
One or two digits of resolution	Range selected is too high or instrument not sensitive enough	Select a lower voltage range, or use autoranging. Use a more sensitive ohmmeter. Increase test current.	

SECTION 4

Applications

4.1 Introduction

The applications for today's low-level measurement instruments are no longer limited to the calibration department or R&D lab. Low-level instruments have proven invaluable in many other areas, including product design, device characterization, quality assurance, and production test. This section offers insights into this growing range of applications and the most appropriate instruments and test techniques to solve specific test and measurement challenges.

Section 4 covers a variety of low-level measurement applications:

- 4.2 Applications for Measuring Voltage from High Resistance Sources: Capacitor dielectric absorption, electrochemical measurements and open circuit voltage of TENG devices.
- 4.3 Low Current Measurement Applications: Capacitor leakage measurements, low current semiconductor (diodes and MOSFETs) measurements, light measurements with photomultiplier tubes, ion beam measurements, and I-V characteristics of CNT FETs.
- 4.4 High Resistance Measurement Applications: Surface insulation resistance testing of printed circuit boards, resistivity measurements of insulating materials, resistivity measurements of semiconductors, voltage coefficient testing of high ohmic value resistors, and resistance of cable insulation.
- 4.5 Charge Measurement Applications: Capacitance measurements and static charge measurements using a Faraday cup.
- 4.6 Low Voltage Measurement Applications: Standard cell comparisons, high resolution temperature measurements, and Hall voltage measurements.
- 4.7 Low Resistance Measurement Applications: Contact resistance, superconductor resistance measurements, and resistivity measurements of conductive materials.

4.2 Applications for Measuring Voltage from High Resistance Sources

Electrometer voltmeters and voltage measurements on high resistance sources were described in Sections 1 and 2 respectively. In particular, Section 2.2 discussed error sources and ways to minimize their effects. Voltage measurements on high impedance sources include applications such as capacitor dielectric absorption, some electrochemical experiments, including pH measurements and measurements with ion-selective electrodes, and open circuit voltage of triboelectric nanogenerators.

4.2.1 Capacitor Dielectric Absorption

Overview

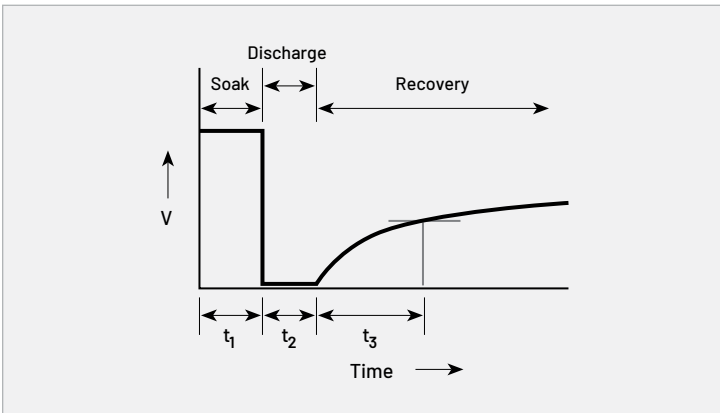
Dielectric absorption occurs when randomly oriented permanent dipoles of molecules within a dielectric are aligned by an applied electric field. After a capacitor is disconnected from a discharge circuit, a residual charge remains on the capacitor, so a voltage is re-established across the capacitor terminals.

For timing and integrating applications, dielectric absorption (or a residual capacitor voltage) can seriously degrade the accuracy of the circuit. Thus, a capacitor's dielectric absorption must be known and compensated for in circuits where capacitance tolerance is a significant factor in circuit accuracy.

Dielectric absorption is measured using a voltage source and a voltmeter with high input impedance. **Figure 4-1** illustrates the basic timing for the test. First, the capacitor is charged to the rated voltage for a specified time period (t_1). Next, the capacitor is discharged for a second time interval (t_2). Then, the capacitor is open-circuited; after a third time interval (t_3), the residual or recovery voltage across the capacitor is measured. The dielectric absorption can then be determined from the recovery voltage:

$$\text{Dielectric Absorption} = \frac{\text{Residual Voltage}}{\text{Soak Voltage}} \times 100\%$$

FIGURE 4-1: Voltage vs. Time in Dielectric Absorption Test



The following paragraphs describe how to make these measurements using both SMUs and electrometers.

Using an SMU to Determine Dielectric Absorption

Residual capacitor voltage can be measured with an SMU, which must source voltage and then measure voltage with a high input impedance. Because an SMU must output 0 A while monitoring voltage, it is important to use an instrument that has the lowest possible input bias current to avoid charging the capacitor while monitoring the recovery voltage. The 4200A-SCS Parameter Analyzer equipped with an SMU and a preamp and the 6430 Sub-Femtoamp Remote SourceMeter SMU both have very high input impedance, as well as low input bias current.

Figure 4-2 shows how the SMU is configured for the three steps of the test. First, in Step 1, the soak voltage is applied across the capacitor for the required soak time (t_1). In Step 2, the SMU is programmed to output 0 V with a source limit, or compliance, of 100 mA for the specified discharge interval (t_2). In Step 3, the SMU is programmed to measure the recovery voltage with an output current of 0 A on the lowest current range possible. The input bias current of the SMU must be significantly lower than the current due to dielectric absorption. The recovery voltage is measured after the prescribed time period (t_3). Finally, the dielectric absorption is calculated from the soak voltage and the recovery voltage.

FIGURE 4-2: Using an SMU to Measure Dielectric Absorption

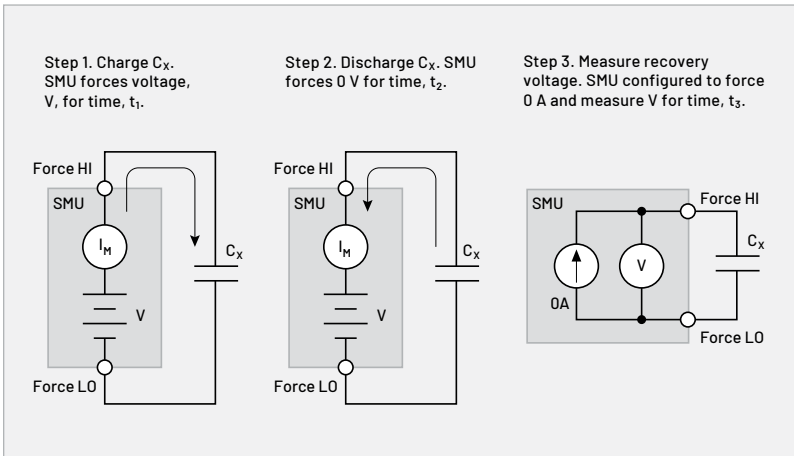
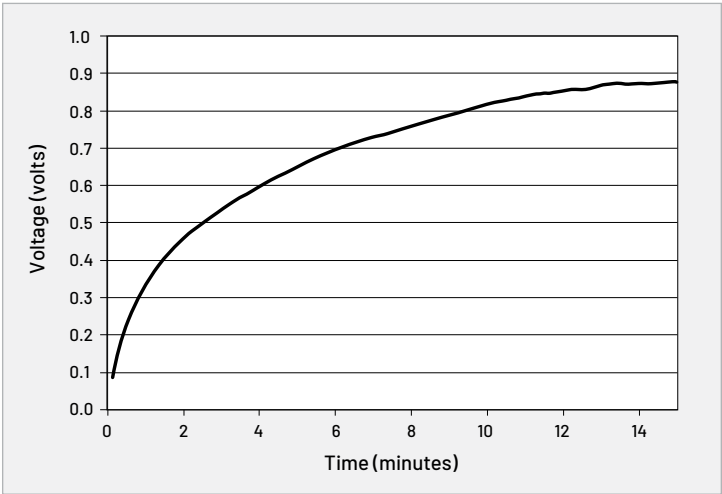


Figure 4-3 shows the results of measuring the recovery voltage of a 1 nF ceramic capacitor as a function of time. These measurements were taken with the 4200A-SCS Parameter Analyzer using an SMU and a preamp, which has an input bias current in the femtoamp range. In this example, a voltage was applied for 60 minutes, the capacitor was discharged for 10 seconds, and the recovery voltage was measured as a function of time for 15 minutes.

FIGURE 4-3: Results of Dielectric Absorption Test of 1nF Ceramic Capacitor Measured by the 4200A-SCS Parameter Analyzer



Using an Electrometer to Determine Dielectric Absorption

An electrometer voltmeter is particularly useful for measuring dielectric absorption because, like an SMU, it draws virtually no charge from the capacitor during the measurement, nor does it put charge on the capacitor being measured.

FIGURE 4-4: Using an Electrometer to Measure Dielectric Absorption

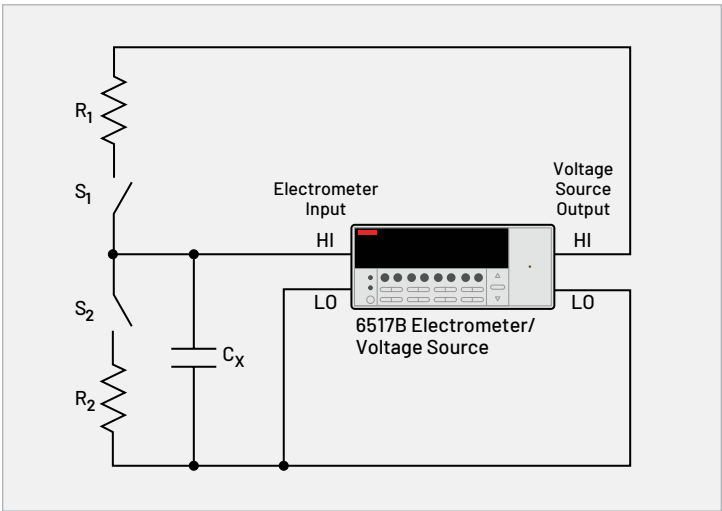


Figure 4-4 illustrates the basic circuit that uses an electrometer to determine dielectric absorption. This application employs a 6517B Electrometer, which can supply the test voltage and measure the residual voltage.

Initially, the capacitor (C_X) is charged through R_1 for the required soak time. Next, the voltage source is turned off; S_1 is opened and S_2 is closed, discharging the capacitor through R_2 for the required discharge time. Next, S_2 is opened and the capacitor must remain undisturbed for the specified recovery time, at the end of which the electrometer voltmeter is used to measure the residual voltage. The dielectric absorption is then calculated using the equation given previously.

4.2.2 Electrochemical Measurements

Overview

To ensure the accuracy of measurements when determining the potentials of electrochemical electrodes or cells, these measurements must be made without drawing appreciable current from the cells. Otherwise, the current drawn from the cell electrodes will cause a voltage drop across the internal resistance of the electrode and may polarize the cell. Therefore, a voltmeter with high input resistance, such as an electrometer, is required.

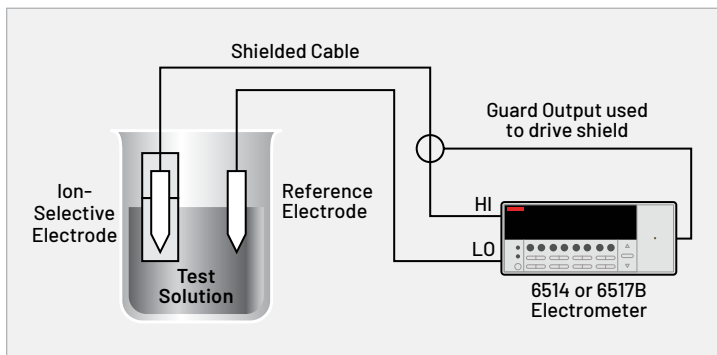
Electrometers are commonly used for pH measurements and other ion-selective electrodes to determine a specific ionic concentration. They are also often used for measuring liquid conductivity. This section discusses a number of measurement fundamentals related to these applications. Keep in mind that these measurements typically require close temperature regulation.

Measurements with Ion-Selective Electrodes

These measurements are particularly useful where continuous measurements of ionic activity are needed. Such monitoring is important to prevent loss of valuable material or to detect possible pollutants in the effluents from industrial processes.

The cell potential of an ion-selective electrode varies directly with the logarithm of ionic activity. At room temperature, the potential of most ion-selective electrodes will change about 57 mV when the activity of an equivalent ion is changed by a factor of ten. This log response enables constant precision over dynamic ranges of ionic activity of up to eight orders of magnitude. **Figure 4-5** shows a typical circuit. Note that the ion-selective electrode usually has higher impedance than the reference electrode and should be connected to the HI terminal of the electrometer input with shielded cable. The shield can be driven by the Guard (Preamp) Output to improve the response speed. With the 6517B or 6514 electrometers, this can be done by either an external connection to the preamp output (as shown) or using the volts guard function on the front panel.

FIGURE 4-5: Ion-Selective Electrode Measurements

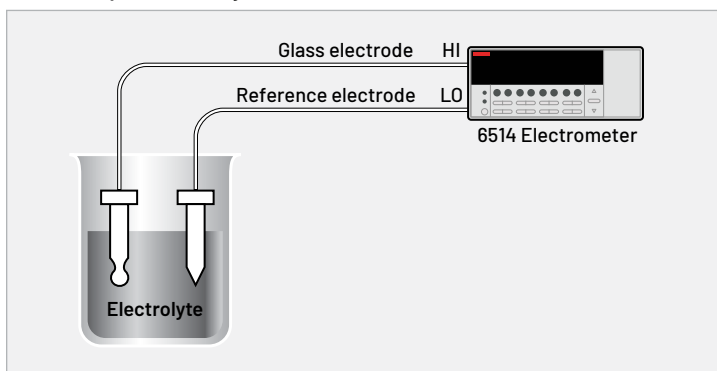


pH Measurements

Any pH electrode system (**Figure 4-6**) can be seen as a large resistor (from 10 M Ω to 1 G Ω) in series with a voltage source. This resistance is the sum of the ion-selective electrode wall (typically glass) and the electrolyte, which has low mobility. The potential in this system cannot be measured with an ordinary DMM.

If current flows, the electrodes will become polarized. Therefore, the electrode potential should be measured with an electrometer, which draws negligible current from the electrodes. The electrometer should not be zero-checked while connected to the glass electrode; the instrument's normally near-infinite input resistance drops to 10 M Ω in zero check and the resulting current flow may polarize the electrodes.

FIGURE 4-6: pH Electrode System

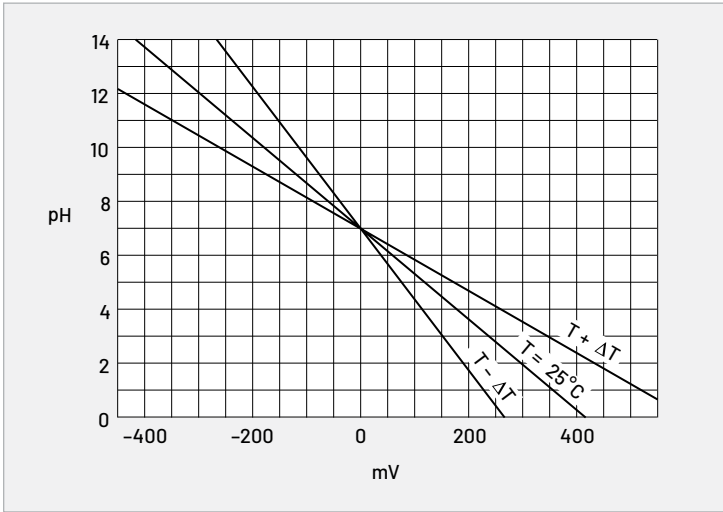


If the approximate pH to be measured is known, the electrodes should be standardized, using two values of buffer solutions to calibrate the system at each end of the desired pH scale. This step is necessary to obtain the best accuracy. For example, to measure from 6.5 pH (29.6 mV at 25°C)

to 1 pH (355 mV at 25°C), it is advisable to use one buffer solution with a pH of six and another with a pH of one.

When placed in the buffer solution, the voltage reading from the electrodes may differ from the theoretical value by several hundred microvolts. The voltage is also highly temperature dependent. For a given cell and temperature, the pH-to-voltage relationship is linear. For example, the theoretical voltage with a pH of 4.0 is 177.5 mV at 25°C, assuming the calomel cell is used as the reference electrode. Other reference electrodes, such as the silver/silver chloride cell, will give a slightly different voltage. The reference electrode doesn't change with pH, so its contribution to the measurement can be corrected for by measuring known buffer solutions. The electrode voltages measured by the electrometer may be converted to pH values by using appropriate conversion data. **Figure 4-7** is a typical plot of millivolt difference versus pH value.

FIGURE 4-7: Electrode Output Voltage at Various pH Values

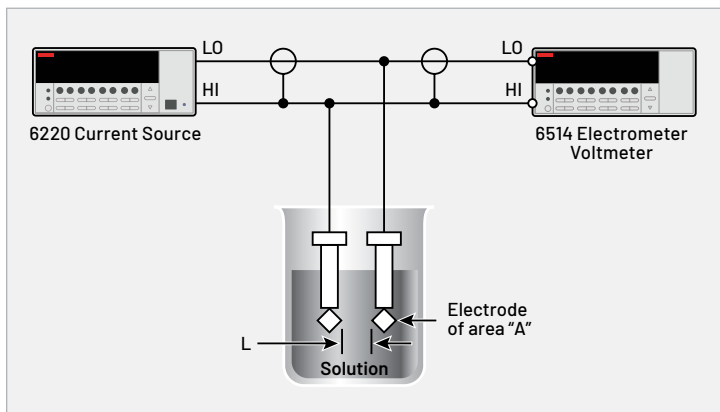


Conductivity Cells

Measuring the electrical conductivity of many chemical solutions is difficult if the ionic concentration is very low. In these instances, an electrometer voltmeter with a current source can be used to make this measurement; **Figure 4-8** shows a typical configuration.

The conductivity of a solution is sensitive to the presence of impurities, so its value is meant to be more an index of impurity than a characteristic constant. Therefore, high accuracy is unnecessary and the test equipment need not be elaborate.

FIGURE 4-8: Conductivity Cell Measurements



As with pH measurements, the current should be kept as low as possible. Its polarity can be alternated to avoid electrode polarization.

The electrodes of the cell must be rigidly mounted to prevent vibration and motion from creating noise and pickup. Additionally, shielding the leads to the electrodes helps reduce interference.

Each cell arrangement has a particular constant, which is a function of the volume of the conducting solution between the electrodes. Electrometers can be very useful where electrode areas are very small and solution conductivities are very low. Temperature control is essential to making reliable measurements.

Conductivity is computed from the known value of current (I), the voltage reading (V), and the area and spacing between the electrodes:

$$\sigma = \frac{I}{V} \cdot \frac{L}{A}$$

where: σ = conductivity (Siemens/cm)

A = surface area of the electrodes (cm^2)

L = distance between the electrodes (cm)

4.2.3 Open Circuit Voltage (V_{oc}), Short-Circuit Current (I_{sc}), and Transferred Charge (Q) Measurements of Triboelectric Nanogenerators (TENGs)

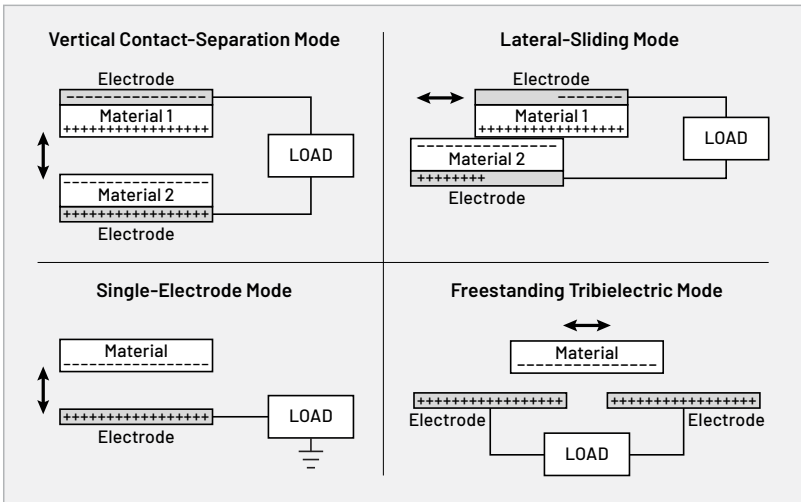
Overview

The triboelectric effect is a phenomenon whereby the generation and transfer of charges are created by contact and friction between two materials. Triboelectric nanogenerators (TENG) are energy-harvesting devices that convert mechanical motion from these materials to electricity using the triboelectric effect. Depending on the application for the TENG device,

the mechanical energy to power the device can come from movements such as walking, pressing, sliding, or tapping. Applications for these devices include self-powered wearables, implantable medical sensors, IoT devices, and environmental monitors.

Figure 4-9 shows the circuit diagrams of the four modes of operation of TENG devices connected to a load. The modes include Vertical Contact-Separation, Lateral-Sliding, Single-Electrode, and Freestanding Triboelectric-Layer Modes. Each mode has its own advantages for energy harvesting. These modes are further defined in the following paragraphs.

Figure 4-9: Four Modes of Operation of TENG Devices



1. Vertical Contact-Separation Mode (press and release): Two dissimilar dielectric materials with back electrodes come into contact and generate opposite charges (press). A potential difference is generated between the two electrodes when they are separated (release).
2. Lateral-Sliding Mode: Two dissimilar dielectric materials with back electrodes slide against each other horizontally. The frictional sliding induces a potential difference between the two electrodes.
3. Single-Electrode Mode: This mode involves the motion of only one material with one electrode. This moving material, or object such as a human finger, acts as the triboelectric layer. The one fixed electrode is connected to ground. A potential difference occurs with the contact and separation of the mobile material with the electrode.
4. Freestanding Triboelectric-Layer Mode: This mode involves the motion of one material with two electrodes. A charged, triboelectric material moves back and forth between the two electrodes causing a change in electrostatic balance.

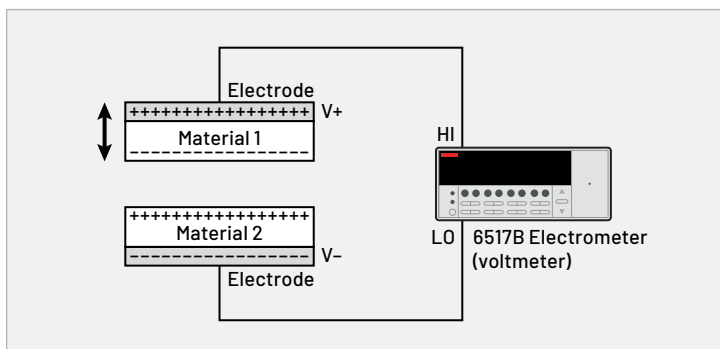
Electrically characterizing the TENG outputs includes measuring the open-circuit voltage, current density, and the transferred charge. An electrometer can measure voltage using a high input impedance, low current, and charge making them the ideal instrument for measuring the multiple parameters of the TENG outputs.

Measuring Open-Circuit Voltage (Voc)

TENG devices have very high internal impedance, typically ranging from $10^7 \Omega$ to greater than $10^9 \Omega$. Using a typical DMM, which often has an input impedance in the $M\Omega$ range, may cause loading effects when measuring the open-circuit voltage (Voc) of the device. Measuring the open circuit voltage (Voc) of TENG devices requires a very high input impedance voltmeter, such as an electrometer, to prevent loading errors and charge leakage of the device. Both the 6517B and 6514 Electrometers have high input impedance ($>2 \times 10^{14} \Omega$) making these instruments ideal choices for these measurements because they can be used for measuring voltage under open circuit conditions.

As shown in **Figure 4-10**, the HI and LO input terminals of the 6517B Electrometer are connected directly to the electrodes of a Vertical Contact-Separation TENG device. The voltage is measured while subjecting the device to motion (press and release).

Figure 4-10: Using an Electrometer to Measure the Output Voltage of a Vertical Contact-Separation TENG Device



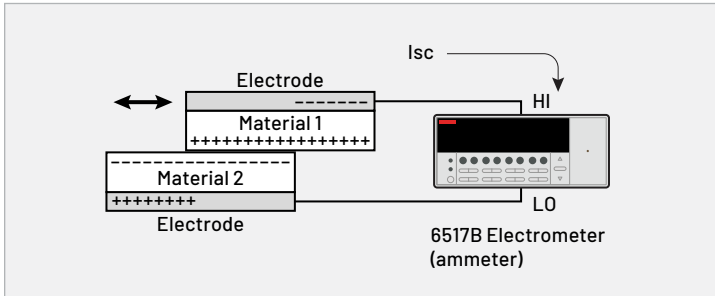
Measuring the Short-Circuit Current (Isc)

The magnitude of the current output, often referred to as the short-circuit current, of the TENG device varies by the type of design, operating mode, type of movement and the amount of force applied to the device. Typical magnitudes are in the nanoamp to microamp ranges. Since the electrometer is a multi-function instrument, it can also be used to measure the current and charge density of the device.

Figure 4-11 shows an electrometer, in the current measurement mode, connected to the output of the Lateral Sliding TENG device. In this case the

friction due to sliding of the two dielectric materials causes a current to flow and is measured by the electrometer.

Figure 4-11: Using an Electrometer to Measure the Short-Circuit Current of a Lateral Sliding TENG Device



Measuring the Transferred Charge (Q)

The transferred charge (Q) of a TENG device is the amount of charge that flows through the external circuit during the mechanical motion. The two terminals of the TENG device are connected directly to the HI and LO terminals of the electrometer. The electrometer measures the charge as the device operates under a controlled mechanical excitation. Depending on the method used, the electrometer can be used in the coulombmeter, ammeter, or voltmeter modes to measure the transferred charge. These methods are described in the following paragraphs.

1. **Electrometer/Coulombmeter Method:** The electrometer is used in the charge(Q) measurement mode(coulombmeter) to directly integrate the short-circuit current between the two electrodes. This directly measures the charge transferred during the mechanical operation.
2. **Current Integration Method:** Connect the electrometer in the ammeter (I) mode to measure the short-circuit current. The total charge (Q) can be derived by integrating the current as a function of time:

$$Q = \int I dt$$

3. **Capacitor Voltage Method:** Connect a large capacitor (C), in the 100 nF to 10 μ F range, in parallel with the TENG device. The electrometer, in the voltage (V) mode, measures the voltage drop across the capacitor. The transferred charge (Q) is derived from the equation:

$$Q = C \times V$$

4.3 Low Current Measurement Applications

Electrometer ammeters, picoammeters, SMUs, and methods of making low current measurements were described in Sections 1 and 2 respectively. In particular, Section 2.3 discussed a number of error sources that can seriously affect measurement accuracy. The following sections discuss

several low current measurement applications in more detail: capacitor leakage measurements, low current semiconductor device (diodes and MOSFETs) measurements, light measurements with photomultiplier tubes, ion beam measurements, and I-V characteristics of CNT FETs. To ensure greater accuracy in these measurements, be sure to use the low current optimization techniques discussed in previous sections.

4.3.1 Capacitor Leakage Measurements

Overview

Capacitors are essential components of virtually every type of electronic equipment. They are widely used for bypassing, coupling, filtering, and tuning electronic circuits. However, to be useful, they must be characterized for capacitance value, voltage rating, temperature coefficient, and leakage current or resistance. The capacitor manufacturer performs these tests; end users may also perform them. Depending on the device under test, the leakage current is typically very small, usually <10 nA.

This application focuses on the measurement of leakage resistance using either a 6487 Picoammeter/Source, a 6517B Electrometer/SourceMeter, or a 2450 Graphical SourceMeter SMU. This leakage resistance may be referred to as “IR” (Insulation Resistance) and is expressed in megohm-microfarads (the resistance may be computed by dividing the “IR” value by the capacitance). In other cases, leakage may be expressed as a leakage current at a given voltage, usually the operating voltage.

Description of Test Method Using a Picoammeter or Electrometer

Capacitor leakage is measured by applying a fixed voltage to the capacitor under test and measuring the resulting current. The leakage current will decay exponentially with time, so it's usually necessary to apply the voltage for a known period (the “soak” time) before measuring the current.

FIGURE 4-12: Simple Capacitor Leakage Test Circuit

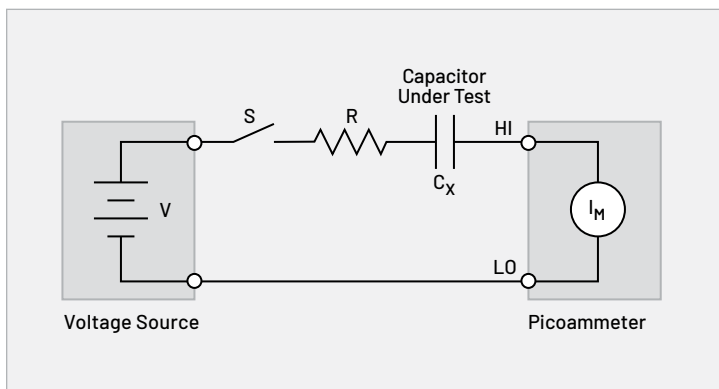
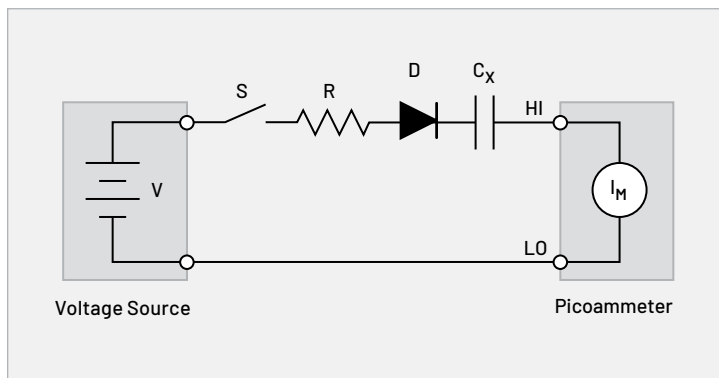


Figure 4-12 depicts a general circuit for testing capacitor leakage using a picoammeter and a voltage source. Here, the voltage is placed across the capacitor (C_X) for the soak period, then the ammeter measures the current after this period has elapsed. The resistor (R), which is in series with the capacitor, is an important component in this test system. The resistor has two functions:

1. The resistor limits the current in case the capacitor becomes shorted.
2. As discussed in Section 2.3.2, the decreasing reactance of the capacitor with increasing frequency will increase the gain of the feedback ammeter. The resistor limits this increase in gain to a finite value. A reasonable value is one that results in an RC product from 0.5 to 2 seconds.

Even better performance will result if a forward-biased diode (D) is included in the circuit, as shown in **Figure 4-13**. The diode acts like a variable resistance, low when the charging current to the capacitor is high, then increasing in value as the current decreases with time. The series resistor can be much smaller because it is only needed to prevent overload of the voltage source and damage to the diode if the capacitor becomes short-circuited. The diode should be a small signal diode, such as 1N914 or 1N3595, and must be in a light-tight enclosure. For dual-polarity tests, use two diodes back-to-back in parallel.

FIGURE 4-13: Capacitor Leakage Test Circuit with Diode

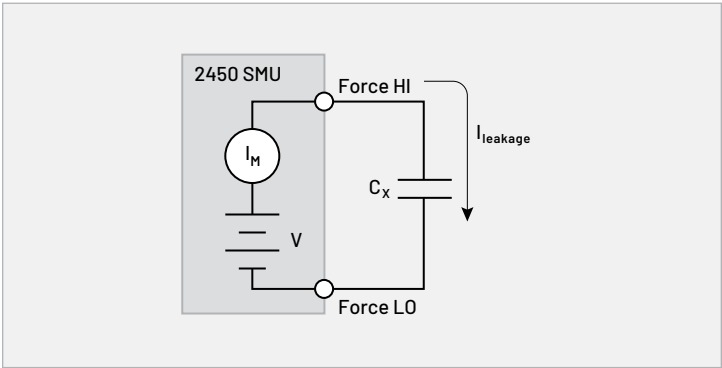


Using an SMU to Measure Capacitor Leakage

The same technique used to perform capacitor leakage testing with a picoammeter or electrometer can be used when testing with an SMU. However, in the case of the SMU, the ammeter is internally connected to the voltage source, as shown in **Figure 4-14**. The current limiting resistor is no longer necessary because a source limit, or compliance, value is set to limit the current.

If the current readings are noisy, then a forward-biased diode should be added in series with the capacitor under test to reduce the noise gain.

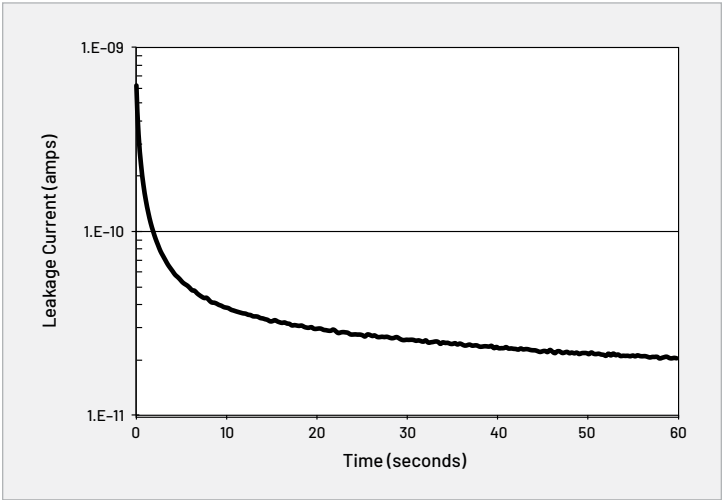
FIGURE 4-14: SMU Connections for Capacitor Leakage



Some SMUs, like the 2450 Graphical SourceMeter SMU, have a high capacitance mode that can be enabled to make stable leakage current measurements. In this case, the series diode may not be necessary.

Figure 4-15 illustrates the results of measuring the leakage current of a 10 μF ceramic capacitor as a function of time. These current measurements were taken with the 2450 SourceMeter SMU using a 1N3595 diode in series with the capacitor to reduce the noise gain.

FIGURE 4-15: Leakage Current Measurements of a 10 μF Capacitor Taken with a 2450 Graphical SourceMeter SMU



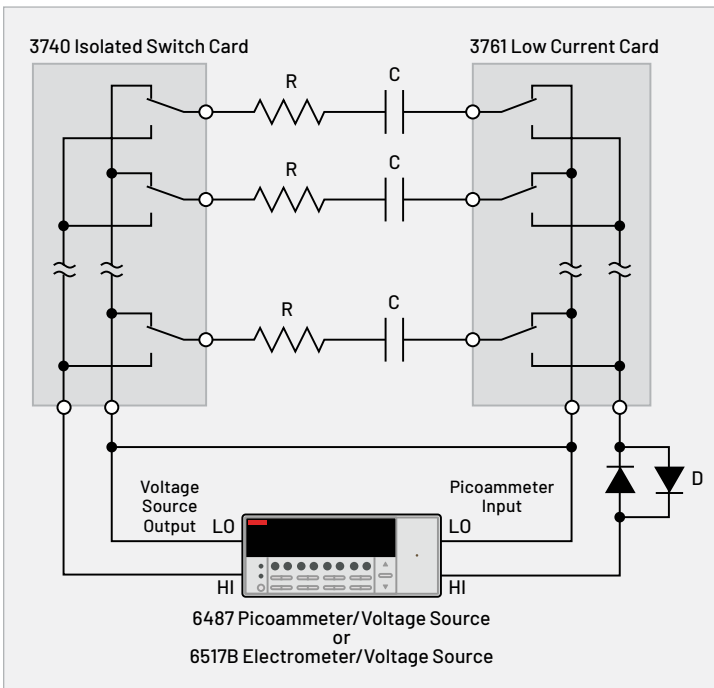
Test Circuit for Testing Multiple Capacitors

For statistical purposes, a quantity of capacitors is often tested to produce useful data. Obviously, it is impractical to perform these tests manually, so some sort of automated test system is required. **Figure 4-16** illustrates such a system, which employs a 6487 Picoammeter/Voltage Source, 3761 10 Channel Low Current Multiplexer Card, and 3740 32 Channel Isolated switch card, which has 28 form C relays. These cards are installed in the 3706A System Switch, which controls the relays. These instruments can be controlled remotely for automated testing.

In this test system, a single instrument, the 6487 Picoammeter/ Source, provides both the voltage sourcing and low current measurement functions. This instrument is particularly useful for this application because it can display either resistance or leakage current and will source DC voltage. The 6517B can also be used in this system for lower current measurements.

Depending on the polarity of the voltage source, one of the two diodes (D) in parallel is used to reduce noise while the other provides a discharge path. The normally closed contact of the 3740 serves to discharge the capacitor after it has been measured.

FIGURE 4-16: Capacitor Leakage Test System



One set of switches is used to apply the test voltage to each capacitor in turn; a second set of switches connects each capacitor to the picoammeter after a suitable soak period.

4.3.2 Low Current Diode Measurements

Overview

The current-voltage (I-V) characterization of diodes often involves measuring very small currents while sourcing voltage. Because of their wide range of current measuring capability, SMUs are often used to make these measurements. This section describes how to measure diode current with forward bias as well as the reverse leakage at high voltage.

Forward Bias I-V Characterization of a Diode

The forward bias current of a diode spans many decades. The measuring instrument must be configured using a Kelvin (four-wire) technique to avoid unwanted voltage drops in the test leads while measuring the diode current, which can be in the milliamp range when forward biased. The use of low current measurement techniques is also critical. **Figure 4-17** shows the test configuration for measuring the forward bias current of a diode using a 2450 Graphical SourceMeter SMU. The 2450 can measure currents from picoamps to 1 A, as well as sweep the bias voltage.

FIGURE 4-17: Connections For Performing Forward Bias I-V Characterization of a Diode

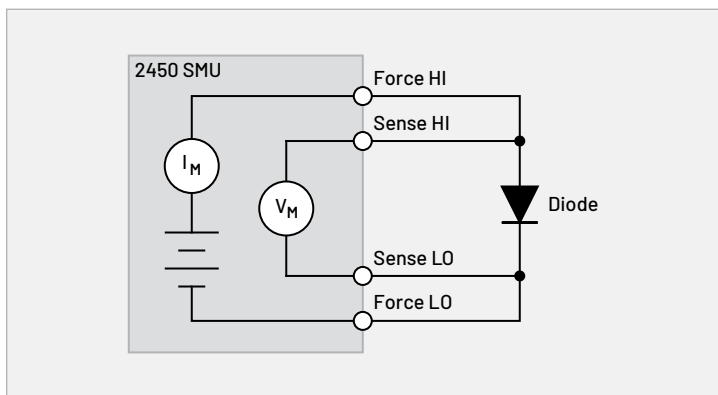
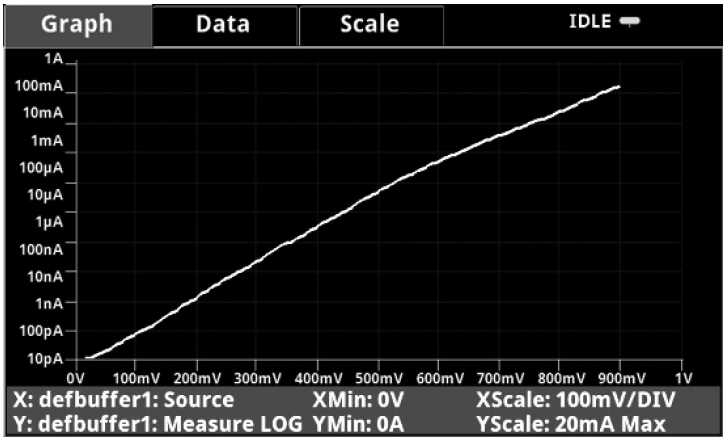


Figure 4-18 shows the results of measuring the forward I-V characteristics of the 1N3595 diode. This diode curve was generated using the 2450, which has a touchscreen display and built-in graph. Note that because the current spans 11 decades, the log of the current is plotted as a function of the applied voltage.

FIGURE 4-18: 2450 Graphical SourceMeter SMU Front Panel Touchscreen Display Showing Forward Bias Diode Curve on Built-in Graph

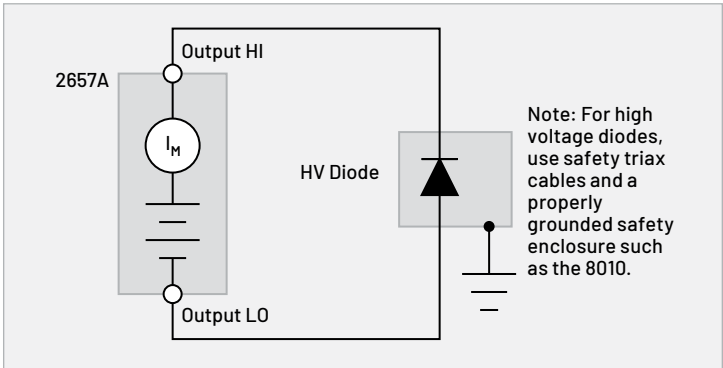


Reverse Leakage Current of a High Voltage Diode

Ideally, the reverse current of a diode should be 0 A; however, a small reverse current does flow when a reverse voltage is applied. The reverse leakage is often measured as the voltage is swept from 0 V to a specified reverse voltage. A large current will flow when the voltage is at the reverse breakdown level. An SMU is an ideal instrument for measuring the reverse leakage characteristics of a diode because it can measure low current and sweep voltage.

Figure 4-19 shows how the 2657A High Power System SourceMeter SMU can be used to test the leakage current of a high voltage diode. The 2657A can measure the current with 1 fA resolution as well as source voltage up to 3 kV. Alternatively, a 2470 High Voltage Graphical SMU can be used

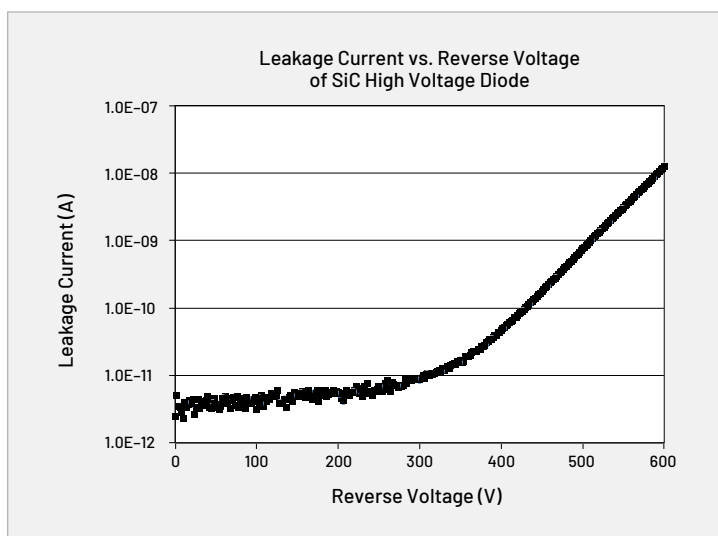
FIGURE 4-19: Connecting the 2657A High Power System SourceMeter SMU to a High Voltage Diode



to source up to 1 kV. When testing high voltage diodes, it is important to ensure the diode is placed in a properly grounded safety enclosure and that high voltage cabling is used.

Figure 4-20 shows the results of performing a reverse voltage sweep from 0 V to 600 V in 2 V steps on a SiC high voltage diode that is rated to 600 V. This curve was generated using the 2657A; the high voltage SiC diode was placed in the 8010 High Power Test Fixture, which enhances operator safety and provides electrostatic shielding.

FIGURE 4-20: Reverse Breakdown Voltage Characteristics of High Voltage Diode Generated by 2657A System SourceMeter SMU



4.3.3 Low Current Measurements of MOSFETs

Overview

Various MOSFET tests require making low current measurements. Some of these tests include gate leakage current, drain-source leakage current, gate induced drain leakage (GIDL) current, and sub-threshold current. These measurements are performed at the wafer level and on packaged devices. The following sections describe how to measure gate leakage current and sub-threshold current.

Gate Leakage Current

Gate leakage current is usually determined by sourcing a DC voltage ramp and measuring the resulting current. The leakage current can be due to the quality of the oxide or it can be due to the physics of the materials. The leakage current could be ohmic or due to tunneling.

The gate leakage current is measured between the gate terminal and the other terminals of the device. **Figure 4-21** shows a typical test configuration for measuring the gate leakage current of a MOSFET. In this setup, SMU1 is connected to the Gate terminal and sweeps the gate voltage and measures the resulting leakage current. SMU2, connected to the Drain terminal, and SMU3, connected to the Source terminal, are both set to bias 0 V.

FIGURE 4-21: Gate Leakage Test Configuration Using Multiple SMUs

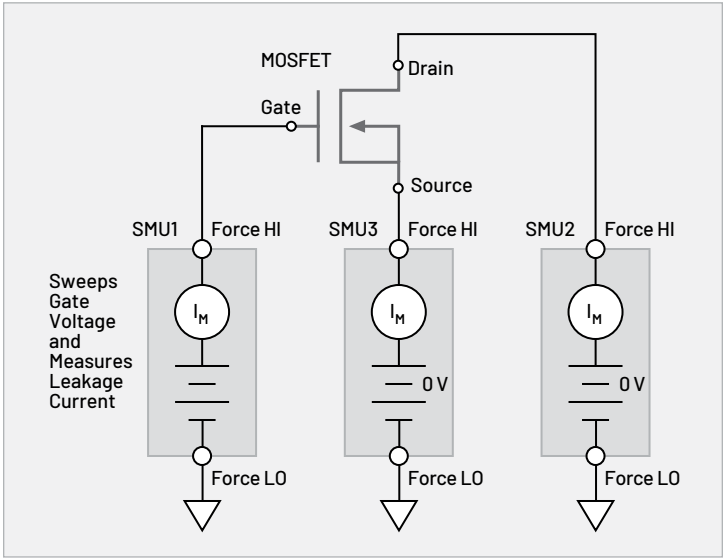


FIGURE 4-22: Gate Leakage Current vs. Gate Voltage of a Packaged n-MOSFET Measured by the 4200A-SCS Parameter Analyzer

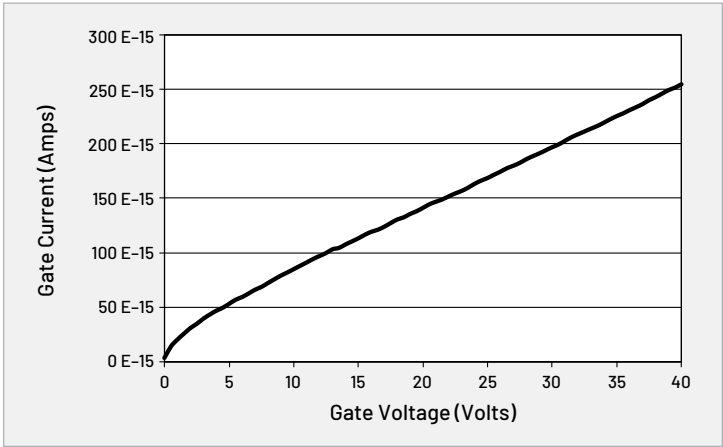


Figure 4-22 shows a gate leakage measurement curve for a packaged n-MOSFET. Note that the magnitude of this curve is less than 1 pA so it requires a very sensitive ammeter. This curve was generated with the 4200A-SCS Parameter Analyzer equipped with three SMUs and preamps, which provide 100 aA resolution.

Sub-Threshold Current of MOSFETs

The sub-threshold current test of a MOSFET, which is often done at the wafer level, is a measure of how quickly the device will turn on and off.

Figure 4-23 shows a typical test set-up for measuring the sub-threshold current. In this setup, a 4200A-SCS Parameter Analyzer equipped with two SMUs and preamps uses one SMU to supply a constant drain-to-source voltage (V_{DS}) and measures the resulting drain current (I_{DS}). Another SMU is used to sweep the gate-to-source voltage (V_{GS}). For this SMU, the current compliance or measure current value should be set to the highest expected gate current on a fixed measurement range.

FIGURE 4-23: Sub-Threshold Current Measurement Using Two SMUs

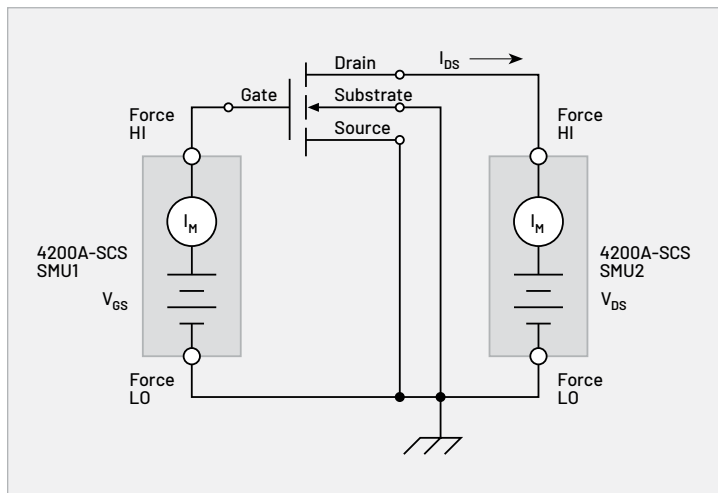
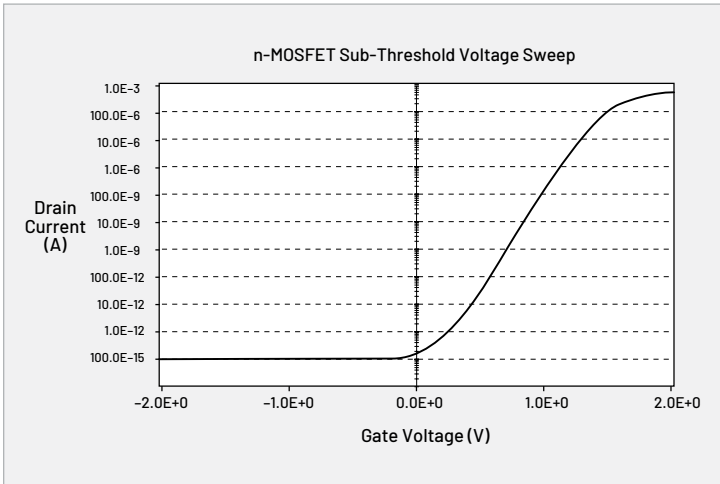


Figure 4-24 is a plot of I_{DS} vs. V_{GS} for an enhancement mode MOSFET, which was generated by the 4200A-SCS Parameter Analyzer.

FIGURE 4-24: I_{DS} vs. V_{GS} for an Enhancement Mode MOSFET



4.3.4 Light Measurements with Photomultiplier Tubes

Overview

Applications such as measuring light with a photomultiplier tube generally require the use of an electrometer or picoammeter due to the low current levels involved.

A photomultiplier tube (PMT) is a device for converting light to electrical current. The tube consists of a light-sensitive cathode that emits electrons in proportion to the photons striking it. These electrons are then accelerated to the next stage, where they impinge and cause the emission of three to six secondary electrons. The process continues through six to fourteen stages (called "dynodes"), depending on tube type. Overall gains of one million or more are commonly attained.

Detailed Operation

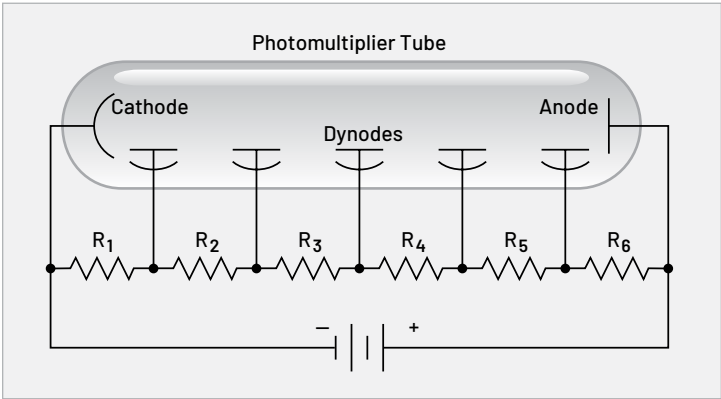
Electrons are accelerated by making the voltage of each successive dynode of the tube more positive than the previous one. The easiest way to accomplish this is to apply a potential across the entire tube and tap the dynode voltages off a voltage divider, as shown in **Figure 4-25**.

The voltages that should be applied to each dynode are a function of PMT design and are specified for each tube type.

The total resistance of the dynode resistors should be such that the current flowing through the series resistance is at least 100 times the expected anode current of the tube:

$$R_T = \frac{\text{Voltage, Anode-to-Cathode}}{100 \times \text{Anode Current}}$$

FIGURE 4-25: Voltage Supply for Photomultiplier Tube

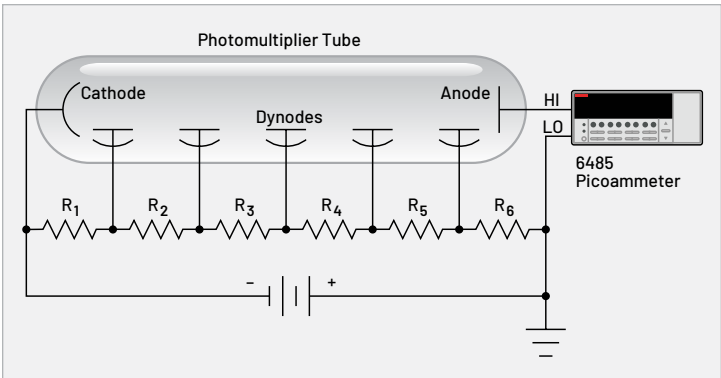


Most photomultiplier tubes require anode-to-cathode potentials from 1000 to 3000 V. The anode is the readout point, so it is usually operated at near-ground potential and the cathode at a high negative potential. The 2290-5 High Voltage Supply provides up to 5000 V for such applications.

The anode current of most photomultiplier tubes ranges from just picoamps to 100 μ A. The picoammeter is commonly used as a readout because of its high sensitivity. The low input voltage drop (voltage burden) of such a picoammeter keeps the anode at virtually ground potential. **Figure 4-26** illustrates a typical test configuration using a 6485 Picoammeter. If the PMT requires no more than 1000 V, the 6517B Electrometer/Source would provide a convenient solution because it can measure the current as well as supply the voltage.

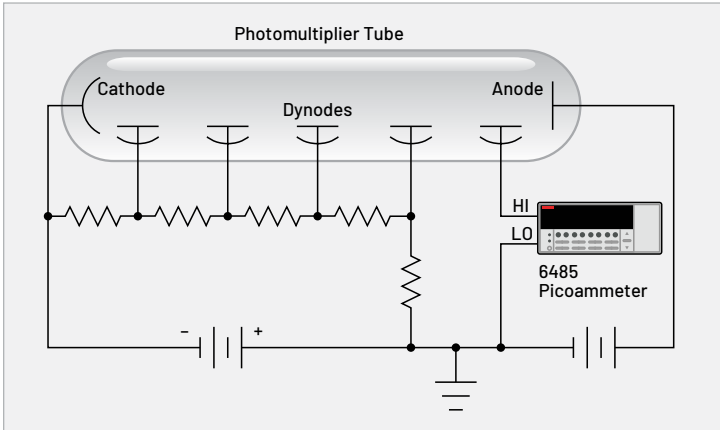
With this connection method, the picoammeter reads a negative current. Occasionally, the current must be measured as a positive value. In

FIGURE 4-26: Basic Photomultiplier Tube Connections



such cases, a simple re-arrangement and an additional power supply permit reading positive current. The configuration for measuring positive PMT current is shown in **Figure 4-27**. The picoammeter reads the current at the last dynode, which is equal to the anode current minus the current flowing to the previous dynode. In effect, a slight amount of PMT gain is sacrificed to make the measurement.

FIGURE 4-27: Reading Positive PMT Current



A PMT usually has a small amount of current flowing even when the cathode is not illuminated. This phenomenon is known as “dark current,” and is insignificant in most measurements. In other cases, it can either be subtracted from the reading by using the REL (zero) setting of the picoammeter or simply canceled out by using built-in zero suppression if the instrument has this feature.

4.3.5 Ion Beam Measurements

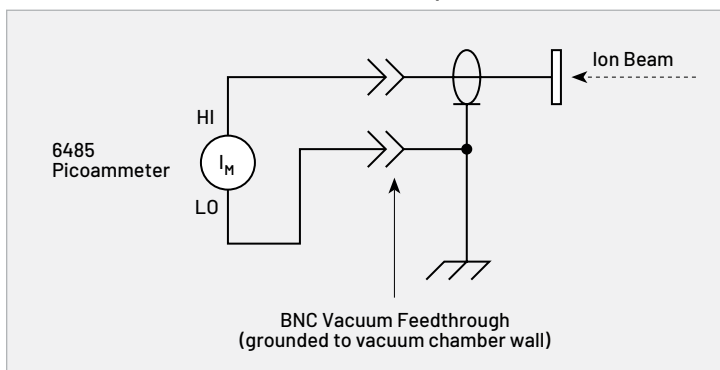
Overview

Ion beams are used in a variety of applications, such as with mass spectrometers and ion implanters. Ion beam current is usually very small ($<\mu\text{A}$), so an electrometer or picoammeter is needed to make this measurement. This section describes how to make these measurements with a 6485 Picoammeter and a 6487 Picoammeter/Voltage Source. An electrometer can be substituted for greater current sensitivity.

Test Method

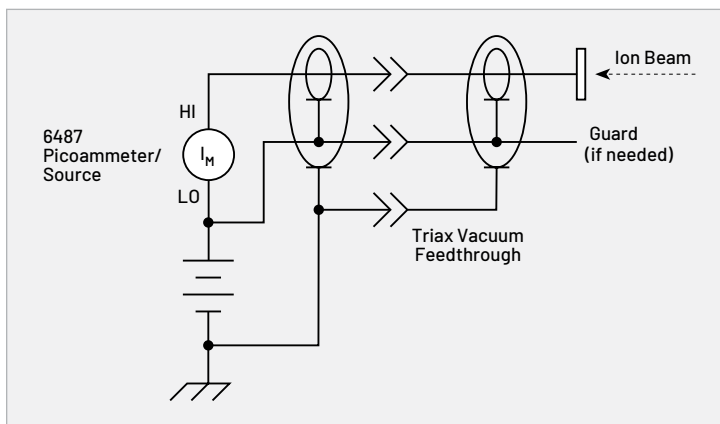
If the source of ions is biased off ground, then the ion collector will most likely be at ground potential. If this is the case, a simple coaxial vacuum feedthrough can be used to make connections from the collector to the picoammeter. **Figure 4-28** shows a 6485 Picoammeter measuring the current from the ion collector, which is operating at ground potential.

FIGURE 4-28: Ion Collector with Grounded BNC Receptacle



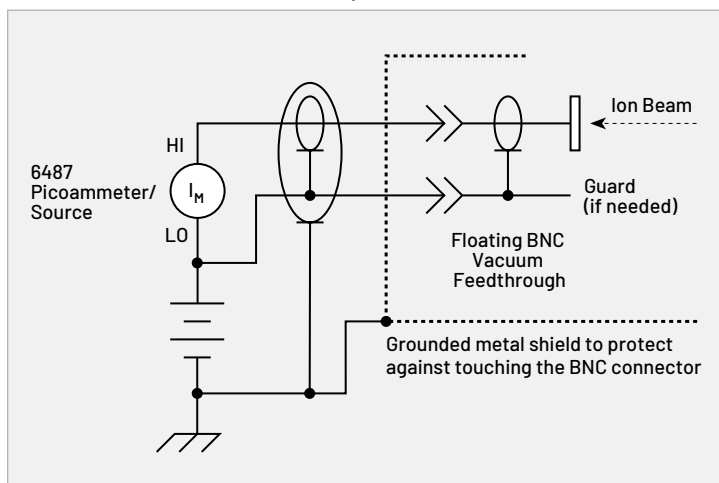
However, if the source of ions is at ground potential, then the ion collector must be biased off ground. The 6485 Picoammeter can float off ground by only 42 V. Therefore, a picoammeter such as the 6487, which can float off ground up to 500 V, must be used. **Figure 4-29** is an example of the 6487 biased off ground and measuring an ion beam. The HI terminal of the picoammeter is connected to the ion collector via a triax vacuum feedthrough. The LO terminal of the picoammeter is biased off ground by the voltage source. For safety reasons, a triaxial vacuum feedthrough should be used if the bias voltage is more than 42 V. The 6487 can supply up to 500 V of bias.

FIGURE 4-29: Ion Collector with Triax Receptacle



If a triaxial vacuum feedthrough is unavailable, a metal safety shield should be built around the isolated BNC connection (**Figure 4-30**). The metal safety shield is connected to ground. Floating input signals are discussed in detail in Section 2.6.8.

FIGURE 4-30: Ion Collector with BNC Receptacle



If the bias voltage is less than 42 V off ground, the isolated BNC vacuum feedthrough will not need a safety shield.

After the connections are made, verify the system is working properly by turning the bias voltage on and taking a current measurement with no ion beam current. If there is significant current compared to the current to be measured, there must be a stray leakage path, which should be corrected.

Often, the beam current is plotted as a function of time. This can be done by using either the analog output of the picoammeter or the communications interface to collect readings, then plotting them. The DMM App for KickStart Software can be used to control the picoammeter and graph readings.

4.3.6 Photoconductive Current of PIN Photodiode

Overview

A photodiode is a type of diode or photo detector that converts light into a current or voltage, depending on whether it is in the photovoltaic (zero-bias) or photoconductive (reverse-bias) mode. When the photodiode is in the reverse-bias mode, the output current is directly proportional to the light intensity applied to the photodiode junction. Consequently, photodiodes can be used to detect even very small quantities of light in a wide range of applications, including photography, light detectors, medical imaging instrumentation, and optical communications.

When the photodiode is reverse biased, the resulting current is a combination of the photocurrent (obtained with light) and the dark current (no light). The photocurrent is typically constant as a function of the applied

voltage. However, the dark current is a small leakage current induced by an applied voltage. The dark current is a thermally activated leakage of the PN junction and is often measured as a function of temperature. The photocurrent and dark current measurements can be made with an SMU that can sweep voltage and measure current over a wide range.

Test Configuration

Figure 4-31 shows a 2635B System SourceMeter SMU connected to measure the photocurrent of a reverse-biased photodiode. This instrument can sweep the voltage and measure the current with <1 fA resolution. It can also measure the forward bias I-V characteristics of the photodiode. In that case, make Kelvin connections to the DUT to prevent unwanted voltage drops from affecting measurement accuracy.

FIGURE 4-31: Low Current SourceMeter SMU Connected to Perform I-V Characterization of a Photodiode

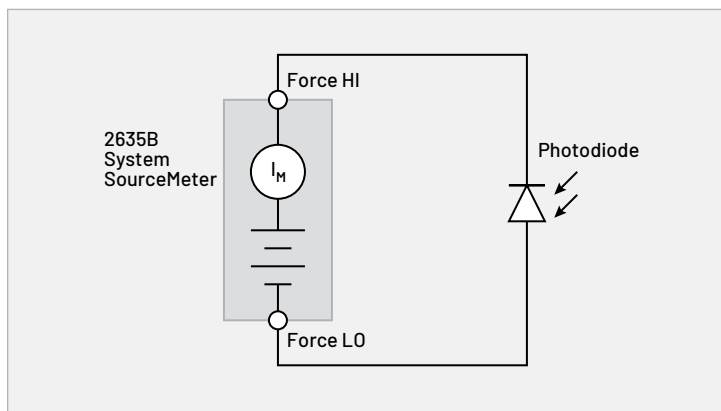
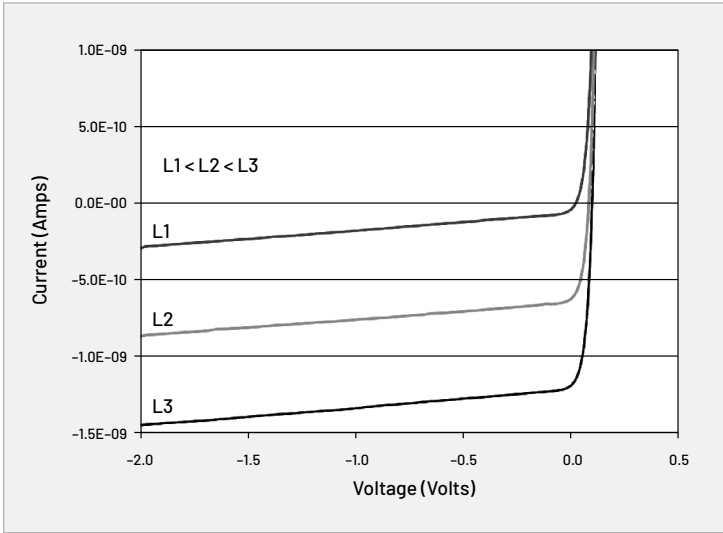


Figure 4-32 illustrates the results of measuring the reverse-bias current of a PIN silicon photodiode using the 2635B. In this example, the I-V curves were generated at different low levels of light intensity (L1-L3).

FIGURE 4-32: Photoconductive Current vs. Reverse Bias Voltage of PIN Photodiode at Different Light Intensities (L) Measured by the 2635B



4.3.7 Avalanche Photodiode Reverse Bias Current Measurements

Overview

An avalanche photodiode (APD) is a high sensitivity, high speed photodiode that has an internal gain mechanism activated by applying a reverse voltage. The gain of the APD can be controlled by the magnitude of the reverse bias voltage. A larger reverse bias voltage results in a larger gain. APDs are operated with an electric field strength such that an avalanche multiplication of photocurrent occurs similar to a chain reaction. APDs are used in a variety of applications requiring high sensitivity to light such as fiberoptic communications and scintillation detectors.

Common electrical measurements of APDs typically include the breakdown voltage, responsivity, and reversed bias current measurements. The maximum current rating for a typical APD is 10^{-4} to 10^{-2} A, while the dark current can be as low as the 10^{-12} to 10^{-13} A range. The maximum reverse bias voltage will vary, depending on the material of the APD, but can be up to 100 V for InGaAs APDs or up to 500 V for Si devices.

Test Description

Measuring the reverse bias current of an APD requires an instrument that can measure current over a wide range as well as output a voltage sweep. Because of these requirements, instruments such as the 6487 Picoammeter/Voltage Source or the 6430 Sub-Femtoamp SourceMeter SMU are ideal for these measurements.

Figure 4-33 shows a 6430 connected to a photodiode. The photodiode is placed in an electrically shielded dark box. To shield the sensitive current measurements from electrostatic interference, connect the box to the LO terminal of the 6430.

FIGURE 4-33: APD Connected to a 6430 Sub-Femtoamp Remote SourceMeter SMU

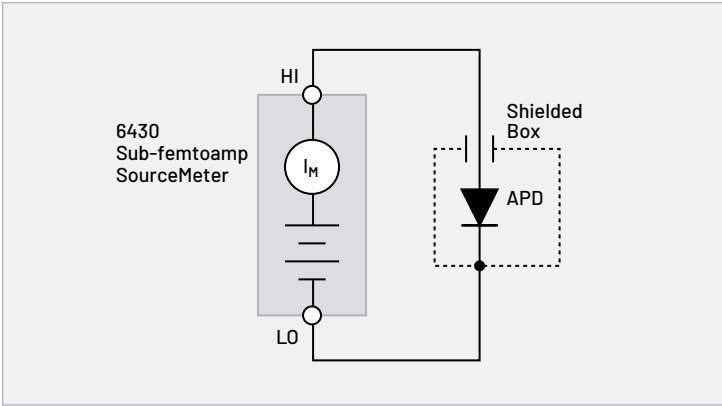
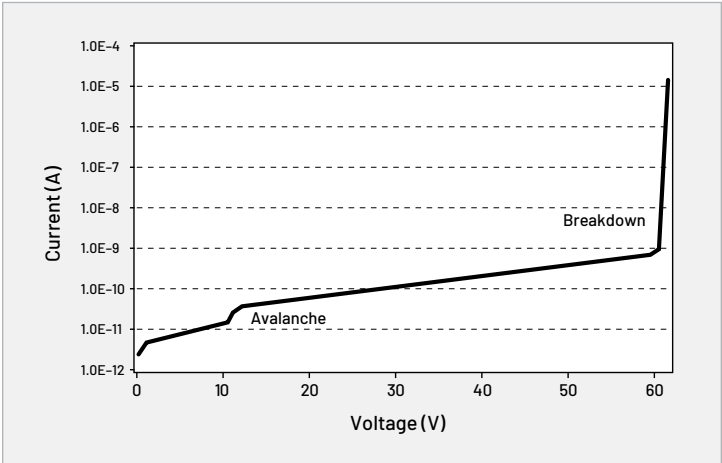


Figure 4-34 shows a current vs. reverse voltage sweep of an InGaAs APD, generated by the 6430 SourceMeter SMU. Note the wide range of current measurements. The avalanche region becomes more pronounced with increasing light. The breakdown voltage will cause the current to flow freely since electron-hole pairs will form without the need for light striking the diode to generate current.

FIGURE 4-34: Current vs. Reverse Voltage Sweep of an InGaAs APD Measured by the 6430



4.3.8 Current-Voltage Characterization of Carbon Nanotube (CNT) FETs

Overview

Carbon nanotube transistors (CNTs) have been the subject of much scientific research in recent years, due not only to their small size but their remarkable electronic and mechanical properties and wide range of potential applications. In particular, researchers are studying CNT-based devices, such as CNT FETs, for use in applications such as sensors, transistors, memory devices, etc. Characterizing the electrical properties of delicate nanoelectronic devices requires instruments and measurement techniques optimized for low power levels and high measurement sensitivity.

Test Configuration

CNT FETs are three-terminal devices and require two or three SMUs for I-V characterization. **Figure 4-35** shows a typical test configuration for generating an I_D - V_G curve. In this setup, two SMUs in a 4200A-SCS Parameter Analyzer are connected to the Gate and Drain terminals of a CNT FET. The Source terminal is either connected to a third SMU or to the Ground Unit. For the drain current vs. gate voltage test, SMU1 sweeps the gate voltage (V_G) and SMU2 outputs a constant DC drain voltage (V_D) and measures the resulting drain current (I_D). This configuration allows obtaining a DC I-V drain family of curves easily by changing the function of SMU1 to step the gate voltage and SMU2 to sweep the drain voltage and measure the drain current.

Figure 4-36 is an I_D - V_G plot of a CNT FET. In this curve, the drain voltage was set to a constant 2 V. The SMUs with preamps in this system allow measuring current over a wide range from 100 mA down to 100 aA resolution.

To prevent damage to the device while performing I-V characterization, it's important to limit the amount of current that can flow through the device, which can be done by adjusting the source limit (compliance) on the SMU.

When measuring low current on CNT FETs, a variety of sources can generate noise, including particle collisions, defects, AC pickup, and electrostatic interference. Always use shielding techniques and the appropriate line cycle integration time to minimize noise.

Make sure the probes are in the up position (not in contact with the device) when connecting and disconnecting instruments from the terminals of the device. The process of moving cables has the potential to inject charge into the device and cause damage.

FIGURE 4-35: Circuit for Measuring the DC I-V Characteristics of a CNT FET

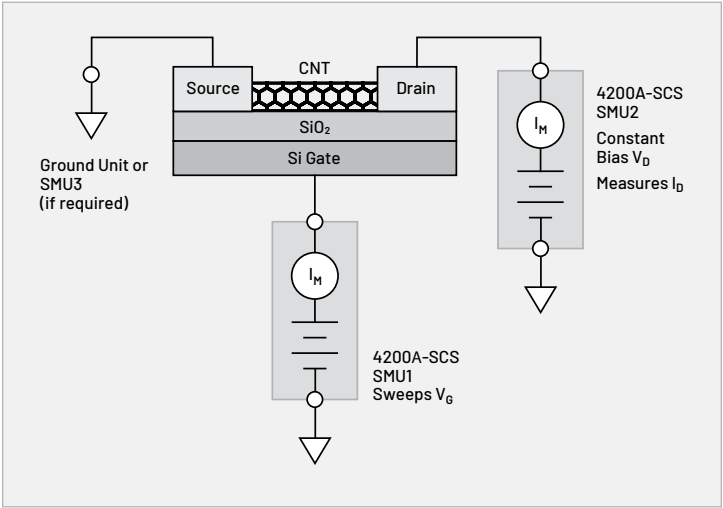
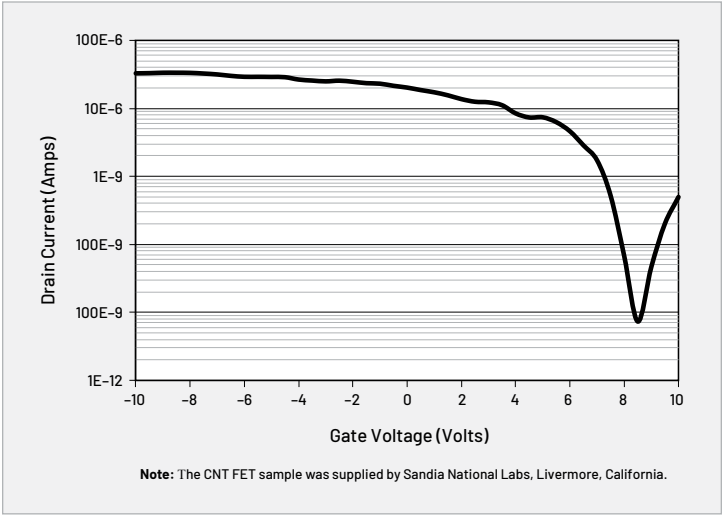


FIGURE 4-36: Drain Current vs. Gate Voltage of CNT FET Measured with a 4200A-SCS Parameter Analyzer



4.4 High Resistance Measurement Applications

Electrometers can measure high resistance either by sourcing current and measuring voltage or by sourcing voltage and measuring current. These methods were discussed in Section 2.4. Picoammeters with voltage sources and some SMUs can also measure high resistances. High resistance measurement applications include surface insulation resistance testing, resistivity measurements of insulators and semiconductors, and voltage coefficient testing of high ohmic value resistors.

4.4.1 Surface Insulation Resistance Testing of Printed Circuit Boards

Overview

Low surface insulation resistance (SIR) of a printed circuit board (PCB) can degrade the performance of the circuits on the board considerably. Factors that affect a board's surface insulation resistance include the board material used, the presence of coatings such as solder masking or conformal coatings, board cleanliness, and relative humidity.

The measured insulation resistance typically ranges from $10^7 \Omega$ to $10^{16} \Omega$, so an electrometer or picoammeter must be used to make this measurement. This section describes surface insulation resistance measurements using the 6517B Electrometer/Voltage Source. For some applications, the 6487 Picoammeter/Voltage Source may be substituted for the 6517B.

Basic Test Procedure

The procedure for insulation resistance testing consists of preparing, conditioning, and measuring the sample. Details may vary, based on specific test methods.

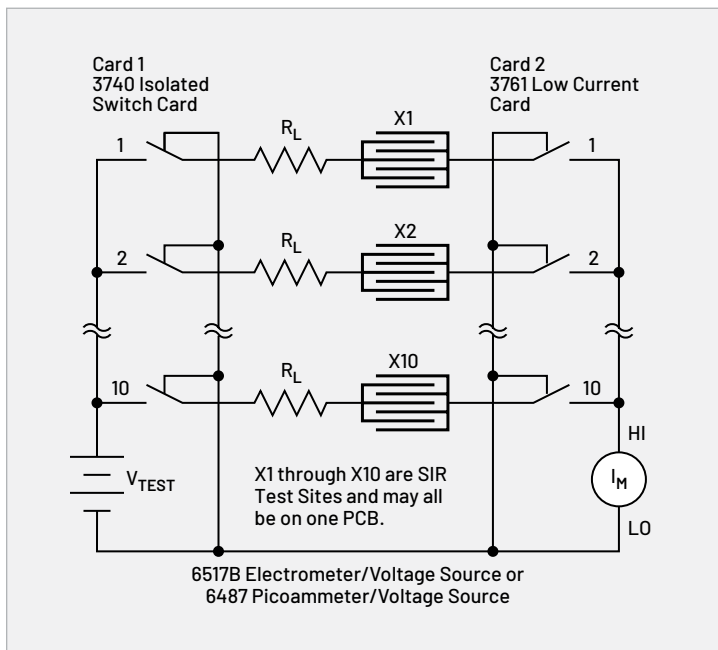
In preparation, the sample is inspected visually for defects. Then Teflon®-insulated leads are attached to the sample. One alternative method is to use test boards with card edge connectors for easy connection to the test system. Finally, the samples are cleaned and dried according to the requirements of the test method used. After preparation, insulation resistance measurements are typically made before, during, and after the samples are placed in an environment with controlled temperature and humidity.

To make the measurement, a constant voltage is applied for a pre-defined period, usually 60 seconds, and the resulting current is measured with a picoammeter or electrometer.

Test Configuration

Figure 4-37 depicts a system to test the insulation resistance of ten test sites. Each test site can be thought of as an isolated resistor. The 6517B Electrometer applies the bias voltage (V_{TEST}), measures the leakage current, then calculates the resistance of each resistor. The 3706A System Switch/Multimeter switches the electrometer and voltage source to each

FIGURE 4-37: SIR Test System to Measure Ten Test Sites



test pattern, X1 through X10. The voltage channels are switched with the 3740 Isolated Switch Card, while the current channels are switched with the 3761 Low Current Multiplexer Card. Note that the maximum source voltage is limited to 300 V when using the 3740 Card.

To measure X1, Channel 1 on the 3740 Card and Channel 1 on the 3761 Card are closed. This will bias the X1 resistor and, after a specified "soak" time, the resulting current is measured. To measure the X2 resistor, Channel 1 on both the 3740 and 3761 cards is opened, and Channel 2 on each card is closed. Again, the current is measured after the desired soak time.

The resistors (R_L) are current limiting resistors used to protect the switches and electrometer from high current. These resistor values should be such that the voltage drop at the maximum measured current will not affect measurement accuracy.

Note that when a channel is opened, the corresponding resistor terminal is connected to circuit LO. This allows any charge across the resistance to be discharged to circuit LO when the resistance is not being measured.

The 3706A Mainframe has six slots, so for this application, two 3740 cards and four 3761 cards will enable switching up to 40 devices.

4.4.2 Resistivity Measurements of Insulating Materials

Overview

The ideal way to measure the resistance of an insulating material is to apply a known potential to the sample and measure the resulting low current with an electrometer or picoammeter. The resistivity is derived by measuring the resistance and then converting the resistance to surface or volume resistivity by taking geometric considerations into account. Insulators are poor conductors of electrical current and typically have a volume resistivity in the range of 10^9 to 10^{20} ohm-cm. To account for the sample's geometry, electrodes with convenient dimensions should be used, such as Keithley's 8009 Resistivity Test Fixture.

Two common standard resistivity test methods for insulating materials include ASTM D257 "DC Resistance or Conductance of Insulating Materials" and the IEC 62631-1 "Dielectric and Resistive Properties of Solid Insulating Materials". These standards provide detailed information on calculations, sample preparation, correction factors and other measurement considerations. The test configurations will vary depending on whether measuring the surface or volume resistivity of the sample.

This section describes how to make volume and surface resistivity measurements using an electrometer or picoammeter and the 8009 Resistivity Test Fixture, describes measurement dependencies, and discusses offset compensation techniques including the Alternating Polarity Method and the Alternating Voltage Method.

Volume Resistivity Measurements

Volume resistivity is a measure of the leakage current directly through a material. It is defined as the electrical resistance through a one-centimeter (1 cm × 1 cm × 1 cm) cube of insulating material and is expressed in ohm-centimeters. When measuring the volume resistivity, the test sample is placed between two electrodes and a potential difference is applied between them. The resulting current is distributed through the volume of the test sample and is measured using a picoammeter or electrometer. The resistivity is calculated from the geometry of the electrodes and the thickness of the sample:

$$\rho = \frac{K_V}{t} \times \frac{V}{I}$$

where:

ρ = volume resistivity (Ω -cm)

K_V = effective area of measuring electrode based on cell geometry (cm^2)

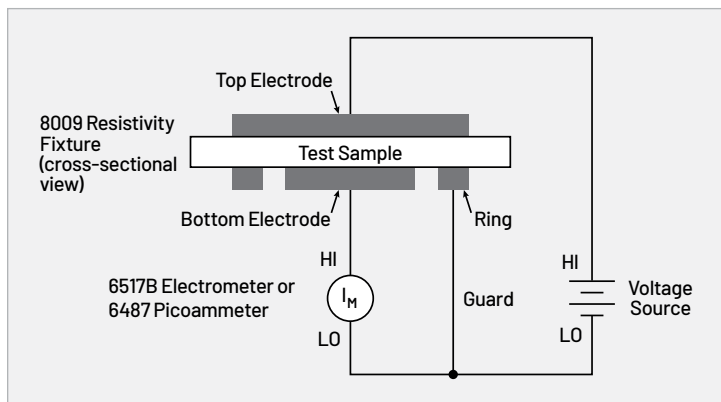
V = applied voltage (volts)

I = measured current (amperes)

t = sample thickness (cm)

Figure 4-38 depicts a measurement configuration that complies with ASTM D257 for volume resistivity measurements. In this circuit, the HI of the ammeter is placed on the bottom electrode and the HI of the voltage source to the top electrode. The LO of the ammeter and the LO of the source are connected. The bottom ring electrode is connected to guard (LO of the ammeter) to prevent surface leakage currents from being added into the measurement.

FIGURE 4-38: Volume Resistivity



Surface Resistivity Measurements

Surface resistivity is defined as the electrical resistance of the surface of a material and is expressed in ohms or ohms per square. The units for sheet resistivity are expressed as ohms per square (Ω/sq) in order to distinguish this number from the measured resistance (V/I) which is units of just ohms. It is measured by placing two electrodes on the surface of the test sample, applying a potential difference between them, and measuring the resulting current. The surface resistivity is calculated as follows:

$$\sigma = K_S \times \frac{V}{I}$$

where:

σ = surface resistivity (Ω or Ω/sq)

K_S = effective perimeter (P) of the measured space divided by the gap (g) between the two electrodes based on cell geometry

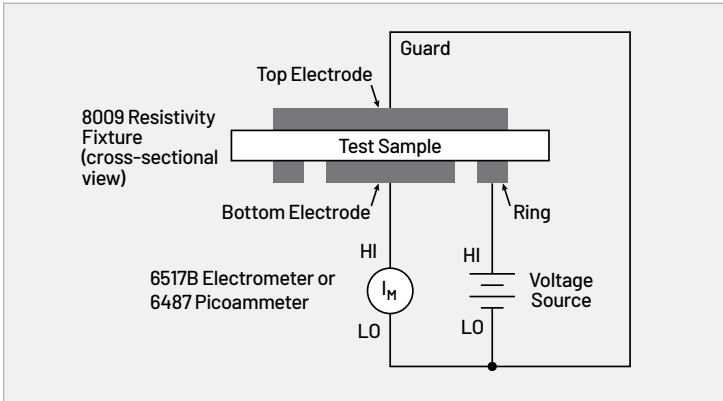
V = applied voltage (volts)

I = measured current (amperes)

Figure 4-39 is a configuration for measuring surface resistivity. This configuration is similar to the circuit for performing volume resistivity measurements, except that the resistance is measured between the bot-

tom two electrodes. Note the top electrode is guarded, so that only current flowing across the insulator is measured by the picoammeter.

FIGURE 4-39: Surface Resistivity



Using the 8009 Resistivity Test Fixture

No sample preparation is necessary when using the 8009 Resistivity Test Fixture. This fixture ensures a standardized electrode configuration, eliminating the need to paint electrodes on the sample. The recommended sample size for using this test fixture is 2.5 – 4 inches in diameter and up to 0.125 inch thick.

Some extremely rigid samples, such as glass epoxy and ceramics, require an interface between stainless steel electrodes and the sample surface. The 8009 includes conductive rubber for the top and bottom electrodes to enhance surface contact between the sample and the fixture. Care must be taken because the electrode area becomes the area of the contact medium. If it is not the same configuration and size as the electrodes, the conversion constants furnished with the system may be invalid.

The 8009 employs a safety interlock to prevent the high voltage from being applied to the electrode until the test fixture lid is closed. This fixture also shields the sample from electrostatic interference.

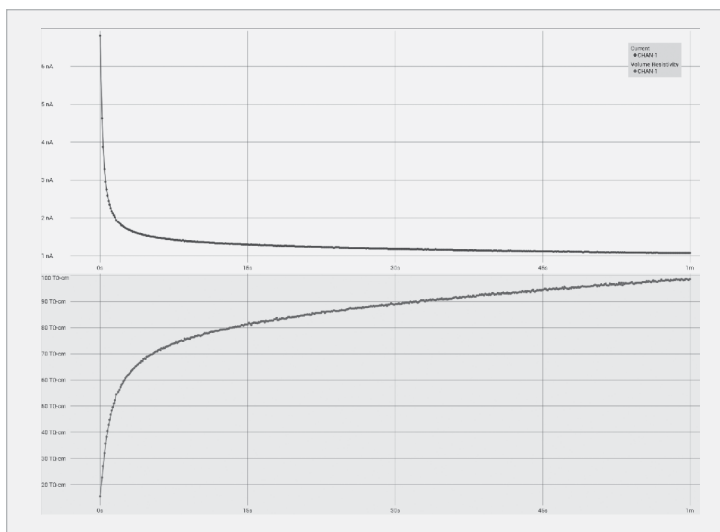
Resistivity Measurements

Keithley electrometers and picoammeters can be controlled by a computer to automate the measurements and return the current, resistance, and time. The voltage is applied for a known period (usually 60 seconds) while the resulting current is measured. At the end of the electrification time, a current measurement is taken, and the resistivity is calculated based on the applied voltage and electrode geometry.

The graphs of the current and resistance as a function of time to an applied voltage on a PVC compound material are shown in **Figure 4-40**. The

exponential current response is shown in the top graph, and the calculated resistance is shown in the bottom graph. These graphs were generated using the High Resistivity App for Kickstart Software which programs the 6517B to automatically measure the current and resistance as a function of time to an applied voltage.

Figure 4-40: Current and Resistance vs Time Response to an Applied Voltage



Factors Affecting High Resistivity Measurements

Several factors can affect volume and surface resistivity measurements of insulators including the electrification time, the magnitude of the applied voltage, and humidity.

First, these measurements are functions of the applied voltage. Sometimes, the voltage is varied intentionally to determine an insulator's voltage dependency. To illustrate the voltage dependency of resistivity measurements, the 6517B Electrometer/Voltage Source was used to measure the resistivity of a Bakelite® plastic sample at four test voltages: 25 V, 50 V, 100 V, and 200 V. **Figure 4-41** shows the results of these resistivity measurements as a function of time. Notice how, in this example, the resistivity decreases as the voltage increases.

The resistivity of insulators also varies as a function of the electrification time. The longer the voltage is applied, the lower the measured current (and the higher the resistivity) becomes because the material continues to charge exponentially. This time dependency is illustrated in **Figure 4-42**. In this example, a constant DC bias was applied to a Bakelite plastic sample and the resistivity as a function of time was measured with the 6517B Electrometer/Voltage Source. At time $t=10$ s, the volume resistivity (ρ) was

FIGURE 4-41: Voltage Dependency of Volume Resistivity Measurements of Bakelite Sample Measured with 6517B

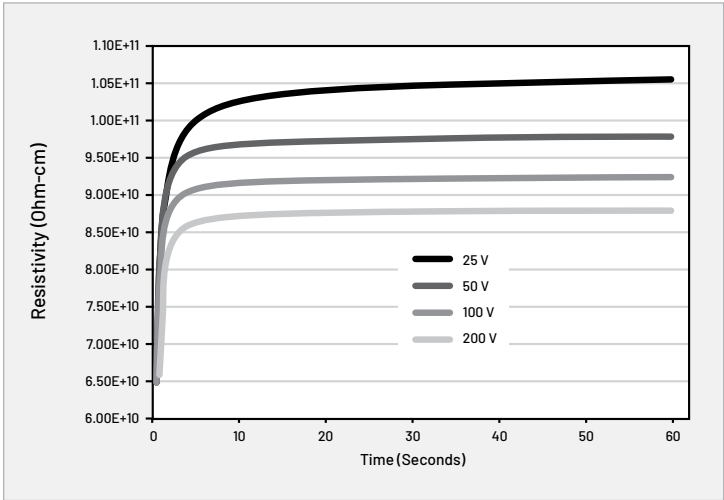
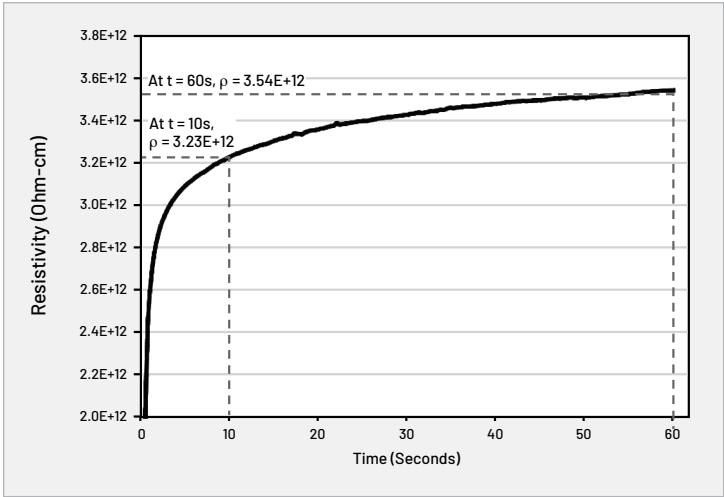


FIGURE 4-42: Resistivity vs. Time of Bakelite Plastic Sample at Constant DC Bias Measured with the 6517B



3.23E12 Ω -cm. However, at $t=60$ s, the resistivity was 3.54E12 Ω -cm. This is a difference of about 10%. When comparing samples, it is always important to use the same time of electrification.

Humidity greatly affects the results of surface resistivity measurements and, to a lesser degree, volume resistivity measurements, as well.

Moisture will cause the surface resistivity measurements to be lower than normal.

To make accurate comparisons between specific tests, the applied voltage, electrification time, and environmental conditions should be kept constant from one test to the next.

Correcting for Offset Current

When measuring materials with very high resistivity, background currents may cause measurement errors. Background currents may be due to charge stored in the material (dielectric absorption), static or triboelectric charge, or piezoelectric effects. Background currents can be equal to or greater than the current stimulated by the applied voltage. If the background current is the same polarity as the measured current, the resultant measured current value will be much higher than the true value. If the background current is the opposite polarity, these unwanted currents could cause a reverse polarity current reading. That is, the current polarity is opposite the polarity of the applied voltage, so the calculated resistance will be negative. To counter these problems, the Alternating Polarity and Alternating Voltage Methods can virtually eliminate the effects of background currents in the sample.

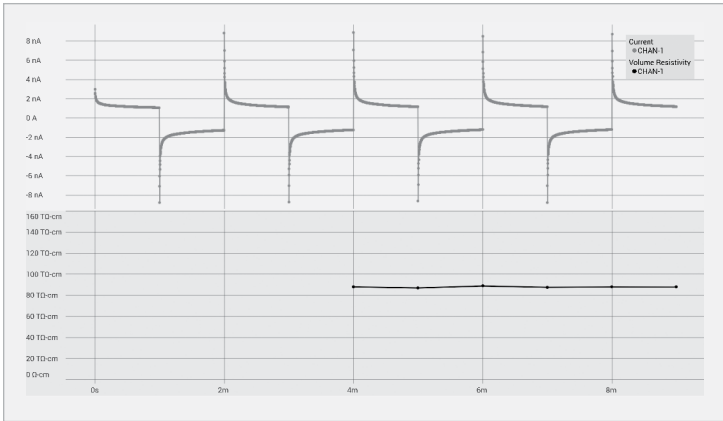
Alternating Polarity Method

The Alternating Polarity Method applies a bias voltage of positive polarity, then the current is measured after a specified delay. Next, the polarity is reversed and the current is measured again, using the same delay. By alternating the polarity of the applied voltage using this iterative process, it is possible to cancel out the effects of these background currents. The polarity reversal process can be repeated any number of times. The resistance is calculated based on a weighted average of the most recent current measurements.

The 6517B Electrometer has the Alternating Polarity Method built into a test sequence. With this method, the user enters the test voltage, measurement time, and the number of iterations. The final resistance value is calculated and stored in memory.

The Alternating Polarity Method can be automated by controlling 6517B using the High Resistivity App for Kickstart Software. Kickstart Software enables the user to view the actual current vs time waveform that results from the alternating polarity voltage. A typical screen capture showing the current vs time results taken by the software is shown in **Figure 4-43**. Note the exponential decay in the current using both positive and negative test voltage. The top graph shows the current vs time and the bottom graph shows the calculated surface resistivity. Notice the surface resistivity doesn't appear on the graph until after the second iteration. The software provides a final calculated resistivity value when the test is finished.

Figure 4-43. Current Waveform Resulting from Applied Alternating Polarity Voltage



Alternating Voltage Method

In addition to measuring resistance by forcing a voltage and measuring current, the 6487 Picoammeter/Voltage Source offers a built-in Alternating Voltage Ohms mode, which consists of taking two current measurements—one at a user-specified test voltage and one at 0 V. By determining the current difference that results from the step voltage, this mode cancels out the effects of background current.

4.4.3 Resistivity of High Resistance Semiconductor Materials

Overview

Semiconductor materials may have high resistivities depending on the level of doping. Several factors can complicate measuring the resistivity of these materials, including problems in making good contact with the material. Special probes have been designed for making resistivity measurements on semiconductor wafers and bars. These probes typically use a hard metal such as tungsten, which is ground to a sharp point. Contact resistance is very high in these cases, so either a four-point collinear probe or four isolated probes should be used. While two contacts supply a constant current, the other two contacts measure the voltage drop across a portion of the sample. The resistivity can be calculated by applying geometrical factors to the measured resistance.

These measurements may seem straightforward, but certain precautions should be observed. Good shielding of the contacts and electrical leads is important for three reasons:

1. The circuit involves high impedance, so it's susceptible to electrostatic interference.
2. The contact points on the semiconductor material can cause a diode action and, thus, rectify any pickup and display it as a DC offset.

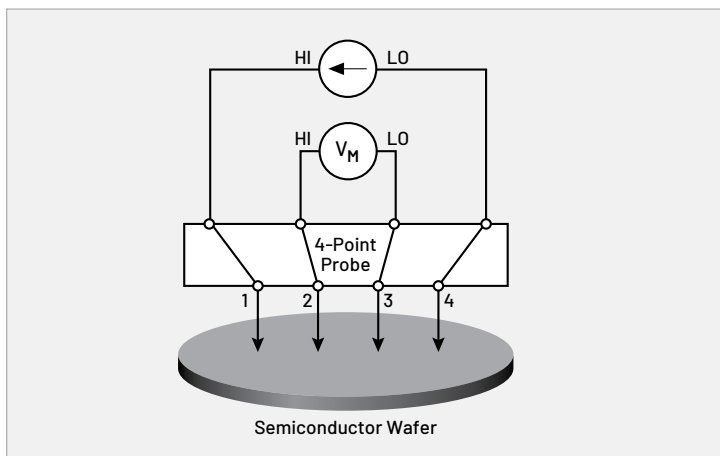
3. The material is usually sensitive to light.

The following paragraphs discuss measuring semiconductor resistivity using both the four-point collinear probe and van der Pauw techniques.

Four-Point Probe Technique

The four-point collinear probe resistivity measurement technique involves bringing four equally spaced probes in contact with the material of unknown resistance. The probe array is placed in the center of the material. **Figure 4-44** is a diagram of this technique.

FIGURE 4-44: Four-Point Collinear Probe Method of Measuring Resistivity



A known current is passed through the two outside probes and the voltage is sensed at the two inside probes. The resistivity is calculated as follows:

$$\rho = \frac{\pi}{\ln 2} \times \frac{V}{I} \times t \times k$$

where: V = the measured voltage (volts)

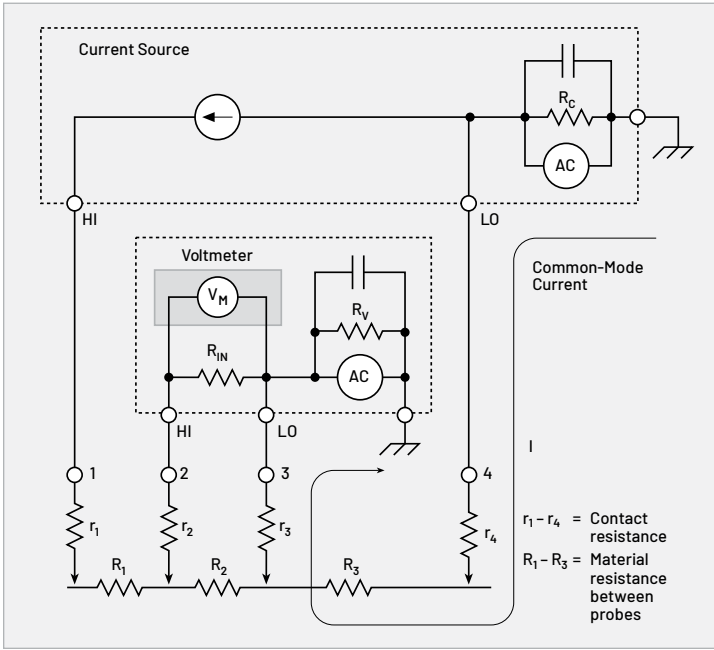
I = the source current (amps)

t = the wafer thickness (cm)

k = a correction factor based on the ratio of the probe to wafer diameter and on the ratio of wafer thickness to probe separation

As shown in **Figure 4-45**, a more realistic circuit would include a contact or spreading resistance at each probe (r_1 through r_4), the finite resistance from LO to earth ground of both the current source (R_C) and the voltmeter (R_V), and the input resistance of the voltmeter (R_{IN}). Depending upon the material, the contact resistance (r) may be as much as 300 times or

FIGURE 4-45: Realistic Circuit of the Four-Point Collinear Probe Method



more than the measured resistance (R_2). This requires the current source to have considerably higher compliance voltage than might be expected and the voltmeter must have a much higher input resistance.

The current source is not completely isolated from earth ground, so as the sample resistance increases, it becomes increasingly necessary to use a differential electrometer. The problem exists because the sample may have a very high resistance ($10^8 \Omega$ or higher), which is of the same order of magnitude as the isolation (Input **LO** to chassis, R_V) of the electrometer voltmeter. As shown in **Figure 4-45**, an AC current will flow from the **LO** terminal of the current source through the sample and to the voltmeter's **LO** terminal, then back to ground. The resulting voltage drop across r_3 will cause erroneous results when the voltmeter measures the voltage drop between probes 2 and 3.

Using two electrometers eliminates this problem, as shown in **Figure 4-46**. The voltmeter will read the difference between the two electrometers' buffer outputs, which is equal to the voltage across R_2 . The values (r_1, r_2, r_3 , and r_4) represent the resistance due to the probe in contact with the material. The unity-gain buffers have very high input impedance, so little common-mode current will flow through r_3 and the value of R_2 can be calculated easily. The buffers can be a pair of JFET op amps or two electrometers with unity-gain outputs.

FIGURE 4-46: Making Differential Four-Point Probe Measurements

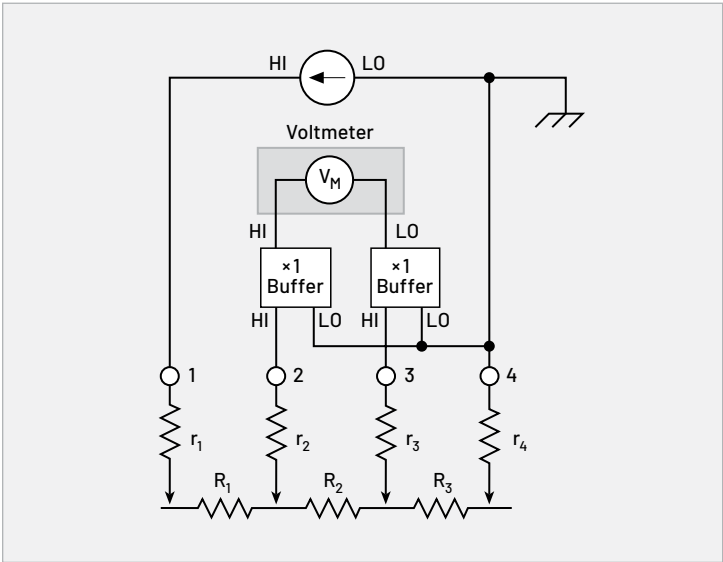


FIGURE 4-47: High Resistance Four-Point Probe Measurement System

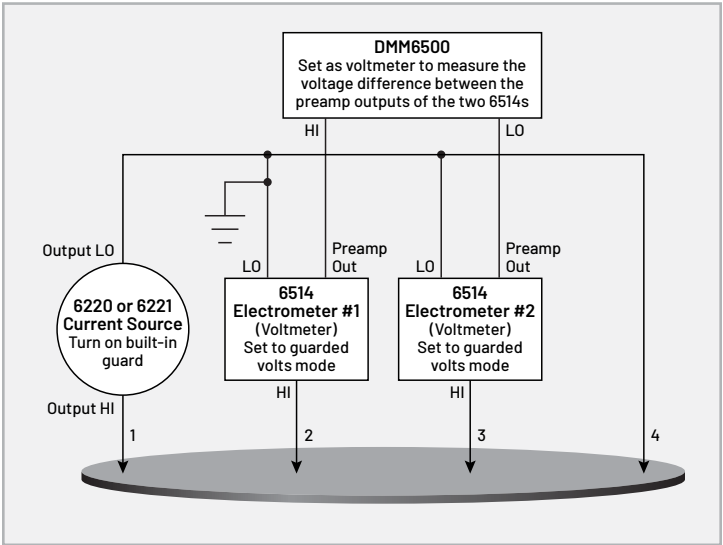


Figure 4-47 is a schematic of a high resistance measurement system, which includes two electrometers (6514), a DMM (DMM6500), and a current source (6220 or 6221). In this circuit, the electrometers are used as

unity-gain buffers. The DMM measures the difference between the outputs of the two electrometers. The settling time of the measurement will be much faster if the electrometers are used in the guarded mode. Guarding reduces the effects of the shunt capacitance in the circuit so the charging time can be much faster. If feasible, triax cables should be used to ensure that guard is connected as close to the material as possible.

Both the 6220 and 6221 have very high output impedance and a built-in guard. Just as with electrometers, using guarding will reduce the measurement time because the cable capacitance will no longer affect the time constant of the measurement.

To avoid leakage currents, use either isolated or guarded probes to make contact with the sample. The current source should be in the guarded mode. See Section 2.2.1 for a more detailed discussion of guarding.

van der Pauw Technique

The van der Pauw technique for measuring resistivity also employs a constant-current method. This method is particularly useful for measuring very small samples because the dimensions of the sample and the spacing of the contacts are unimportant. This technique uses four isolated contacts on the boundary of a flat, arbitrarily shaped sample. Eight measurements are made around the sample, as illustrated in **Figure 4-48**.

Two values of resistivity, ρ_A and ρ_B , are then computed as follows:

$$\rho_A = \frac{\pi}{\ln 2} f_A t_s \frac{(V_2 + V_4 - V_1 - V_3)}{4I}$$

$$\rho_B = \frac{\pi}{\ln 2} f_B t_s \frac{(V_6 + V_8 - V_5 - V_7)}{4I}$$

where: ρ_A and ρ_B are resistivities in ohm-cm;

t_s is the sample thickness in cm;

V_1 - V_8 represent the voltages measured by the voltmeter;

I is the current through the sample in amperes;

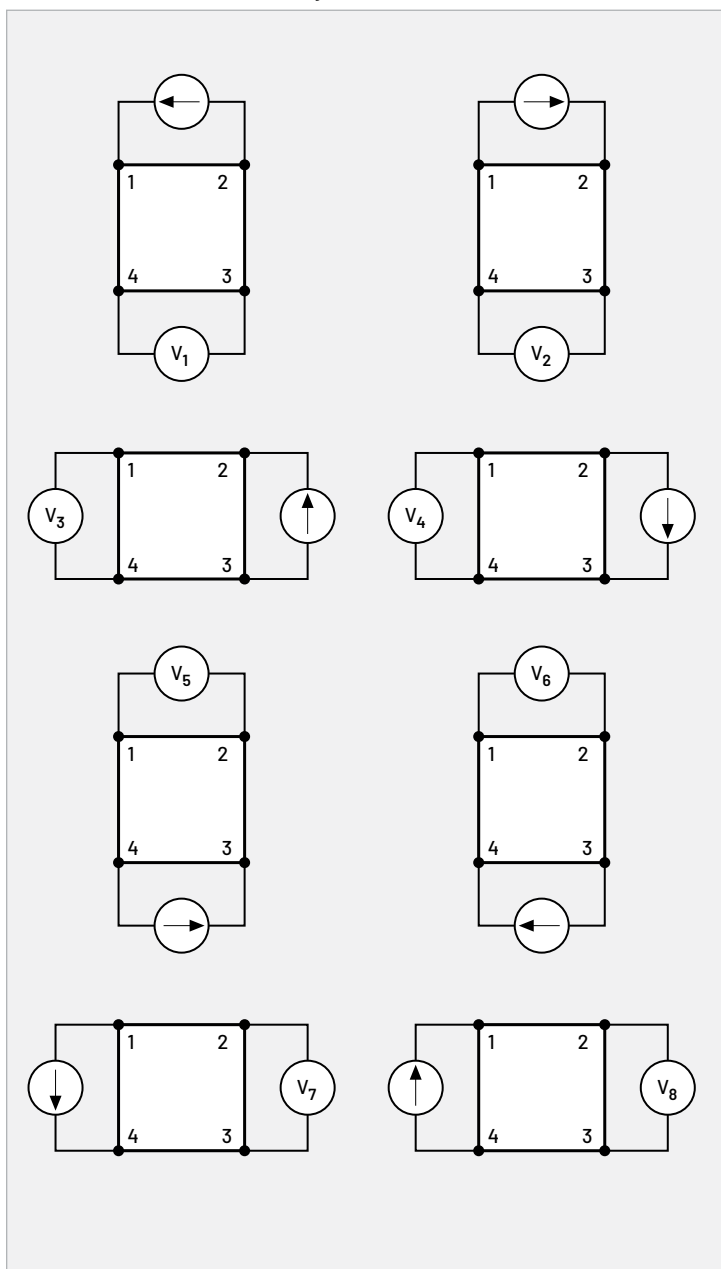
f_A and f_B are geometrical factors based on sample symmetry, and are related to the two voltage ratios Q_A and Q_B as shown in the following equations ($f_A = f_B = 1$ for perfect symmetry).

Q_A and Q_B can be calculated using the measured voltages as follows:

$$Q_A = \frac{V_2 - V_1}{V_4 - V_3}$$

$$Q_B = \frac{V_6 - V_5}{V_8 - V_7}$$

FIGURE 4-48: van der Pauw Resistivity Measurement Conventions

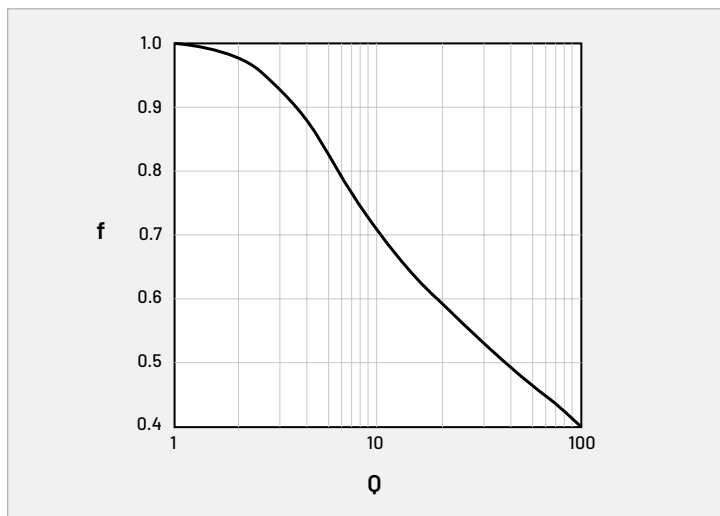


Also, Q and f are related as follows:

$$\frac{Q-1}{Q+1} = \frac{f}{0.693} \operatorname{arc} \cosh \left(\frac{e^{0.693/f}}{2} \right)$$

A plot of this function is shown in **Figure 4-49**. The value of f can be found from this plot once Q has been determined.

FIGURE 4-49: Plot of f vs. Q



Note that if ρ_A and ρ_B are not within 10% of one another, the sample is not sufficiently uniform to determine resistivity accurately, and should be discarded.

Once ρ_A and ρ_B are known, the average resistivity (ρ_{AVG}) can be determined as follows:

$$\rho_{AVG} = \frac{\rho_A + \rho_B}{2}$$

As with the four-point collinear probe method, a differential measurement may be required if the sample resistance is of the same magnitude as the isolation (meter common to ground) of the voltmeter. As **Figure 4-50a** shows, common-mode current may flow between terminals 4 and 3 of the sample. **Figure 4-50b** illustrates how this problem can be resolved by using unity-gain buffers and a differential measurement. Now, very little common-mode current flows between terminals 4 and 3.

The system shown in **Figure 4-51** employs the Keithley 3765 Hall Effect Card to make van der Pauw measurements. The system includes the following instruments: 3765 Hall Card, DMM6500, 6220 Current Source, 6485 Picoammeter, and 3706A System Switch. The current source and voltmeter

FIGURE 4-50a: Erroneous Measurements Caused by Common-Mode Problems

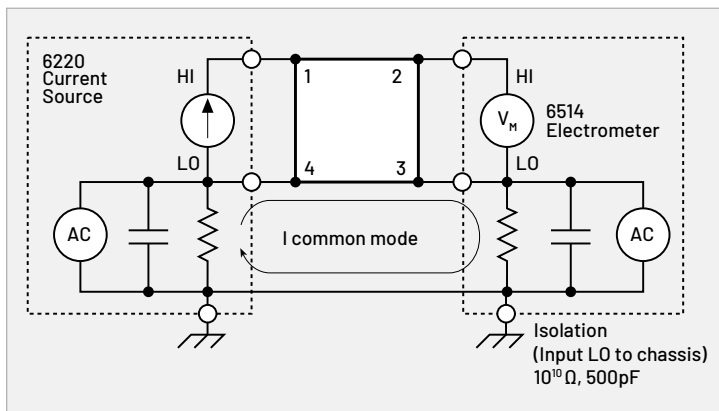
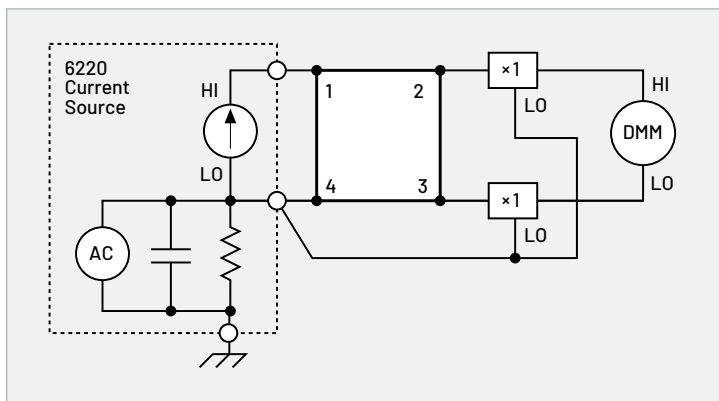


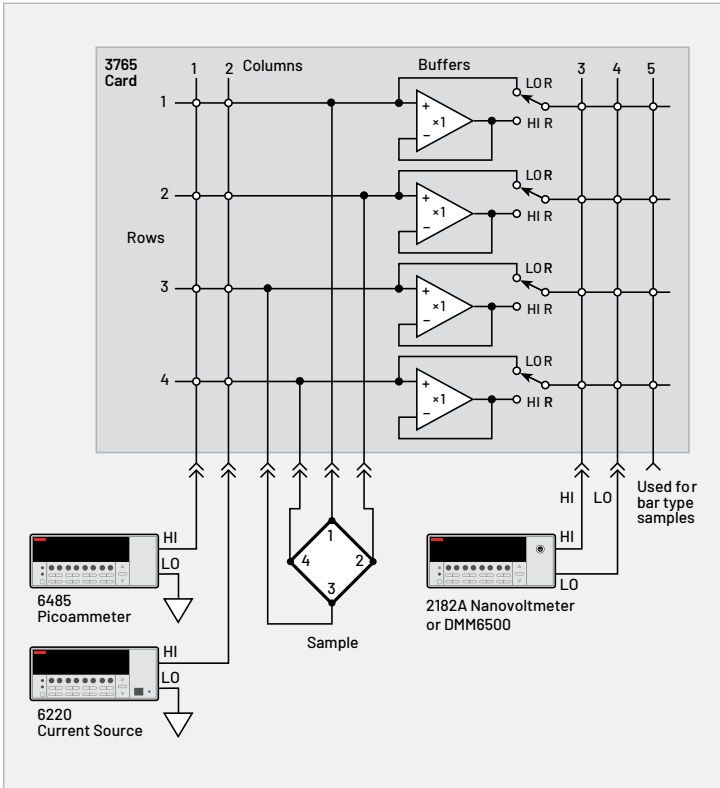
FIGURE 4-50b: Eliminating Common-Mode Errors with a Differential Measurement



are automatically switched to all sides of the sample using the Hall Effect Card. This eliminates the need to connect and disconnect the leads four times. Also, the card has built-in unity-gain buffers, so differential measurements can be made easily on high resistivity samples. With the addition of a controlled magnetic field, this system can also be used to determine the Hall coefficient of the sample.

The 4200A-SCS Parameter Analyzer can measure resistivity using either the four-point collinear probe or van der Pauw methods. Testing high resistance samples requires the use of voltmeters with very high input impedance ($>10^{14} \Omega$) and a current source that can output very small current ($<1 \text{ nA}$) and which has high output impedance. The 4200A-SCS is suitable for this application because it can be configured with multiple SMUs that have high input impedance ($>10^{16} \Omega$) and accurate low current sourcing.

FIGURE 4-51: van der Pauw Measurement System



A 4200A-SCS configured with four SMUs and four preamps can be used to make van der Pauw resistivity measurements. One SMU is connected to each terminal of the sample and a series of eight measurements are made around the sample, as shown in **Figure 4-52**. From these eight measurements, the resistivity is calculated.

The 4200A-SCS includes built-in interactive software, called Clarius, which has a test library that includes vdp tests that use four SMUs. The Clarius software automatically changes the function of each SMU to either a current source, voltmeter, or common in order to source current and measure voltage around all sides of the sample. **Figure 4-53** is a screen capture of the Configure view of the Clarius software of the vdp test for determining volume resistivity.

The Clarius library also has a test for determining the Hall coefficient. An electromagnet must be used for this application.

FIGURE 4-52: SMU Configurations for the eight van der Pauw Measurements in Clarius Software

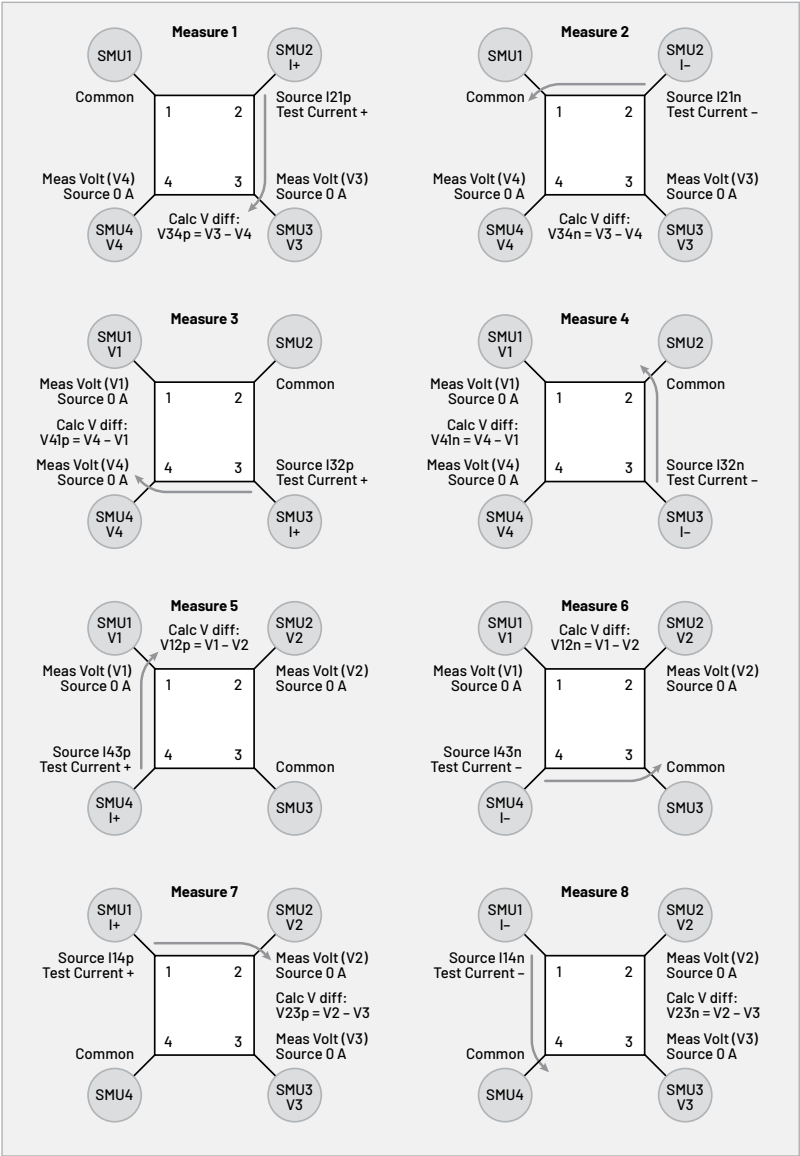
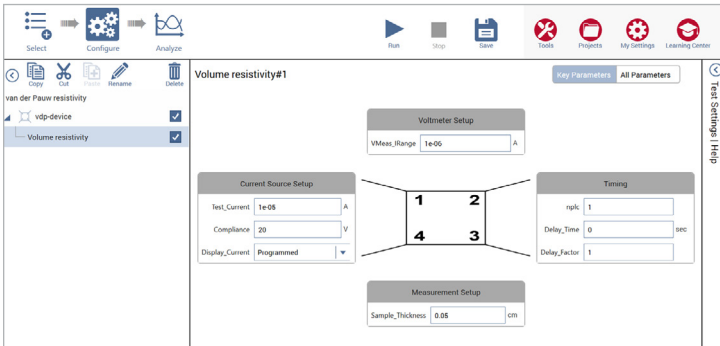


FIGURE 4-53: Screen Capture of van der Pauw resistivity test on the 4200A-SCS



4.4.4 Voltage Coefficient Testing of High Ohmic Value Resistors

Overview

Very high ohmic value resistors may exhibit a significant change in resistance with a change in applied voltage. This effect is known as the voltage coefficient. The voltage coefficient is the percent change in resistance per unit change in applied voltage and is defined as follows:

$$\text{Voltage Coefficient (\%/V)} = \frac{(R_2 - R_1)}{R_1} \times \frac{1}{(V_2 - V_1)} \times 100\%$$

Alternately, the voltage coefficient may be expressed in ppm as follows:

$$\text{Voltage Coefficient (ppm/V)} = \frac{(R_2 - R_1)}{R_1} \times \frac{1}{(V_2 - V_1)} \times 10^6$$

where: R_1 = resistance calculated with first applied voltage (V_1).

R_2 = resistance calculated with second applied voltage (V_2).

$$V_2 > V_1$$

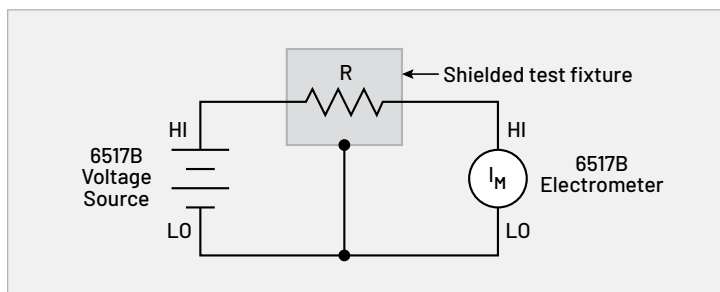
A typical voltage coefficient for a 10 G Ω resistor can be about -0.008 %/V or -80 ppm/V. Thus, if a high resistance is required in a measurement circuit, the error analysis must account for the error due to the voltage coefficient of the resistor, in addition to all other time and temperature error factors.

Using the 6517B to Determine Voltage Coefficient

Measuring the voltage coefficient of a high resistance requires sourcing a voltage and measuring a low current. An electrometer, such as the 6517B, is required to make this measurement. The 6517B has a built-in test sequence for determining voltage coefficient. This test makes resistance measurements at two different voltage levels, then calculates the voltage coefficient. The voltage coefficient is displayed as a percent change in resistance per volt.

Figure 4-54 is a typical test configuration for voltage coefficient measurements with the 6517B. To minimize noise and leakage resistance, the resistor should be placed in a shielded, guarded test fixture. Connect the shield of the test fixture to the LO of the electrometer and connect the LO of the electrometer to the LO of the source. Connect the HI terminal of the electrometer to one end of the resistor and the HI of the voltage source to the other end.

FIGURE 4-54: Connecting the 6517B Electrometer for Voltage Coefficient Testing



The resistor is first measured with test voltage V_1 , giving R_1 . Next, it is measured with test voltage V_2 (where V_2 is greater than V_1), giving R_2 . The voltage coefficient for the resistor is then calculated using the equation given in the overview.

4.4.5 Insulation Resistance (IR) Measurements of Cables

Overview

A cable's insulation resistance (R_{IR}) is the ratio of the voltage applied to the conductors of the cable to the total current between them. This current is known as the leakage current. For a coaxial cable, the R_{IR} would be the resistance between the shield and center conductor. Insulation resistance measurements are also performed on cables with multiple conductors. In these instances, the insulation resistance is measured from one conductor to all other conductors and the shield.

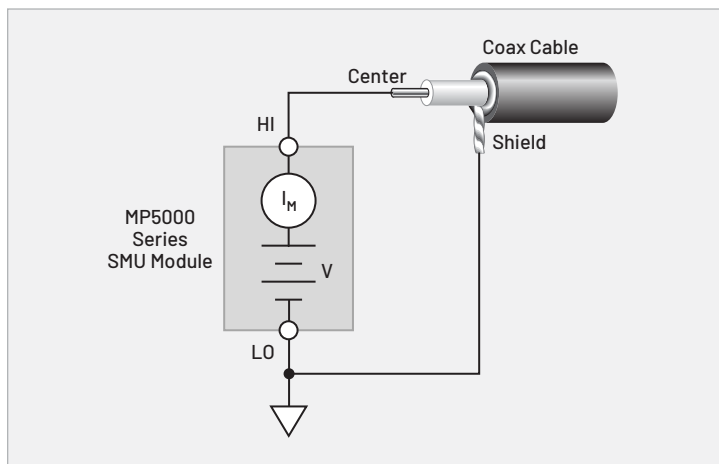
Insufficient insulation resistance may indicate a cable failure, such as a soft short, a mechanical failure, insulator defects, or moisture penetration and contamination. High insulation resistance is especially important in applications where the source resistance is very high. If the insulation resistance is of the same magnitude as the source resistance, system errors are inevitable.

The insulation resistance between the conductors within a cable should be very high, usually >10 G Ω . Because the leakage currents being measured are usually in the nanoamp to picoamp ranges, an electrometer, picoammeter, or SMU are required due to their excellent low current sensitivity.

Using an SMU to Measure the IR of a Coax Cable

Figure 4-55 illustrates a test configuration for measuring the insulation resistance between the two conductors of a coaxial cable using an SMU. The two conductors in a coaxial cable are the center conductor and the shield, or outer conductor. In this circuit, the HI terminal of the MP5000 Series SMU Module is connected to the center conductor, and the LO terminal of the SMU is connected to the outside shield of the coax cable.

Figure 4-55: SMU Connections for Insulation Resistance Test of Coax Cable



After the connections are made, a test voltage (V) is applied to charge the dielectric. The voltage and soak times should be specified in the test criteria, and the same values should be used consistently. After the specified time, the resulting current (I) is measured. The insulation resistance is then calculated, using the equation:

$$R_{IR} = V/I.$$

IR measurements are often performed at a specified cable length. For example, the R_{IR} per 1000 feet of cable can be written as R_{IR1000} . In some cases, other geometrical considerations, such as the diameter of the cable, may be taken into account in the IR calculation.

To avoid errors due to leakage current, the resistance of the cables and fixtures used to make the measurement should be as high as possible. Electrostatic shielding should be used to avoid noisy readings.

4.5 Charge Measurement Applications

The coulombmeter and the techniques used to make charge measurements were described in Sections 1 and 2 respectively. Charge measurements include applications such as measuring capacitance and static

charge on objects. As discussed in Section 2.3.8, charge measurement techniques can also be used to measure very low currents (<10 fA).

4.5.1 Capacitance Measurements

Overview

The coulombs function of an electrometer can be used with a step voltage source to measure capacitance. This technique is especially useful for testing cables and connectors because it can measure capacitances ranging from <10 pF to hundreds of nanofarads. The unknown capacitance is connected in series with the electrometer input and the step voltage source. The calculation of the capacitance is based on this equation:

$$C = \frac{Q}{V}$$

Test Method

The basic configuration for measuring capacitance with a 6517B Electrometer is shown in **Figure 4-56**. The instrument is used in the charge (or coulombs) mode and its internal voltage source provides the step voltage. Just before the voltage source is turned on, disable the meter's zero check and suppress the charge reading by using the REL function to zero the display. Then, turn on the voltage source and note the charge reading immediately. The capacitance is calculated from:

$$C = \frac{Q_2 - Q_1}{V_2 - V_1}$$

where: Q_2 = final charge

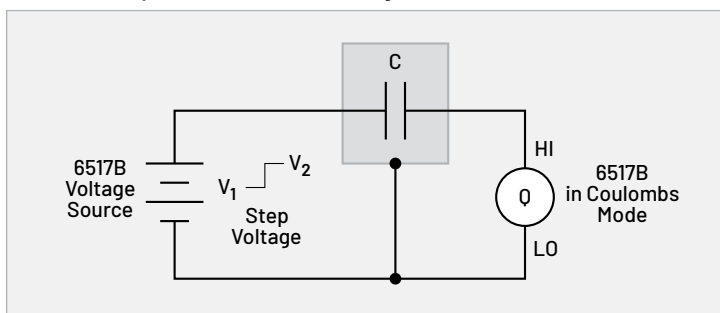
Q_1 = initial charge assumed to be zero

V_2 = step voltage

V_1 = initial voltage assumed to be zero

After the reading has been recorded, reset the voltage source to 0 V to dissipate the charge from the device. Before handling the device, verify the capacitance has been discharged to a safe level.

FIGURE 4-56: Capacitance Measurement Using 6517B Electrometer



The unknown capacitance should be in a shielded test fixture. The shield is connected to the LO input terminal of the electrometer. The HI input terminal should be connected to the highest impedance terminal of the unknown capacitance. For example, when measuring the capacitance of a length of coaxial cable, connect the HI terminal of the electrometer to the center conductor of the cable, allowing the cable shield to minimize electrostatic interference to the measurement.

If the rate of charge is too great, the resulting measurement will be in error because the input stage becomes temporarily saturated. To limit the rate of charge transfer at the input of the electrometer, add a resistor in series between the voltage source and the capacitance. This is especially true for capacitance values >1 nF. A typical series resistor would be 10 k Ω to 1 M Ω .

4.5.2 Using a Faraday Cup to Measure Static Charge on Objects

Overview

Insulators permit only a slight motion of electrons; therefore, electrostatic charges can build up on a material and create hazards. The problem generally is not the static charge itself on the object, but rather the spark generated when the object discharges. Therefore, in order to understand and control these problems, it's necessary to measure the static electricity on an object. This can be done by placing the object in a Faraday cup and measuring the charge with an electrometer. The Faraday cup method can be used to measure the charge on a wide range of substances and objects, such as plastics, films, liquids, gases, and electronic components.

A Faraday cup (sometimes called a Faraday cage) is an enclosure made of sheet metal or conductive mesh. The electric field within a closed, empty conductor is zero, so the cup shields the object placed inside it from any atmospheric or stray electric fields. This allows measuring the charge on the object accurately.

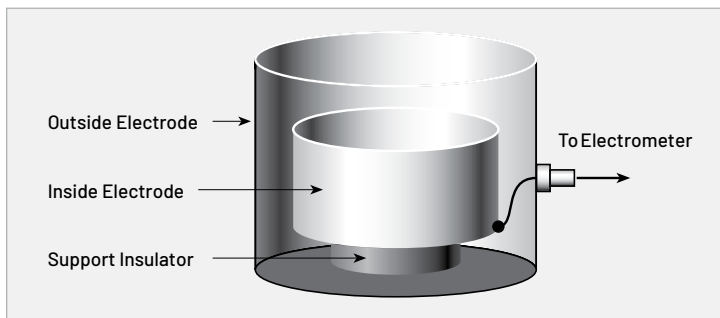
Description of a Faraday Cup

Figure 4-57 illustrates a Faraday cup. It consists of two electrodes, one inside the other, separated by an insulator. The inside electrode is connected to the electrometer HI and the outside electrode is connected to the electrometer LO. When a charged object is placed within the inside electrode, an induced charge will flow into the electrometer.

A Faraday cup can have virtually any dimensions, depending on the size and shape of the object to be tested. Cylindrical and spherical shapes are typically the most convenient choices—simple metal containers like paint cans are often used. The electrodes can be made of any conductive material. The support insulators should be made of materials with very high resistance, such as Teflon[®] or ceramic.

For convenience in making connections, mount a BNC connector on the outside electrode. Connect the outer or shield connection of the BNC

FIGURE 4-57: Faraday Cup



connector to the outside electrode, then connect the inner conductor of the BNC connector to the inside electrode. Use an adapter to connect the BNC connector to the triax input of the electrometer.

Test Procedure

To perform the test, connect an electrometer to the Faraday cup using a shielded cable. Turn on the electrometer, select the coulombs function, then disable “Zero Check.” Press “Rel” to zero the display. Drop the charged object to be tested into the Faraday cup. Note the charge reading on the electrometer immediately; don’t wait for the reading to settle because the input bias current of the electrometer will continue charging the input of the meter. This is particularly important when the unknown charge is at the pico-coulomb level. If the object is conductive, it will be discharged as soon as it touches the electrode. Enable “Zero Check” to re-zero the meter in preparation for the next measurement.

4.6 Low Voltage Measurement Applications

Nanovoltmeters and techniques for measuring low voltage were described in Sections 1 and 3 respectively. In particular, Section 3.2 discussed a number of error sources that can seriously affect precision measurements. Low voltage measurement applications include standard cell comparisons, high resolution temperature measurements and microcalorimetry, and Hall voltage measurements.

4.6.1 Standard Cell Comparisons

Overview

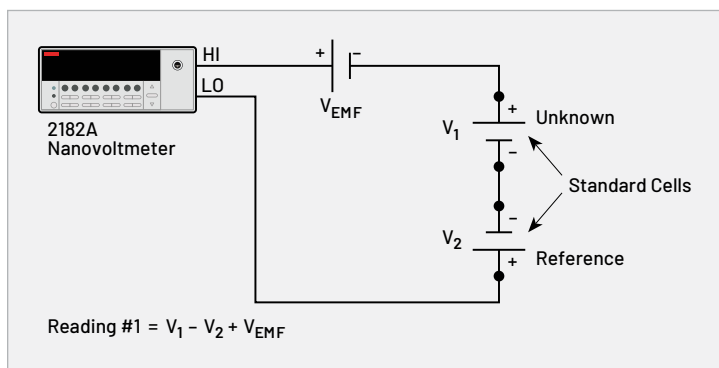
Standard cells are electrochemical cells used as voltage references in many electrical standards laboratories. If cared for properly, standard cells are very stable. The voltage of the individual cell is determined by calculating the present value, based on a series of measured cell differences, from an accepted reference.

Because individual cells may differ by only a few microvolts, making accurate measurements requires using a nanovoltmeter and low voltage measurement techniques. This application describes comparing two standard cells and comparing a standard cell with a precision voltage reference.

Comparing Two Standard Cells

Standard cell intercomparisons require measuring the potential difference between a reference and an unknown standard cell. All cell differences are determined in a series opposition configuration. As shown in **Figure 4-58**, the negative terminals of the standard cells, V_1 and V_2 , are connected. Copper conductors connect the cells to the voltmeter to minimize errors due to thermoelectric EMFs (V_{EMF}).

FIGURE 4-58: Connections for Standard Cell Comparison, Reading #1

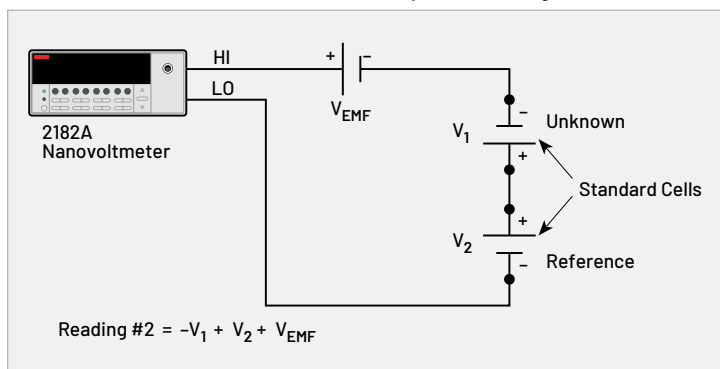


Once the measurement connections are made, take care to avoid errors due to thermally generated potentials. To minimize the effects of thermoelectric EMFs, a second measurement is taken with the cells reversed, as shown in **Figure 4-59**. The small voltage difference is calculated by averaging the absolute values of the two readings, as discussed in Section 3.2.2.

Throughout the entire intercomparison process, it's desirable to establish the stability of a measured cell difference by calculating a standard deviation across several redundant readings.

Once stability is achieved, the voltage for each cell is calculated based on the group mean. Several readings are usually averaged for each comparison. This process of intercomparing cells is repeated at intervals established by the standards laboratory. The results can be plotted and compared over time. This process is useful for maintaining fewer than six cells. If more cells must be maintained, an automated scanner with computer control can be used to manage them more effectively.

FIGURE 4-59: Connections for Standard Cell Comparison, Reading #2

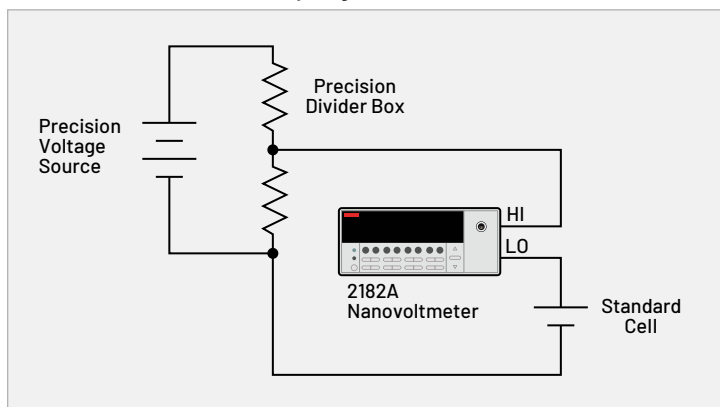


Comparing a Precision Voltage Source with a Standard Cell

A standard cell can be used to determine the value of a precision DC voltage source, as shown in **Figure 4-60**. A precision divider box divides the voltage source down to roughly the standard cell voltage. A nanovoltmeter is used as a null detector to determine the difference between the divider output and the standard cell. Once the divider ratio and the standard cell voltage are known, the precision DC source voltage can be determined. Take care to avoid drawing any current from the standard cell, which would cause the cell voltage to drift.

The output impedance of the divider is likely to be much higher than the standard cell impedance, so the nanovoltmeter HI terminal must be connected to the divider output, as shown in **Figure 4-60**, to prevent common-mode current from creating additional voltage drop to the resistive divider.

FIGURE 4-60: Connections for Comparing Precision DC Source to Standard Cell



4.6.2 High Resolution Temperature Measurements and Microcalorimetry

Overview

Microcalorimetry measurements are used to determine various energy relationships. Microcalorimetry techniques are often required when performing calorimetric experiments with small sample sizes or slow heating rates. The design of a microcalorimeter can vary greatly, depending on the specific application, and many are custom made. When running tests, differential thermometry techniques allow users to measure small changes in temperature. Microcalorimetry experiments may require measuring temperature changes as small as $100\ \mu^{\circ}\text{C}$. This section describes two types of temperature sensors and a microcalorimetry measurement system using thermocouples and a 2182A Nanovoltmeter.

Temperature Sensors

Thermistors and thermocouples are common types of transducers used in differential thermometry. The choice of transducer depends on the specific microcalorimetry application.

Thermistors are temperature-sensitive resistors with good linearity and accuracy characteristics. These devices require a test current, so they will dissipate power in the form of heat, which may lead to a measurement error.

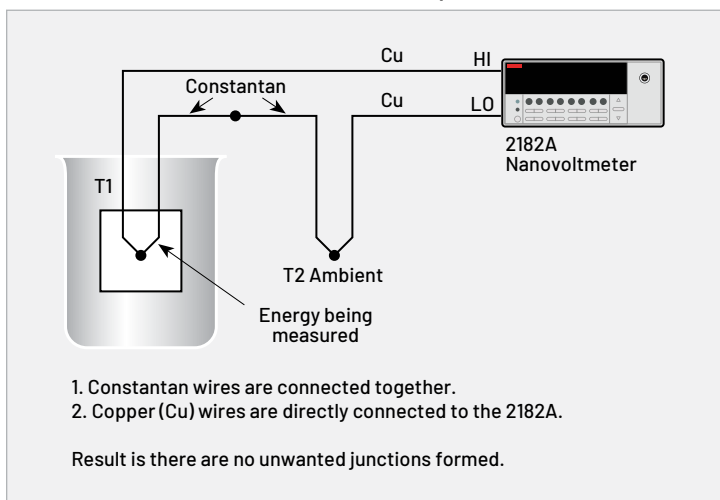
Thermocouples are the most widely used type of temperature sensor. These rugged and inexpensive devices are formed by the junction of two dissimilar metals. Several different types, covering a wide temperature range, are available. Thermocouple linearity varies, depending on thermocouple type and temperature range.

Description

Microcalorimeter design can vary greatly, depending on the experiment being performed. The application described here uses a simple calorimeter to perform microcalorimetry measurements. Temperature is measured with a differential thermocouple thermometer. In the differential configuration, one thermocouple is placed inside the calorimeter and the other is placed outside the device. The difference voltage is proportional to the temperature differential. Measurement sensitivity is approximately $25\ \text{m}^{\circ}\text{C}/\mu\text{V}$ (depending on the thermocouple type). **Figure 4-61** shows a typical microcalorimeter setup, which uses a 2182A Nanovoltmeter to make the necessary voltage measurements.

The temperature inside the calorimeter need only be known with moderate accuracy; however, to measure the small changes that occur during the experiment, the greatest precision and resolution are essential. Measuring the differential thermocouple's signal to sub-millidegree resolution demands a very sensitive voltmeter. The 2182A can detect temperature changes of about $100\ \mu^{\circ}\text{C}$, depending on the type of thermocouple

FIGURE 4-61: Microcalorimeter with Differential Temperature Measurement



used. Each thermocouple type is unique in terms of the amount of potential for a given change in temperature.

The thermocouples may be calibrated in a separate apparatus or they may be an integral part of the calorimeter and calibrated in place. Calibration can be performed using a standardized thermometer at the approximate temperature range of the test or with a fixed point reference (e.g., the boiling point of oxygen).

Before a test can be performed, the heat capacity of the calorimeter must be determined. This can be determined by directly measuring the temperature increase associated with the introduction of a known quantity of heat. Heat can be precisely determined by sourcing a current accurately through a known resistance. Heat can also be introduced by a standard chemical reaction.

Making low temperature measurements with thermocouples means that low level voltages are being measured, so take special care to consider the effects of both thermoelectric EMFs and magnetic fields on measurement accuracy. See Sections 3.2.2 and 3.2.9 for more details on these aspects.

Running the Test

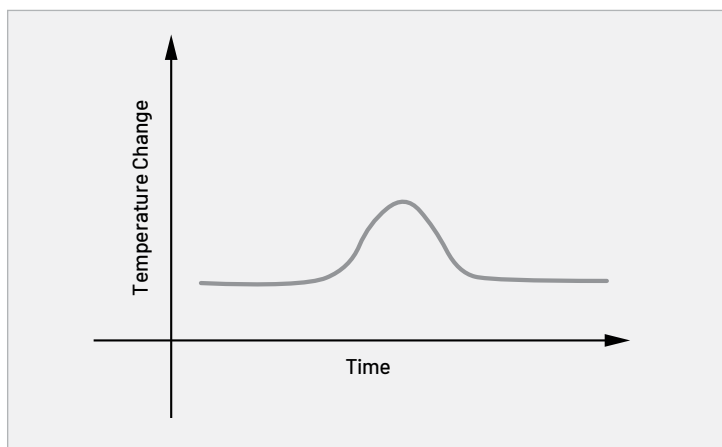
For best results, set the 2182A for the 10 mV range and for line cycle integration (1NPLC) for maximum line frequency noise rejection. Enabling the filter can reduce noise further. Take care that the response time of the filter doesn't cause errors in the peaks of the heat curve. A slow responding filter will smooth the peaks of the data, which could allow vital temperature

information to be lost. The 2182A provides a selection of filter settings to optimize system noise rejection and ensure proper peak detection.

The temperature inside and outside the calorimeter must be the same before the experiment begins. A temperature difference of 0° corresponds to a differential thermocouple output voltage of 0 V. If a change in temperature occurs, it's assumed to be caused by the phenomena of the experiment.

After the test is complete, the data can be applied to the calibration curve, converted to temperature, and analyzed. **Figure 4-62** is a typical heat curve graph of a chemical reaction. The final result is usually expressed as heat (calories) or energy (joules).

FIGURE 4-62: Typical Heat Reaction Curve



4.6.3 Hall Voltage and Resistivity Measurements of a Six-Terminal Bridge Sample

Overview

Important material parameters include mobility, resistivity, carrier density, and carrier type. These parameters are often determined using Hall effect measurement techniques. For resistivity measurements, a known current is forced and the resulting voltage is measured. If a magnetic field is applied to the sample perpendicular to the direction of the current flow, an electric field will develop that is perpendicular to both the current and the magnetic field. This electric field is known as the Hall voltage. Once the Hall voltage is known, the Hall coefficient can be defined as follows:

$$R_H = \frac{V_H t}{IB}$$

where: R_H = Hall coefficient (m^3/C)
 V_H = Hall voltage (V)
 t = sample thickness (m)
 I = current (A)
 B = magnetic field (T)

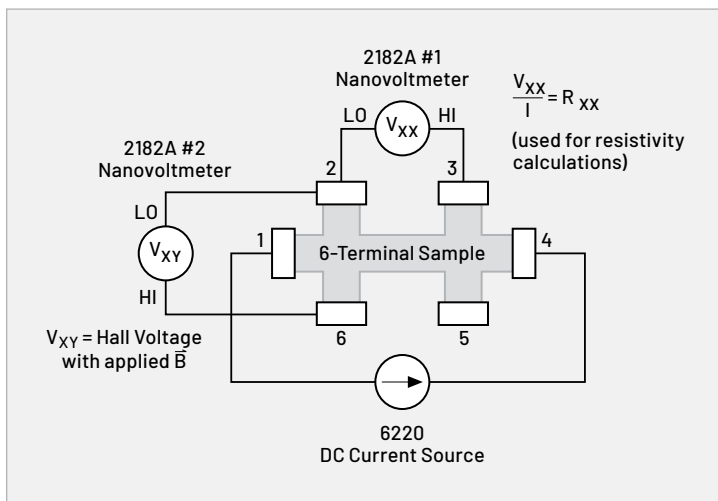
From the Hall coefficient and the resistivity, the carrier concentration and mobility can be calculated.

Method

Although there are several types of resistivity/Hall structures, this example uses a six-terminal bridge sample. Sections 4.4.3 and 4.7.3 offer information on Hall voltage and resistivity measurements on four-terminal samples.

Figure 4-63 illustrates an example test configuration for a six-terminal bridge sample. For this example, a 6220 Current Source is connected between terminals 1 and 4 of the sample. One nanovoltmeter (2182A #1) measures the voltage drop between terminals 2 and 3. This voltage (V_{xx}) is used for determining the resistivity of the sample. A second nanovoltmeter (2182A #2) is connected between terminals 2 and 6 and is used to measure the Hall voltage (V_{xy}). This voltage measurement is made with an applied magnetic field and is perpendicular to the current flow. Notice the LO terminals of both voltmeters are connected to terminal 2. A nanovoltmeter is used because the Hall voltage is usually very small, in the millivolt or microvolt range.

FIGURE 4-63: Example Test Configuration for Finding Hall Voltage with a Six-Terminal Bridge Sample



4.7 Low Resistance Measurement Applications

Low resistance measurement applications include contact resistance, superconductor resistance, and resistivity measurements of conductors. These measurements can be made with either a sensitive ohmmeter or a nanovoltmeter with a current source. See Section 3.3 for a discussion of precision low resistance measurement methods.

4.7.1 Contact Resistance

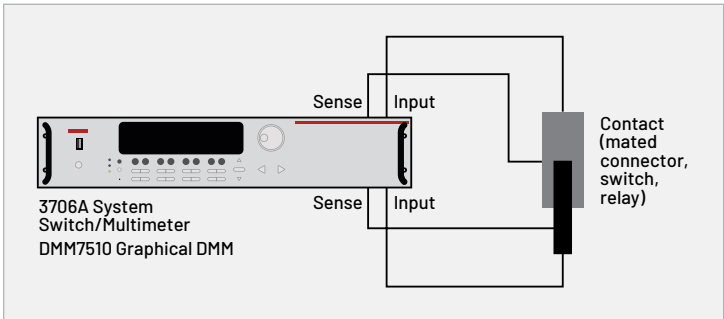
Overview

Contact resistance is the resistance to current flow through a closed pair of contacts. These types of measurements are made on components such as connectors, relays, and switches. This resistance is normally very small, ranging from micro-ohms to a few ohms. Test procedures may vary, depending on the type of device and the application. ASTM Method B539, “Measuring Resistance of Electrical Connections” and EIA-364 Test Procedure 23, “Low-Level Contact Resistance Test” are two published test procedures commonly used to measure contact resistance. In general, certain basic principles apply to four-terminal contact resistance measurements.

Measurement Method

Figure 4-64 illustrates a basic configuration for testing contact resistance of a contact. A sensitive ohmmeter with four-terminal measurement capability is used to prevent lead resistance from being added to the measurement. The source terminals are connected on either end of the contact pair. The sense terminals are connected as closely as possible to the voltage drop across the contact. This is intended to keep the voltage drop due to the test leads and bulk resistance from being included in the measurement. The bulk resistance is the resistance the total contact would have if it were a solid piece of metal having an identical geometry so that the actual contact area had zero resistance.

FIGURE 4-64: Using a DMM to Measure Four-Wire Resistance Across Contact



It is sometimes difficult to make a four-wire connection to a device designed for just two wires. The style of device will determine how to make the connections. In general, devices should be prepared for testing as much as possible as they would be used in a normal application. Voltage probes should be placed on the sample in a manner that does not mechanically disturb the contact. For instance, soldering the probes may cause unexpected changes in the contact resistance. In some cases, however, soldering may be unavoidable. Each connection point to the test contact can create thermoelectric EMFs; however, these can be compensated for by using either the current-reversal or offset-compensation method, which are described in Section 3.3.2.

Dry Circuit Testing

Often, the purpose of the contact resistance test is to determine whether contact oxidation or other surface film buildup has increased the resistance of the device under test. If the voltage across the device is too high for even a short time, the oxide layer or film will be ruptured, compromising the validity of the test. The level of voltage required to break down a film usually ranges from 30 mV to 100 mV.

Excessive current through the contacts during testing can cause a physical change in the contact area on a microscopic level. Current can cause heating, which can soften or melt the contact points and the surrounding area. As a result, the contact area enlarges, resulting in a reduction in resistance.

To avoid these problems, the dry circuit method is usually employed for contact resistance tests. A dry circuit is one in which the voltage and current are limited to levels that can't cause changes in the physical and electrical condition of the contact junction. In general, that means the open circuit voltage is 20 mV or less and the short circuit current is 100 mA or less.

Because of the low test current level used, a very sensitive voltmeter is required to measure the voltage drop, which is usually in the microvolt range. Because of the potential for physical or electrical changes to the contact that other test methods pose, dry circuit measurements should be done on the device before any other electrical tests are made. Some sensitive DMMs have built-in dry circuit resistance modes to limit the open circuit DUT voltage.

Refer to Section 3.3.5 for further information on dry circuit testing.

Using a Sensitive DMM

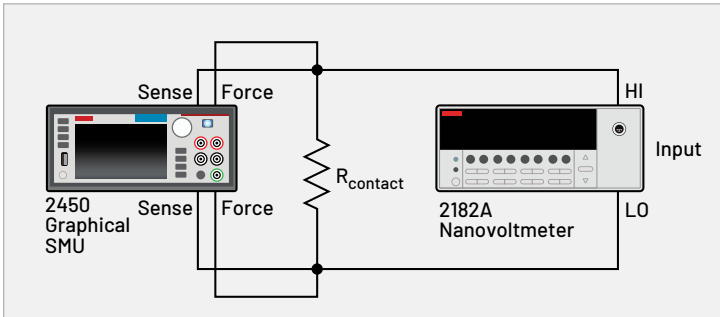
Figure 4-64 shows a basic configuration for making four-wire contact resistance measurements with a 3706A System Switch/Multimeter. This instrument can automatically compensate for thermoelectric offsets in the sense circuit by using the Offset Compensation mode. They also have built-in dry circuit measurement capability. For most applications, the DMM is sufficient for contact resistance measurements. If the short circuit

current or measured resistance values are much smaller than the DMM's specifications, a nanovoltmeter and a current source or SMU must be used.

Using a Nanovoltmeter and SMU

Figure 4-65 illustrates a test configuration that employs a 2182A Nanovoltmeter and a 2450 Graphical SourceMeter SMU for contact resistance measurements. The SMU forces a current through the contact and the nanovoltmeter measures the voltage drop developed across the contact. For dry circuit testing, the open circuit voltage is clamped to 20 mV by setting the SMU compliance to 20 mV. To ensure the compliance voltage is measured only across the contact and not across the test leads, the SMU is configured for the four-wire mode. This is especially important when higher currents are used because the voltage drop across the test leads may be large compared to the voltage drop across the contact. To prevent transients, always turn the source off while switching contacts in and out of the test fixture. A resistor, such as 100 Ω , can be placed directly across the current source output terminals to reduce transients still further.

FIGURE 4-65: Using a 2182A Nanovoltmeter and 2450 SourceMeter SMU to Measure Contact Resistance



The current-reversal method can be used to minimize thermoelectric voltage offsets. The 2182A's Delta Mode feature and the SMU make it possible to implement this technique automatically. In this mode, the 2182A automatically triggers the current source to alternate the polarity, then triggers a reading at each polarity. Then, the 2182A displays the "compensated" voltage value:

$$\text{Delta } V = \frac{V_1 - V_2}{2}$$

The contact resistance may be calculated by:

$$R_{\text{contact}} = \frac{\text{Delta } V}{I}$$

where I = absolute value of test current.

4.7.2 Superconductor Resistance Measurements

Overview

At extremely low temperatures, some metals and alloys lose their resistance to electrical current and become superconductive. A superconductor's transition temperature and critical current density are two commonly measured parameters. The superconducting transition temperature is the point at which a material's resistance changes from a finite value to zero. The critical current density is the maximum current density a material can carry under specific temperature and magnetic field conditions before it becomes resistive. The higher these two parameters are, the better the superconductor is. Determining these two parameters requires measuring very small resistances, so a nanovoltmeter and a programmable current source or SMU are essential for precision measurements.

Test Description

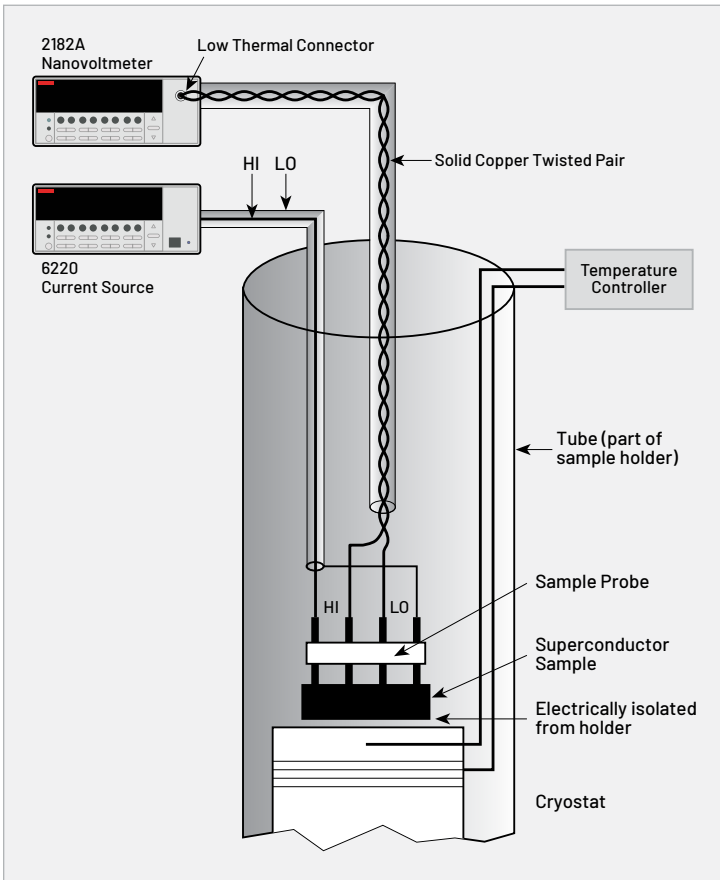
Figure 4-66 shows a basic superconductor resistance measurement test system using the combination of a 2182A Nanovoltmeter and a 6220 Current Source for measuring the resistance. The voltage leads should be made of a material with a low Seebeck coefficient with respect to the sample. The sensitivity of the 2182A Nanovoltmeter is crucial to obtaining precision measurements because the application demands the ability to measure extremely low voltages.

For transition temperature measurements, the current source must be kept below the critical current of the sample. If the current becomes too high, the power dissipated may damage the sample and the cryostat. For critical current measurements, however, the current source must be able to exceed the critical current of the sample. If that means that more than 100 mA is needed (the current the 6220 Current Source can provide), a 2460 or 2461 High Current Graphical SourceMeter SMU may be an appropriate solution. For up to 20 A DC, use the 2651A High Power SourceMeter SMU. The current source should have programmable polarity, so the test can be performed using the current-reversal method.

The resistance is measured using the techniques described in Sections 3.2 and 3.3 for low voltage and low resistance measurements. It is essential that a four-wire measurement be made. This technique eliminates lead resistance by forcing a current through the sample with one pair of leads while measuring the voltage drop with a second pair of leads. In addition, the Delta method is essential to eliminate the effects of changing thermoelectric EMFs, which may interfere with measurement accuracy.

The Delta method consists of measuring the voltage drop across the material with the current in one direction, then reversing the polarity of the current source and taking a second voltage measurement. Three voltage measurements are used to calculate each resistance value. The Delta method is discussed in greater detail in Section 3.3.2. In cases where hysteresis, non-linearity, or asymmetry is apparent, the current can be varied

FIGURE 4-66: Superconductor Resistance Test System



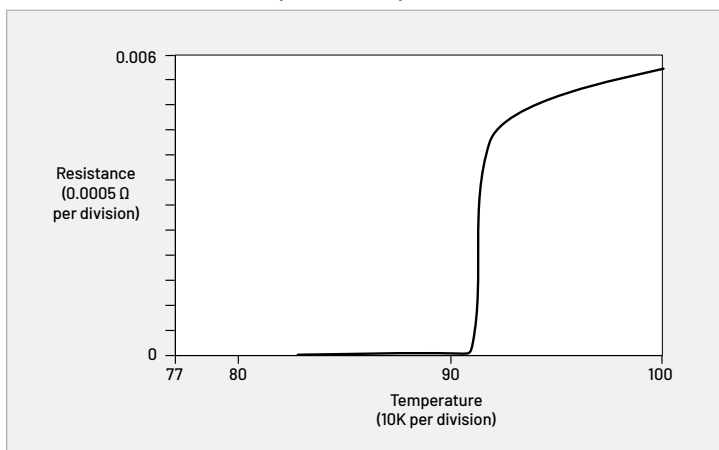
from one value to another of the same polarity. This will provide the average resistance between these two currents.

The 2182A Nanovoltmeter and 6220 Current Source work together to implement the Delta method automatically. In this mode, the 6220 automatically alternates the polarity, then triggers the nanovoltmeter to take a reading at each polarity. Then, the 6220 displays the “compensated” resistance value.

As shown in **Figure 4-67**, the resistance can be plotted vs. temperature as the sample temperature is changing.

For determining the critical current, the 2182A and 6220 Current Source can be used together to produce a precision I-V curve over a range of currents.

FIGURE 4-67: Resistance vs. Temperature of Superconductor



4.7.3 Resistivity Measurements of Conductive Materials

Overview

The resistivity of a conductor is determined by measuring the resistance of a sample of known geometry by forcing a current through the sample with one pair of leads while measuring the voltage drop with a second pair of leads. While the specific method used for determining the resistivity depends on the size and shape of the sample, all methods require a sensitive voltmeter with a current source or a sensitive ohmmeter to make the measurements because the measured resistance is usually very low.

Resistivity of Bulk Materials

Figure 4-68 shows a system for testing the resistivity of a bulk material such as a metal bar or rod. The current source is connected to both ends of the sample. The voltmeter leads are placed a known distance apart. The resistivity is calculated from the cross-sectional area of the sample and the distance between the voltmeter leads:

$$\rho = \left(\frac{V}{I} \right) \left(\frac{A}{L} \right)$$

where: ρ = resistivity in ohm-cm

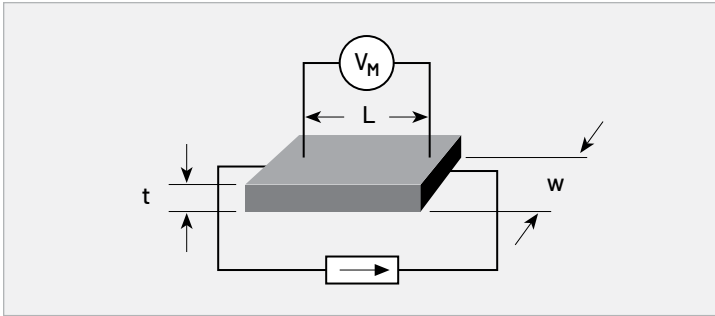
V = voltage measured by voltmeter

I = source current

A = cross sectional area of sample ($w \times t$) in cm^2

L = length of distance between voltmeter leads in cm

FIGURE 4-68: Measuring Resistivity of Metal Bar



To compensate for thermoelectric voltages, one voltage reading is taken with positive test current and another taken with negative current. The absolute values of these two readings are then averaged and used in the equation for V/I . Most materials have a significant temperature coefficient, so be certain to maintain the sample at a known temperature.

Using a Four-Point Probe

The four-point probe method is used on very thin samples such as epitaxial wafers and conductive coatings. **Figure 4-69** is a diagram of four-point collinear probe setup for resistivity measurements. The current is sourced through the two outer probes and the voltage drop is measured between the two inner probes. The surface or sheet resistivity is calculated by:

$$\sigma = \left(\frac{\pi}{\ln 2} \right) \left(\frac{V}{I} \right)$$

where: σ = sheet resistivity in ohms/square

V = voltage measured by voltmeter (V)

I = source current (A)

Note that the units for sheet resistivity are expressed as ohms per square in order to distinguish this number from the measured resistance (V/I). Correction factors to the resistivity calculation may be required for extremely thin or thick samples.

If the thickness of the sample is known, the bulk or volume resistivity can be calculated from

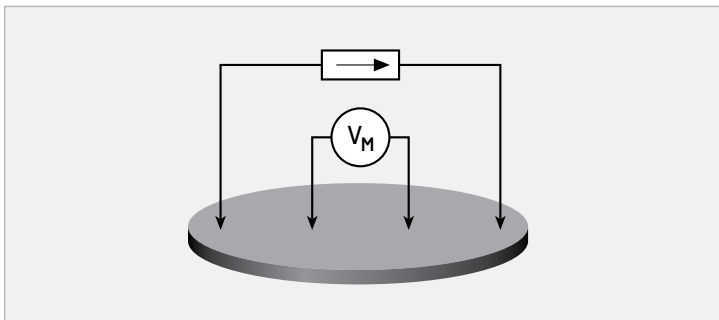
$$\rho = \left(\frac{\pi}{\ln 2} \right) \left(\frac{V}{I} \right) t$$

where: ρ = volume resistivity in ohm-cm

t = thickness in centimeters

Further information on four-point probe measurements can be found in the SEMI® International Standard MF84-0312.

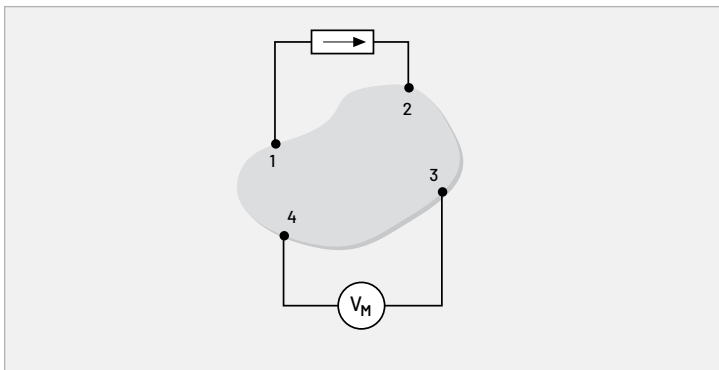
FIGURE 4-69: Using a Four-Point Collinear Probe on Wafer



van der Pauw Method

Although van der Pauw resistivity measurements are used primarily in the semiconductor industry, they have other applications, such as determining the resistivity of superconductors or foil. The van der Pauw method is used on samples that are flat, homogeneous in thickness, and arbitrarily shaped, and that do not contain any isolated holes. As shown in **Figure 4-70**, the contacts should be small and placed on the periphery of the sample.

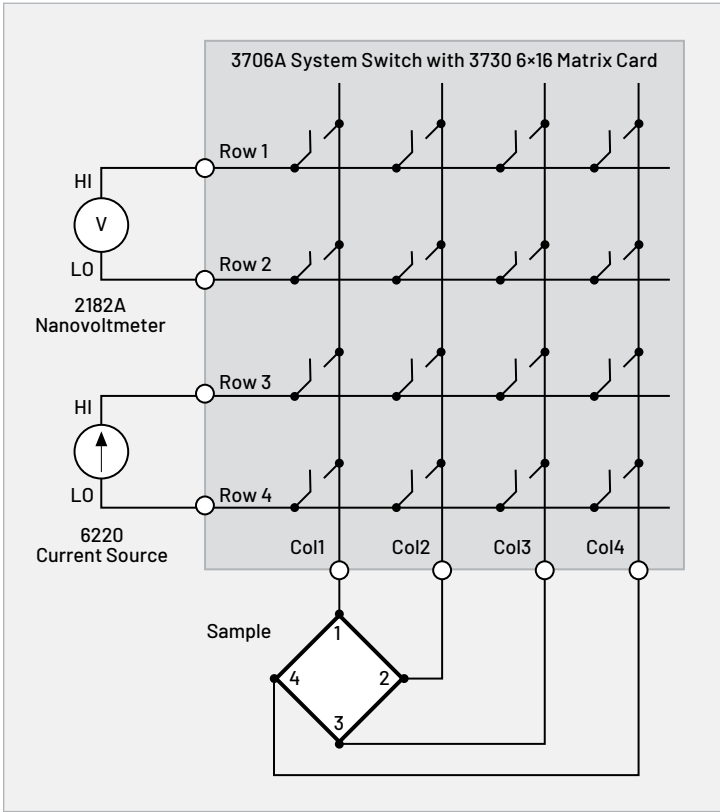
FIGURE 4-70: van der Pauw Connections



Eight measurements are made around the sample. These readings are combined mathematically to determine the average resistivity of the sample and compensate for offsets. The formula for determining the resistivity is given in Section 4.4.3. Further information on van der Pauw measurements can be found in the ASTM method F76.

Figure 4-71 illustrates a complete system for determining the resistivity of a conductive sample using the van der Pauw method. The system includes a 6220 Current Source to supply the current through the sample and a 2182A Nanovoltmeter to measure the resulting voltage drop. A switching matrix switches the voltmeter and current source among the

FIGURE 4-71: van der Pauw Connections Using Matrix Switching



four sample terminals. The 3706A System Switch with the 3730 6x16 Matrix Card switches the voltmeter and current source among the four terminals of the sample.

To source current between terminals 3 and 4 of the sample, close cross points Row3/Col3 and Row4/Col4. Measure the resulting voltage drop between terminals 1 and 2 of the sample by closing cross points Row1/Col1 and Row2/Col2.

If a wide range of sample resistivities must be measured, use the 3765 Hall Effect Card in the 3706A System Switch instead of the other switching options.

SECTION 5

**Low Level
Instrument
Selection Guide**

5.1 Introduction

Choosing a specific low level instrument for a given application depends on a variety of factors, including:

- Functions (voltage, current, resistance, charge)
- Ranges and sensitivity
- Special features required (sourcing, floating operation, etc.)
- Convenience features (programmability, dual-channel, data storage, etc.)
- Price
- Compatibility with other equipment in the test setup (analog output, overload protection, external triggers, etc.)

This section provides an overview of low level instruments available from Keithley. Consult tek.com for further details on any of the products covered in this book (and many other products not listed).

5.2 Instrument Selector Guides

Figure 5-1 is a function/range comparison chart for Keithley’s low level instrumentation. **Tables 5-1** through **5-5** summarize the capabilities of these instruments.

FIGURE 5-1: Low Level Instrument Function/Range Selection Chart

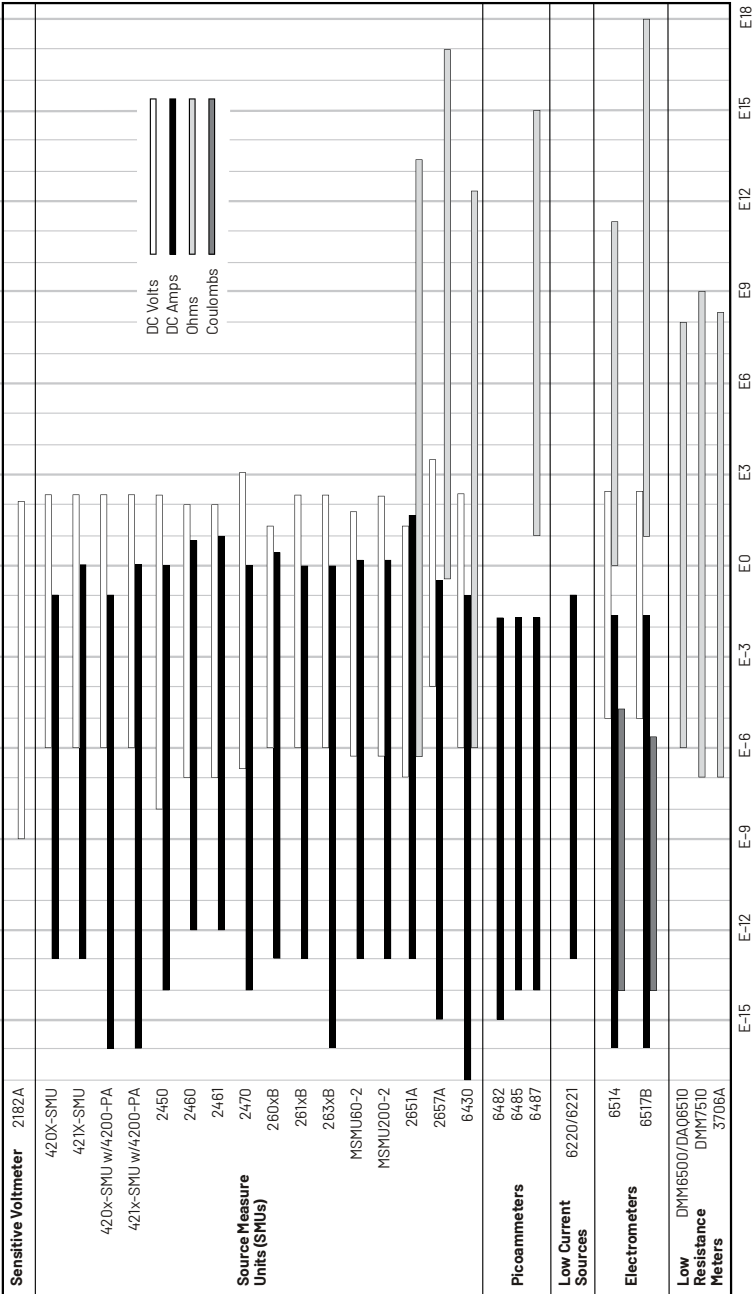


Table 5-1: Low Current / High Resistance Instrumentation

CURRENT SOURCES			PICOAMMETERS			ELECTROMETERS	
Model	6220	6221	6482	6485	6487	6514	6517B
Digits			6½	5½	5½	5½	6½
CURRENT							
Low Resolution	100 fA DC	100 fA DC 4 pAp-p	1 fA	10 fA	10 fA	100 aA	100 aA
Full Scale	100 mA DC	100 mA DC 210 mAp-p	20 mA	20 mA	20 mA	20 mA	20 mA
RESISTANCE							
Low Resolution					10 Ω	10 mΩ	100 Ω
Full Scale					1 PΩ	200 GΩ	1 EΩ¹
CHARGE							
Low Resolution						10 fC	1 fC
Full Scale						20 μC	2 μC
Connections	3 Slot Triax	3 Slot Triax	3 Slot Triax	BNC	3 Slot Triax	3 Slot Triax	3 Slot Triax
Additional Features	10¹⁴ Ω output impedance Delta Mode for low impedance with 2182A GPIB, RS-232, Ethernet AC and Arbitrary Waveform Modes	10¹⁴ Ω output impedance Delta Mode for low impedance with 2182A GPIB, RS-232, Ethernet AC and Arbitrary Waveform Modes	2 channels Dual ±30 V bias sources GPIB, RS-232	GPIB, RS-232	500 V built in source Alternating voltage method for high R sweeps GPIB, RS-232	GPIB, RS-232	±1 kV built in source Temperature and humidity Alternating polarity method for high R GPIB, RS-232

1. Using Manual mode ohms only

Table 5--2: Source Measure Instrumentation

SUB-FEMTOAMP SMU		PARAMETER ANALYZER		GRAPHICAL SERIES SMUS			
Model	6430	4200A		2450	2460	2461	2470
Digits	6½	6½		6½	6½	6½	6½
Power	2 W	2 W (420x-SMU) 20 W (421x-SMU)		20 W	100 W	1000 W	20 W
CURRENT							
Low Resolution	10 aA	1 aA with preamp		10 fA	1 pA	1 pA	10 fA
Full Scale	100 mA	1 A (421x-SMU)		1 A	7 A	7 A (10 A Pulse)	1 A
VOLTAGE							
Low Resolution	1 µV	1 µV		100 nV	100 nV	100 nV	200 nV
Full Scale	200 V	200 V		200 V	100 V	100 V	1100 V
RESISTANCE							
Low Resolution	1 µΩ	N/A		1 µΩ	1 µΩ	1 µΩ	1 µΩ
Full Scale	> 20 TΩ			> 200 MΩ	> 20 MΩ	> 200 MΩ	> 200 MΩ
Connections	3 Slot Triax	3 Slot Triax with Preamp	Front: Banana Rear: 3 Slot Triax	Front: Banana Rear: Screw Terminal	Front: Banana Rear: Screw Terminal	Front: Banana Rear: 3 Slot Triax	
Additional Features	Remote Preamp >10 ¹⁶ Ω input resistance GPIB, RS-232	Remote Preamp High and Low power versions Configurable with 4200A-SCS Parameter Analyzer	5 inch capacitive touchscreen TSP-enabled USB ethernet, GPIB	5 inch capacitive touchscreen TSP-enabled USB ethernet, GPIB	5 inch capacitive touchscreen TSP-enabled USB ethernet, GPIB Extended Operating Area Pulse 1 MHz Digitizer	5 inch capacitive touchscreen TSP-enabled USB ethernet, GPIB USB, ethernet, GPIB	

Table 5-2: Source Measure Instrumentation (continued)

SYSTEM SMUS			HIGH POWER SYSTEM SMUS			MODULAR SMUS	
Model	260XB	261xB	263XB	2651A	2657A	MSMU60-2	MSMU200-2
Digits	6 1/2	6 1/2	6 1/2	6 1/2	6 1/2	6 1/2	6 1/2
Power	200 W	200 W	200 W	200 W DC (2000 W Pulsed)	180 W	30 W	30 W

CURRENT			
Low Resolution	100 fA	100 fA	100 fA
Full Scale	3 A	1.5 A	1.5 A

VOLTAGE			
Low Resolution	100 nV	100 nV	100 nV
Full Scale	40 V	200 V	3000 V

RESISTANCE			
Low Resolution	0.5 µΩ	0.5 µΩ	0.4 µΩ
Full Scale	40 TΩ	10 PΩ	100 PΩ

Connections	Screw Terminal	Screw Terminal	3 Slot Triax	Screw Terminal	HV Triax	Screw Terminal	Screw Terminal
Additional Features	1 or 2 channel models TSP-enabled USB, Ethernet, GPIB, RS-232	1 or 2 channel models TSP-enabled USB, Ethernet, GPIB, RS-232	1 or 2 channel models TSP-enabled USB, Ethernet, GPIB, RS-232	Connect in series or parallel 1 MHz Digitizer TSP-enabled USB, Ethernet, GPIB, RS-232	1 MHz Digitizer TSP-enabled USB, Ethernet, GPIB, RS-232	2 Channels 1 MHz Digitizer TSP-enabled Compatible with MP51031U Mainframe	2 Channels 1 MHz Digitizer TSP-enabled Compatible with MP51031U Mainframe

1. 2634B: 1fA

Table 5-3: Low Voltage and Low Resistance Measurement Instruments

NANOVOLTMETER			DIGITAL MULTIMETERS		DATA ACQUISITION	
Model	2182A	DMM6000	DM7510	DA06510	3706A	
Digits	6½	6½	7½	6½	7½	
VOLTAGE						
Low Resolution	1 nV	100 nV	10 nV	100 nV	10 nV	
Full Scale	100 V	1000 V	1000 V	1000 V	300 V	
RESISTANCE						
Low Resolution	10 nΩ¹	1 μΩ	100 nΩ	1 μΩ	100 nΩ	
Full Scale	100 MΩ¹	100 MΩ	1 GΩ	100 MΩ	100 MΩ	
TEMPERATURE						
Thermocouples	J, K, N, T, E, R, S, B	J, K, N, T, E, R, S, B	J, K, N, T, E, R, S, B	J, K, N, T, E, R, S, B	J, K, N, T, E, R, S, B	
Thermistors		2.2 kΩ, 5 kΩ, 10 kΩ PT100, D100, F100, PT385, PT3916 or user configurable	2.2 kΩ, 5 kΩ, 10 kΩ PT100, D100, F100, PT385, PT3916 or user configurable	2.2 kΩ, 5 kΩ, 10 kΩ PT100, D100, F100, PT385, PT3916 or user configurable	2.2 kΩ, 5 kΩ, 10 kΩ PT100, D100, F100, PT385, PT3916 or user configurable	
RTDs						
Connections	Low thermoelectric LEMO with copper pins	Front/Rear: Banana	Front/Rear: Banana	Front: Banana Rear: Switch card backplane	Switch card backplane	
Additional Features	Secondary channel for temperature or ratio measurements Delta Mode for low R with 622X GPIOB, RS-232	5 inch capacitive touchscreen Optional scanner cards 1MHz digitizer TSP-enabled USB, Ethernet, optional GPIOB, RS-232 or TSP-Link	5 inch capacitive touchscreen 1MHz digitizer TSP-enabled USB, Ethernet, GPIOB, RS-232	2 switch card slots 5 inch capacitive touchscreen 1MHz digitizer TSP-enabled USB, Ethernet, optional GPIOB, RS-232 or TSP- Link	6 switch card slots Production rack sized mainframe Dry Circuit Ohms Mode TSP-enabled USB, Ethernet, GPIOB	

1. Requires external current source.

APPENDIX A

Cable and Connector Assembly

Proper cable and connector assembly is an important factor in maintaining the integrity of low level measurements. Generally, three types of cable are used with low level instruments: coaxial, triaxial, and shielded twisted pair. In addition, the cable is often of low noise or low thermoelectric EMF design. To prepare a low noise coaxial or triaxial cable for either direct connection to a source or for connector assembly, follow the steps illustrated in **Figure A-1**:

1. Gently cut through outer insulation without cutting the shield.
2. Using a sharp point, unravel the shield braid.
3. Twist the braid and thoroughly clean off all traces of graphite with methanol. For triax cable, repeat the previous steps with the inner shield.
4. Cut center conductor to proper length, strip insulation off center conductor, and "tin" leads.
5. For insertion into connectors, the braid is cut back in accordance with the assembly instructions for the particular connector type.

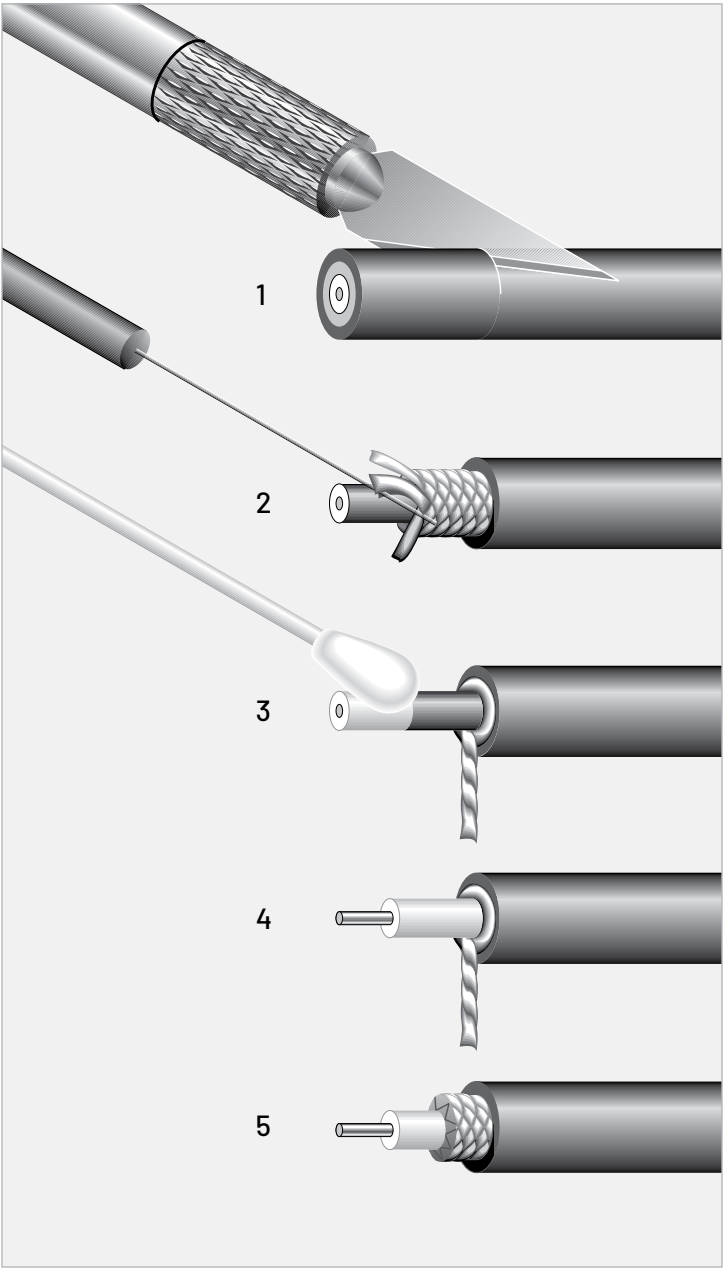
Connectors should have high resistance insulation. Teflon[®] is usually preferred because of its resistance to surface contaminants. Coaxial or triaxial connectors are available with Teflon insulation.

Note that the BNC and two-slot triaxial connectors are very similar; take care to ensure the connector is used only with a properly mating variety. If BNC and two-slot triax connectors are mated, the result will be permanent damage to the plug and receptacle. Using three-slot triaxial connectors where appropriate will eliminate this problem.

Although some low level instruments use coaxial connections, most use triaxial connectors. Triaxial cable provides the grounded shielding necessary for high common-mode measurements; generally, the outer shield is connected to power-line ground, while the inner shield is connected to signal LO. Also, triaxial connections make it easy to use guarding. In this configuration, the inner shield is connected to guard potential, and the outer shield is connected to ground. Safety hazards can exist when using this scheme in situations where exposed metal is at guard potential and the guard voltage is above 30 Vrms.

As with any insulated device, connector insulation must be kept clean to avoid reducing its leakage resistance. Avoid touching insulating material. If the connector becomes contaminated, it can be cleaned with methanol or distilled water. When cleaning, use only the purest cleaning agents and thoroughly flush contaminants away from the affected area. After all contaminants are removed, allow the connector to dry for several hours in a low humidity environment before use.

FIGURE A-1: Cable Preparation



APPENDIX B

Glossary

ABSOLUTE ACCURACY. A measure of the closeness of agreement of an instrument reading compared to that of a primary standard having absolute traceability to a standard sanctioned by a recognized standards organization. Accuracy is often separated into gain and offset terms. *See also* RELATIVE ACCURACY.

A/D (ANALOG-TO-DIGITAL) CONVERTER. A circuit used to convert an analog input signal into digital information. All digital meters use an A/D converter to convert the input signal into digital information.

ANALOG OUTPUT. An output that is directly proportional to the input signal.

AUTO-RANGING. The ability of an instrument to automatically switch among ranges to determine the range offering the highest resolution. The ranges are usually in decade steps.

AUTO-RANGING TIME. For instruments with auto-ranging capability, the time interval between application of a step input signal and its display, including the time for determining and changing to the correct range.

BANDWIDTH. The range of frequencies that can be conducted or amplified within certain limits. Bandwidth is usually specified by the -3dB (half-power) points.

BIAS VOLTAGE. A voltage applied to a circuit or device to establish a reference level or operating point of the device during testing.

CAPACITANCE. In a capacitor or system of conductors and dielectrics, that property which permits the storage of electrically separated charges when potential differences exist between the conductors. Capacitance is related to the charge and voltage as follows: $C = Q/V$, where C is the capacitance in farads, Q is the charge in coulombs, and V is the voltage in volts.

CARBON NANOTUBE. A tube-shaped nanodevice formed from a sheet of single-layer carbon atoms that has novel electrical and tensile properties. These fibers may exhibit electrical conductivity as high as copper, thermal conductivity as high as diamond, strength 100 times greater than steel at one-sixth of steel's weight, and high strain to failure. They can be superconducting, insulating, semiconducting, or conducting (metallic). Non-carbon nanotubes, often called nanowires, are often created from boron nitride or silicon.

CHANNEL (SWITCHING). One of several signal paths on a switching card. For scanner or multiplexer cards, the channel is used as a switched input in measuring circuits, or as a switched output in sourcing circuits. For switch cards, each channel's signals paths are independent of other channels. For matrix cards, a channel is established by the actuation of a relay at a row and column crosspoint.

COAXIAL CABLE. A cable formed from two or more coaxial cylindrical conductors insulated from each other. The outermost conductor is often earth grounded.

COMMON-MODE REJECTION RATIO (CMRR). The ability of an instrument to reject interference from a common voltage at its input terminals with respect to ground. Usually expressed in decibels at a given frequency.

COMMON-MODE CURRENT. The current that flows between the input low terminal and chassis ground of an instrument.

- COMMON-MODE VOLTAGE.** A voltage between input low and earth ground of an instrument.
- CONTACT RESISTANCE.** The resistance in ohms between the contacts of a relay or connector when the contacts are closed or in contact.
- CONTAMINATION.** Generally used to describe the unwanted material that adversely affects the physical, chemical, or electrical properties of a semiconductor or insulator.
- D/A (DIGITAL-TO-ANALOG) CONVERTER.** A circuit used to convert digital information into an analog signal. D/A converters are used in many instruments to provide an isolated analog output.
- DIELECTRIC ABSORPTION.** The effect of residual charge storage after a previously charged capacitor has been discharged momentarily.
- DIGITAL MULTIMETER (DMM).** An electronic instrument that measures voltage, current, resistance, or other electrical parameters by converting the analog signal to digital information and display. The typical five-function DMM measures DC volts, DC amps, AC volts, AC amps, and resistance.
- DRIFT.** A gradual change of a reading with no change in input signal or operating conditions.
- DRY CIRCUIT TESTING.** The process of measuring a device while keeping the voltage across the device below a certain level (e.g., <20 mV) in order to prevent disturbance of oxidation or other degradation of the device being measured.
- ELECTROCHEMICAL EFFECT.** A phenomenon whereby currents are generated by galvanic battery action caused by contamination and humidity.
- ELECTROMETER.** A highly refined DC multimeter. In comparison with a digital multimeter, an electrometer is characterized by higher input resistance and greater current sensitivity. It can also have functions not generally available on DMMs (e.g., measuring electric charge, sourcing voltage).
- EMF.** Electromotive force or voltage. EMF is generally used in context of a voltage difference caused by electromagnetic, electrochemical, or thermal effects.
- ELECTROSTATIC COUPLING.** A phenomenon whereby a current is generated by a varying or moving voltage source near a conductor.
- ERROR.** The deviation (difference or ratio) of a measurement from its true value. True values are by their nature indeterminate. See *also* RANDOM ERROR and SYSTEMATIC ERROR.
- FALL TIME.** The time required for a signal to change from a large percentage (usually 90%) to a small percentage (usually 10%) of its peak-to-peak value. See *also* RISE TIME.
- FARADAY CUP.** A Faraday cup (sometimes called a Faraday cage) is an enclosure made of sheet metal or mesh. It consists of two electrodes, one inside the other, separated by an insulator. While the inner electrode is connected to the electrometer, the outer electrode is connected to ground. When a charged object is placed inside the inner electrode, all the charge will flow into the measurement instrument. The electric field inside a closed, empty conductor is zero, so the cup shields the object

placed inside it from any atmospheric or stray electric fields. This allows measuring the charge on the object accurately.

FEEDBACK PICOAMMETER. A sensitive ammeter that uses an operational amplifier feedback configuration to convert an input current into voltage for measurement.

FLOATING. The condition where a common-mode voltage exists between an earth ground and the instrument or circuit of interest. (Circuit low is not tied to earth potential.)

FOUR-POINT PROBE. The four-point collinear probe resistivity measurement technique involves bringing four equally spaced probes in contact with the material of unknown resistance. The array is placed in the center of the material. A known current is passed through the two outside probes and the voltage is sensed at the two inside probes. The resistivity is calculated as follows:

$$\rho = \frac{\pi}{\ln 2} \times \frac{V}{I} \times t \times k$$

where: V = the measured voltage in volts, I = the source current in amps, t = the wafer thickness in centimeters, k = a correction factor based on the ratio of the probe to wafer diameter and on the ratio of wafer thickness to probe separation.

FOUR-TERMINAL RESISTANCE MEASUREMENT. A measurement where two leads are used to supply a current to the unknown, and two different leads are used to sense the voltage drop across the resistance. The four-terminal configuration provides maximum benefits when measuring low resistances.

FULLERENE. Refers to C₆₀, an approximately spherical, hollow, carbon molecule containing 60 carbon atoms arranged in interlocking hexagons and pentagons, reminiscent of the geodesic dome created by architect R. Buckminster Fuller. Sometimes called “buckminsterfullerene” or “buckyball.”

GROUND LOOP. A situation resulting when two or more instruments are connected to different points on the ground bus and to earth or power line ground. Ground loops can develop undesired offset voltages or noise.

GUARDING. A technique that reduces leakage errors and decreases response time. Guarding consists of a conductor driven by a low impedance source surrounding the lead of a high impedance signal. The guard voltage is kept at or near the potential of the signal voltage.

HALL EFFECT. The measurement of the transverse voltage across a conductor when placed in a magnetic field. With this measurement, it is possible to determine the type, concentration, and mobility of carriers in silicon.

HIGH IMPEDANCE TERMINAL. A terminal where the source resistance times the expected stray current (for example, 1 μ A) exceeds the required voltage measurement sensitivity.

INPUT BIAS CURRENT. The current that flows at the instrument input due to internal instrument circuitry and bias voltage.

INPUT IMPEDANCE. The shunt resistance and capacitance (or inductance) as measured at the input terminals, not including effects of input bias or offset currents.

INPUT OFFSET CURRENT. The difference between the two currents that must be supplied to the input measuring terminals of a differential instrument to reduce the output indication to zero (with zero input voltage and offset voltage). Sometimes informally used to refer to input bias current.

INPUT OFFSET VOLTAGE. The voltage that must be applied directly between the input measuring terminals, with bias current supplied by a resistance path, to reduce the output indication to zero.

INPUT RESISTANCE. The resistive component of input impedance.

INSULATION RESISTANCE. The ohmic resistance of insulation. Insulation resistance degrades quickly as humidity increases.

JOHNSON NOISE. The noise in a resistor caused by the thermal motion of charge carriers. It has a white noise spectrum and is determined by the temperature, bandwidth, and resistance value.

LEAKAGE CURRENT. Error current that flows (leaks) through insulation resistance when a voltage is applied. Even high resistance paths between low current conductors and nearby voltage sources can generate significant leakage currents.

LONG-TERM ACCURACY. The limit that errors will not exceed during a 90-day or longer time period. It is expressed as a percentage of reading (or sourced value) plus a number of counts over a specified temperature range.

MAXIMUM ALLOWABLE INPUT. The maximum DC plus peak AC value (voltage or current) that can be applied between the high and low input measuring terminals without damaging the instrument.

MICRO-OHMMETER. An ohmmeter that is optimized for low resistance measurements. The typical micro-ohmmeter uses the four-terminal measurement method and has special features for optimum low level measurement accuracy.

MOSFET. A metal oxide field effect transistor. A unipolar device characterized by extremely high input resistance.

NANO-. A prefix meaning one billionth (1/1,000,000,000).

NANOELECTRONICS. Electronics on a nanometer scale. Includes both molecular electronics and nanoscale devices that resemble current semiconductor devices.

NANOTECHNOLOGY. Fabrication of devices with atomic or molecular scale precision. Devices with minimum feature sizes less than 100 nanometers (nm) are considered products of nanotechnology. A nanometer [one-billionth of a meter (10^{-9} m)] is the unit of length generally most appropriate for describing the size of single molecules.

NANOVOLTMETER. A voltmeter optimized to provide nanovolt sensitivity (generally uses low thermoelectric EMF connectors, offset compensation, etc.).

NOISE. Any unwanted signal imposed on a desired signal.

NORMAL-MODE REJECTION RATIO (NMRR). The ability of an instrument to reject interference across its input terminals. Usually expressed in decibels at a specific frequency such as that of the AC power line.

NORMAL-MODE VOLTAGE. A voltage applied between the high and low input terminals of an instrument.

OFFSET CURRENT. A current generated by a circuit even though no signals are applied. Offset currents are generated by triboelectric, piezoelectric, or electrochemical effects present in the circuit.

OVERLOAD PROTECTION. A circuit that protects the instrument from excessive current or voltage at the input terminals.

PICOAMMETER. An ammeter optimized for the precise measurement of small currents. Generally, a feedback ammeter.

PIEZOELECTRIC EFFECT. A term used to describe currents generated when mechanical stress is applied to certain types of insulators.

PRECISION. Refers to the freedom of uncertainty in the measurement. It is often applied in the context of repeatability or reproducibility and should not be used in place of accuracy. *See also* UNCERTAINTY.

RANDOM ERROR. The mean of a large number of measurements influenced by random error matches the true value. *See also* SYSTEMATIC ERROR.

RANGE. A continuous band of signal values that can be measured or sourced. In bipolar instruments, range includes positive and negative values.

READING. The displayed number that represents the characteristic of the input signal.

READING RATE. The rate at which the reading number is updated. The reading rate is the reciprocal of the time between readings.

RELATIVE ACCURACY. The accuracy of a measuring instrument in reference to a secondary standard. *See also* ABSOLUTE ACCURACY.

REPEATABILITY. The closeness of agreement between successive measurements carried out under the same conditions.

REPRODUCIBILITY. The closeness of agreement between measurements of the same quantity carried out with a stated change in conditions.

RESOLUTION. The smallest portion of the input (or output) signal that can be measured (or sourced) and displayed.

RESPONSE TIME. For a measuring instrument, the time between application of a step input signal and the indication of its magnitude within a rated accuracy. For a sourcing instrument, the time between a programmed change and the availability of the value at its output terminals. Also known as Settling Time.

RISE TIME. The time required for a signal to change from a small percentage (usually 10%) to a large percentage (usually 90%) of its peak-to-peak amplitude. *See also* FALL TIME.

SENSITIVITY. The smallest quantity that can be measured and displayed.

SETTLING TIME. For a measuring instrument, the time between application of a step input signal and the indication of its magnitude within a rated accuracy. For a sourcing instrument, the time between a programmed change and the availability of the value at its output terminals. Also known as RESPONSE TIME.

SHIELDING. A metal enclosure around the circuit being measured, or a metal sleeve surrounding the wire conductors (coax or triax cable) to lessen interference, interaction, or leakage. The shield is usually grounded or connected to input LO.

SHORT-TERM ACCURACY. The limit that errors will not exceed during a short, specified time period (such as 24 hours) of continuous operation. Unless specified, no zeroing or adjustment of any kind are permitted. It is expressed as percentage of reading (or sourced value) plus a number of counts over a specified temperature range.

SHUNT AMMETER. A type of ammeter that measures current by converting the input current into a voltage by means of shunt resistance. Shunt ammeters have higher voltage burden and lower sensitivity than do feedback ammeters.

SHUNT CAPACITANCE LOADING. The effect on a measurement of the capacitance across the input terminals, such as from cables or fixtures. Shunt capacitance increases both rise time and settling time.

SOURCE IMPEDANCE. The combination of resistance and capacitive or inductive reactance the source presents to the input terminals of a measuring instrument.

SOURCE MEASURE UNIT (SMU). An electronic instrument that sources and measures DC voltage and current. Generally, SMUs have two modes of operation: source voltage and measure current, or source current and measure voltage.

SOURCEMETER. A SourceMeter SMU is very similar to the source measure unit (SMU) in many ways, including its ability to source and measure both current and voltage and to perform sweeps. In addition, a SourceMeter SMU can display the measurements directly in resistance, as well as voltage and current. It is designed for general-purpose, high speed production test applications. It can also be used as a source for moderate to low level measurements and for research applications.

SOURCE RESISTANCE. The resistive component of source impedance. See *also* THEVENIN EQUIVALENT CIRCUIT.

STANDARD CELL. An electrochemical cell used as a voltage reference in laboratories.

SUPERCONDUCTOR. A conductor that has zero resistance. Such materials usually become superconducting only at very low temperatures.

SWITCH CARD. A type of card with independent and isolated relays for switching inputs and outputs on each channel.

SWITCHING MAINFRAME. A switching instrument that connects signals among sourcing and measuring instruments and devices under test. A mainframe is also referred to as a scanner, multiplexer, matrix, or programmable switch.

SYSTEMATIC ERROR. The mean of a large number of measurements influenced by systematic error deviates from the true value. See *also* RANDOM ERROR.

TEMPERATURE COEFFICIENT. A measure of the change in reading (or sourced value) with a change in temperature. It is expressed as a percentage of reading (or sourced value), plus a number of counts per degree change in temperature.

TEMPERATURE COEFFICIENT OF RESISTANCE. The change of resistance of a material or device per degree of temperature change, usually expressed in ppm/°C.

THERMOELECTRIC EMFs. Voltages resulting from temperature differences within a measuring circuit or when conductors of dissimilar materials are joined together.

THEVENIN EQUIVALENT CIRCUIT. A circuit used to simplify analysis of complex, two-terminal linear networks. The Thevenin equivalent voltage is the open-circuit voltage and the Thevenin equivalent resistance equals the open-circuit voltage divided by the short-circuit current.

TRANSFER ACCURACY. A comparison of two nearly equal measurements over a limited temperature range and time period. It is expressed in ppm. See also RELATIVE ACCURACY, SHORT-TERM ACCURACY.

TRIBOELECTRIC EFFECT. A phenomenon whereby currents are generated by charges created by friction between a conductor and an insulator.

TRIGGER. An external stimulus that initiates one or more instrument functions. Trigger stimuli include: an input signal, the front panel, an external trigger pulse, and IEEE-488 bus X, talk, and GET triggers.

TWO-TERMINAL RESISTANCE MEASUREMENT. A measurement where the source current and sense voltage are applied through the same set of test leads.

UNCERTAINTY. An estimate of the possible error in a measurement; in other words, the estimated possible deviation from its actual value.

VAN DER PAUW MEASUREMENT. A measurement technique used to measure the resistivity of arbitrarily shaped samples.

VOLTAGE BURDEN. The voltage drop across the input terminals of an ammeter.

VOLTAGE COEFFICIENT. The change in resistance value with applied voltage. Usually expressed in percent/V or in ppm/V.

WARM-UP TIME. The time required after power is applied to an instrument to achieve rated accuracy at reference conditions.

ZERO OFFSET. The reading that occurs when the input terminals of a measuring instrument are shorted (voltmeter) or open-circuited (ammeter).

APPENDIX C

**Safety
Considerations**

Test System Safety

Many electrical test systems or instruments are capable of measuring or sourcing hazardous voltage and power levels. It is also possible, under single fault conditions (e.g., a programming error or an instrument failure), to output hazardous levels even when the system indicates no hazard is present.

These high voltage and power levels make it essential to protect operators from any of these hazards at all times.

Protection methods include:

- Design test fixtures to prevent operator contact with any hazardous circuit.
- Make sure the device under test is fully enclosed to protect the operator from any flying debris.
- Double insulate all electrical connections that an operator could touch. Double insulation ensures the operator is still protected, even if one insulation layer fails.
- Use high-reliability, fail-safe interlock switches to disconnect power sources when a test fixture cover is opened.
- Where possible, use automated handlers so operators do not require access to the inside of the test fixture or have a need to open guards.
- Provide proper training to all users of the system so they understand all potential hazards and know how to protect themselves from injury.

WARNING: During power-up, the output states of data acquisition products are uncontrolled until hardware and software initialization has been completed. Users must make sure their designs can tolerate this or provide suitable interlocks to prevent dangerous voltages or actions from reaching users.

General Safety Considerations

The following safety precautions should be observed before using any Keithley product and any associated instrumentation. Although some instruments and accessories would normally be used with nonhazardous voltages, there are situations where hazardous conditions may be present.

Keithley products are intended for use by qualified personnel who recognize shock hazards and are familiar with the safety precautions required to avoid possible injury. Read and follow all installation, operation, and maintenance information carefully before using the product. Refer to the user documentation for complete product specifications.

If the product is used in a manner not specified, the protection provided by the product warranty may be impaired.

The types of product users are:

Responsible body is the individual or group responsible for the use and maintenance of equipment, for ensuring that the equipment is operated within its specifications and operating limits, and for ensuring that operators are adequately trained.

Operators use the product for its intended function. They must be trained in electrical safety procedures and proper use of the instrument. They must be protected from electric shock and contact with hazardous live circuits.

Maintenance personnel perform routine procedures on the product to keep it operating properly, for example, setting the line voltage or replacing consumable materials. Maintenance procedures are described in the user documentation. The procedures explicitly state if the operator may perform them. Otherwise, they should be performed only by service personnel.

Service personnel are trained to work on live circuits, perform safe installations, and repair products. Only properly trained service personnel may perform installation and service procedures.

Keithley Instruments products are designed for use with electrical signals that are measurement, control, and data I/O connections, with low transient overvoltages, and must not be directly connected to mains voltage or to voltage sources with high transient overvoltages. Measurement Category II (as referenced in IEC 60664) connections require protection for high transient overvoltages often associated with local AC mains connections. Certain Keithley measuring instruments may be connected to mains. These instruments will be marked as category II or higher.

Unless explicitly allowed in the specifications, operating manual, and instrument labels, do not connect any instrument to mains.

Exercise extreme caution when a shock hazard is present. Lethal voltage may be present on cable connector jacks or test fixtures. The American National Standards Institute (ANSI) states that a shock hazard exists when voltage levels greater than 30 V RMS, 42.4 V peak, or 60 VDC are present. A good safety practice is to expect that hazardous voltage is present in any unknown circuit before measuring.

Operators of these products must be protected from electric shock at all times. The responsible body must ensure that operators are prevented access and/or insulated from every connection point. In some cases, connections must be exposed to potential human contact. Product operators in these circumstances must be trained to protect themselves from the risk of electric shock. If the circuit is capable of operating at or above 1000 V, no conductive part of the circuit may be exposed.

Do not connect switching cards directly to unlimited power circuits. They are intended to be used with impedance-limited sources. NEVER connect switching cards directly to AC mains. When connecting sources to switching cards, install protective devices to limit fault current and voltage to the card.

Before operating an instrument, ensure that the line cord is connected to a properly grounded power receptacle. Inspect the connecting cables, test leads, and jumpers for possible wear, cracks, or breaks before each use.

When installing equipment where access to the main power cord is restricted, such as rack mounting, a separate main input power disconnect device must be provided in close proximity to the equipment and within easy reach of the operator.

For maximum safety, do not touch the product, test cables, or any other instruments while power is applied to the circuit under test. ALWAYS remove power from the entire test system and discharge any capacitors before: connecting or disconnecting cables or jumpers, installing or removing switching cards, or making internal changes, such as installing or removing jumpers.

Do not touch any object that could provide a current path to the common side of the circuit under test or power line (earth) ground. Always make measurements with dry hands while standing on a dry, insulated surface capable of withstanding the voltage being measured.

For safety, instruments and accessories must be used in accordance with the operating instructions. If the instruments or accessories are used in a manner not specified in the operating instructions, the protection provided by the equipment may be impaired.

Do not exceed the maximum signal levels of the instruments and accessories, as defined in the specifications and operating information, and as shown on the instrument or test fixture panels, or switching card.

When fuses are used in a product, replace with the same type and rating for continued protection against fire hazard.

Chassis connections must only be used as shield connections for measuring circuits, NOT as protective earth (safety ground) connections.

If you are using a test fixture, keep the lid closed while power is applied to the device under test. Safe operation requires the use of a lid interlock.

If a  screw is present, connect it to protective earth (safety ground) using the wire recommended in the user documentation.

The  symbol on an instrument means caution, risk of danger. The user must refer to the operating instructions located in the user documentation in all cases where the symbol is marked on the instrument.

The  symbol on an instrument means caution, risk of electric shock. Use standard safety precautions to avoid personal contact with these voltages.

The  symbol on an instrument shows that the surface may be hot. Avoid personal contact to prevent burns.

The  symbol indicates a connection terminal to the equipment frame.

If this  symbol is on a product, it indicates that mercury is present in the display lamp. Please note that the lamp must be properly disposed of according to federal, state, and local laws.

The WARNING heading in the user documentation explains dangers that might result in personal injury or death. Always read the associated information very carefully before performing the indicated procedure.

The CAUTION heading in the user documentation explains hazards that could damage the instrument. Such damage may invalidate the warranty.

Instrumentation and accessories shall not be connected to humans.

Before performing any maintenance, disconnect the line cord and all test cables.

To maintain protection from electric shock and fire, replacement components in mains circuits — including the power transformer, test leads, and input jacks — must be purchased from Keithley Instruments. Standard fuses with applicable national safety approvals may be used if the rating and type are the same. Other components that are not safety-related may be purchased from other suppliers as long as they are equivalent to the original component (note that selected parts should be purchased only through Keithley Instruments to maintain accuracy and functionality of the product). If you are unsure about the applicability of a replacement component, call a Keithley Instruments office for information.

To clean an instrument, use a damp cloth or mild, water-based cleaner. Clean the exterior of the instrument only. Do not apply cleaner directly to the instrument or allow liquids to enter or spill on the instrument. Products that consist of a circuit board with no case or chassis (e.g., a data acquisition board for installation into a computer) should never require cleaning if handled according to instructions. If the board becomes contaminated and operation is affected, the board should be returned to the factory for proper cleaning/servicing.

Safety precaution version April 2025.

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440-248-0400 | www.tek.com