

September 5, 2012

Statement of Volatility for the Keithley Model KPCI-PIO32-IOA and KPCI-PDIS08A

The Keithley KPCI-PIO* and PDIS* contains memory devices to hold boot-up code that is executed by an internal PCI bus controller. The following sections describe how the data is stored in the memory devices.

Description of memory devices and their use:

- 1 of 16kbit serial EEPROM: Stores firmware code for the PCI controller.
- The PCI controller (AMCC S5935) contains volatile memory for the internal registers.

Procedure for clearing memory content:

- None needed. The EEPROM is not accessible to users of the product. The firmware code is loaded at the factory only. The registers in the PCI controller are cleared at power off.

If you have any further questions or comments, please feel free to contact my office at anytime.

Regards,



William Pelster
Director of Quality