

May 12, 2009

## Clearing Data in Non-Volatile Memory in the Keithley Model 175 and 197 products

The Keithley Model 175 and 197 share a similar architecture and contains memory devices to hold firmware code that is executed by internal microprocessors to operate the product. The following sections describe how the data stored in the memory devices may be cleared or destroyed.

**Warning: Some of the procedures in this document may expose hazardous voltage and the risk of electric shock or death. Only trained, qualified, technical persons experienced with the risks and precautions of servicing electrical instruments should perform these procedures.**

### Description of memory devices and their use:

Microprocessor – (146805) contains 112 x 8 bits of RAM for internal use.

RAM – (6514) contains 1k x 4 bits of volatile memory.

ROM – (2764) contains 8k x 8 bits of non-volatile memory that holds the operating firmware.

### Procedure for clearing memory content:

Microprocessor – Turn off unit for one (1) minute.

RAM – Turn off unit for one (1) minute.

ROM – Remove the integrated circuit and destroy it.

If you have any further questions or comments, please feel free to contact my office at anytime.

Regards,



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Director of Quality