



Margin test solution simplifies, speeds PCIe Gen 3, Gen 4 test

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Electronics

Test & Measurement

TEKTRONIX TMT4 Margin Tester revolutionizes PCI Express testing, transforming time to market, cost, and accessibility. Tester breaks conventions of PCIe testing, delivering fast test times. Plug-and-play set up and easy-to-use interface combine to deliver in minutes results that, up until now, required hours or even days of set up and testing, often stretching costs to seven figures. Solution breaks new ground as a specialized testing tool for design and validation of PCIe Gen 3 and Gen 4 motherboards, add-in cards, and system designs. While PCIe testing normally requires complex test systems and engineers with deep expertise and knowledge, product enables engineers at all levels of experience to evaluate the health of transmitter (Tx) and receiver (Rx) links faster than ever, greatly reducing time to market and cost of ownership. Platform supports the majority of common PCIe form factors, including CEM, M.2, U.2, and U.3, with testing capabilities of up to 16 lanes across PCIe presets 0-9, using a single standard connector.

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