

PART NUMBER

SPEC.-TRG1

### 500-TRG1 SPECIFICATIONS:

- Channels:** 1, differential input.
- Input Source:** External trigger input, Global Amplifier output of AMM1A or AMM2, or Software Strobe.
- Input Ranges:** 0 to +1V, 0 to -1V, 0 to +10V or 0 to -10V.
- Resolution:** 8-bits.
- Level Accuracy:** ±2% + 1LSB.
- Input Impedance:** 10 MΩ (External Input).
- Input Protection:** ±30V max (powered), ±15V max (unpowered).
- Input Coupling:** AC or DC
- Trigger Slope:** Rising or Falling Edge.
- Input Filter:** Software selectable single pole low pass filter with the following characteristics:

-3db Frequency	Settling time to 1/2 LSB
1 MHz	1 μS
300 kHz	3.3 μS
100 kHz	10 μS
30 kHz	33 μS
10 kHz	100 μS
3 kHz	333 μS
1 kHz	1 mS
300 Hz	3.3 mS


**Output:** 1.) Low-true trigger signal to motherboard connector for triggering AMM1A or AMM2 in adjacent slot, and/or; 2.) interrupt request (IRQ) to host computer; 3.) status register.

IRQ is open collector low-true pulse 500 nS typical. A/D trigger signal is software selectable to be either a 500 nS low-true pulse, or a low-true level.

**Signal Connection:** 3-pin quick disconnect screw terminal block for external input. Cable to AMM card for global amp connection supplied.

**Note:** The TRG1 requires a +10V reference signal to be present on the backplane, which is typically provided by an AMM1A or AMM2 module used in Slot 1. If one TRG1 is used in a system, it must be placed in Slot 2. If two TRG1 modules are used in a system, they must be used in Slots 2 and 3.

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